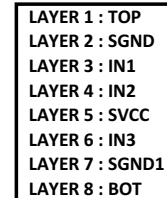
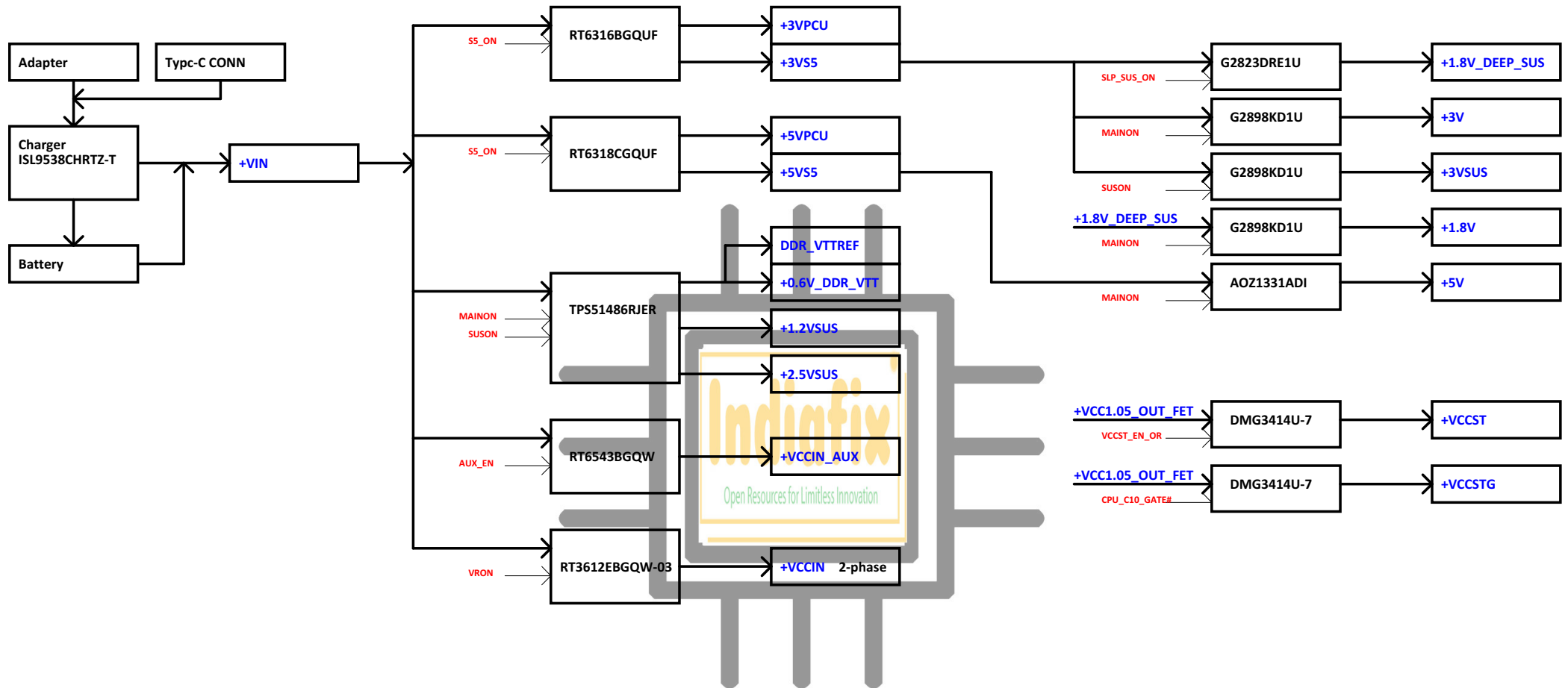
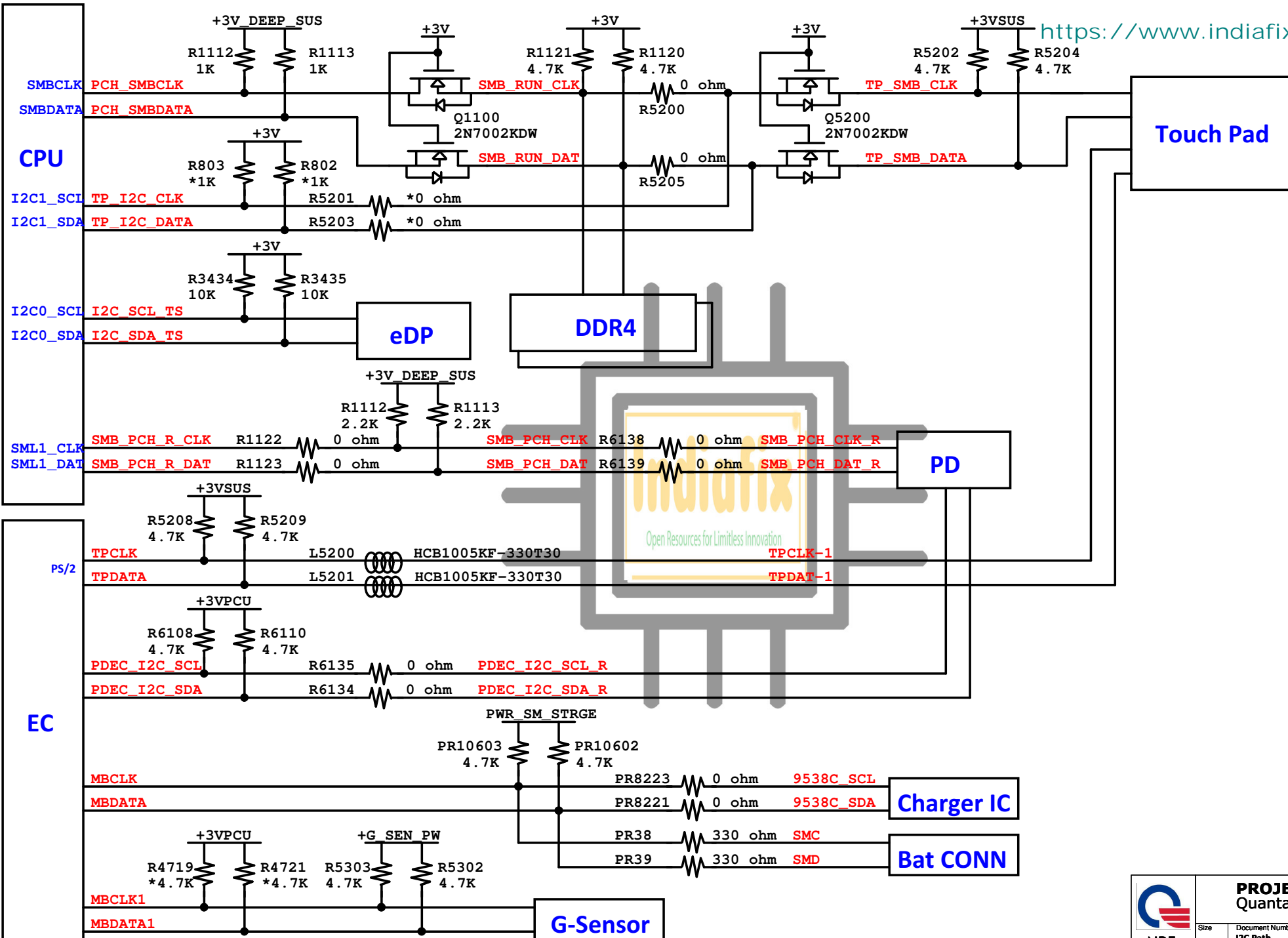


01

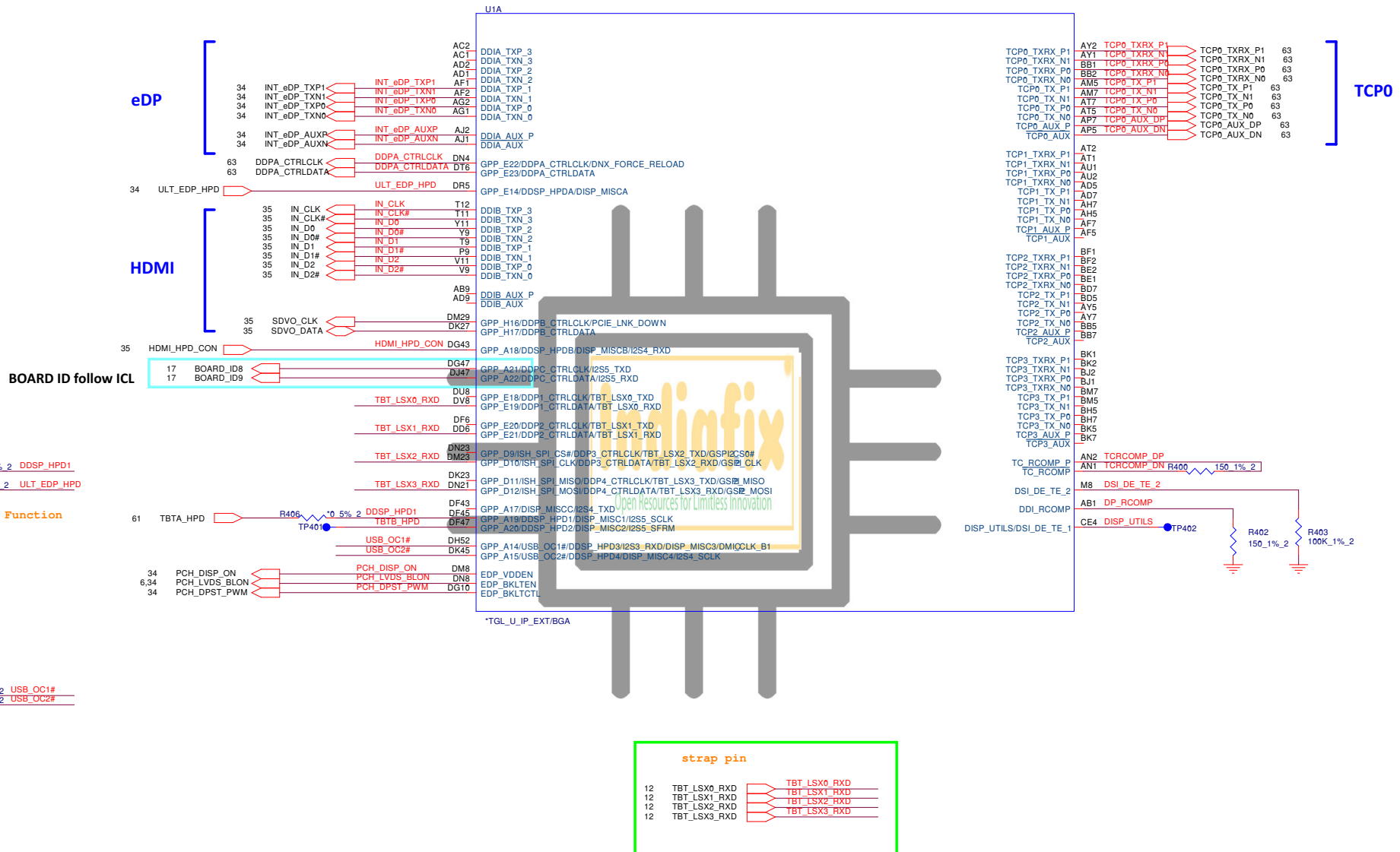






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Quanta Computer Inc.

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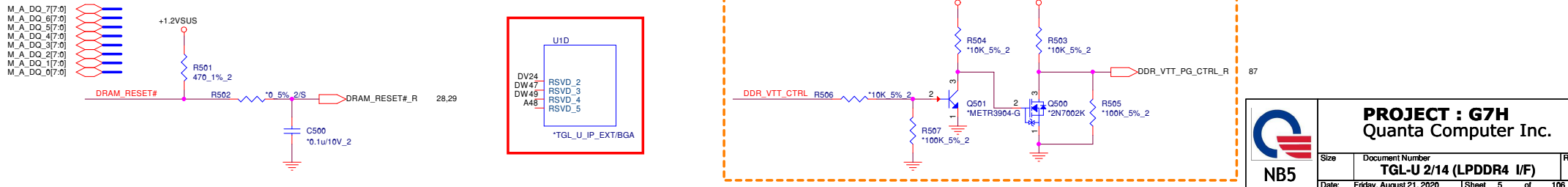
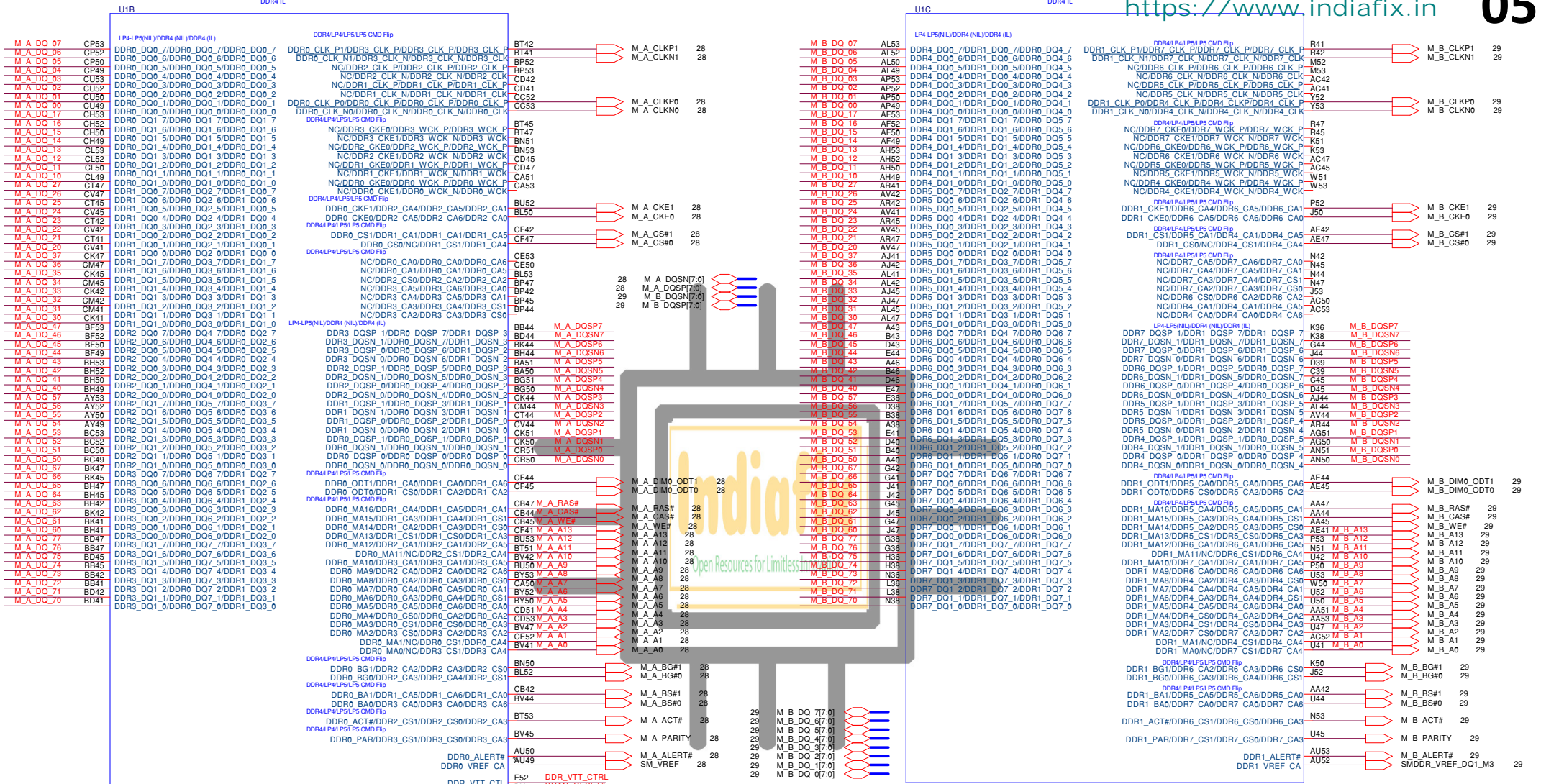
TIGER LAKE Processor DDR4/4X

DDR CHANNEL A

DDR CHANNEL B

<https://www.indiafix.in>

05

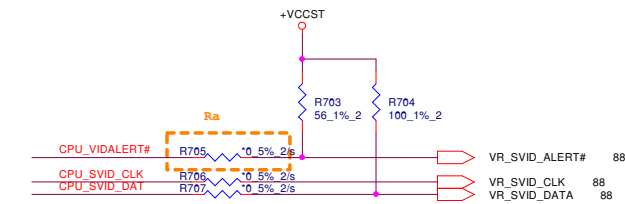
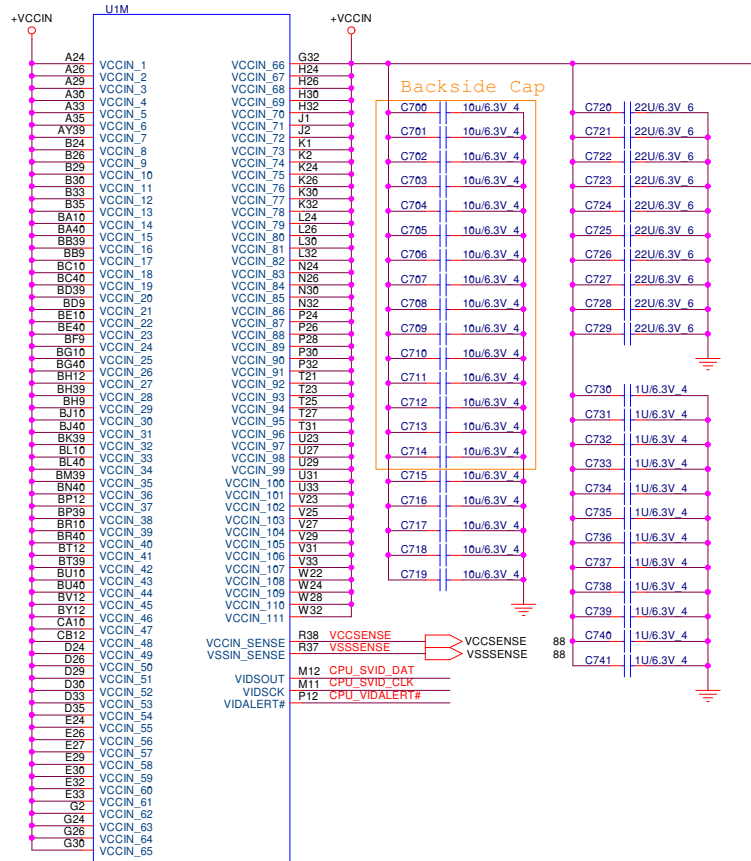


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Quanta Computer Inc.

Size	Document Number	Rev
	TGL-U 2/14 (LPDDR4 I/F)	DB
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<https://www.indiafix.in>

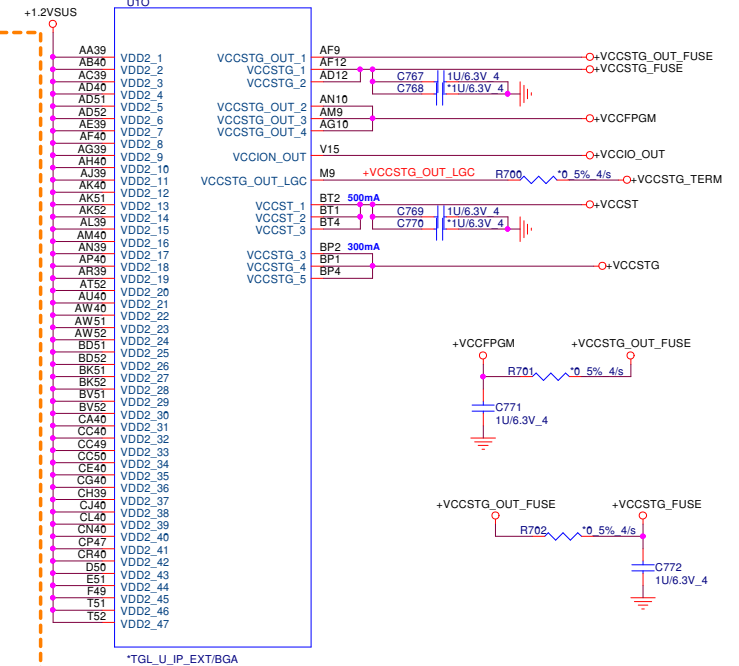
+VCCIN: 65A



ALERT# needs to Place between CLK and DAT
Ra Neen check value

+1.2VSUS POWER CAP MOVE TO PAGE 9


+1.2VSUS: 1.5A

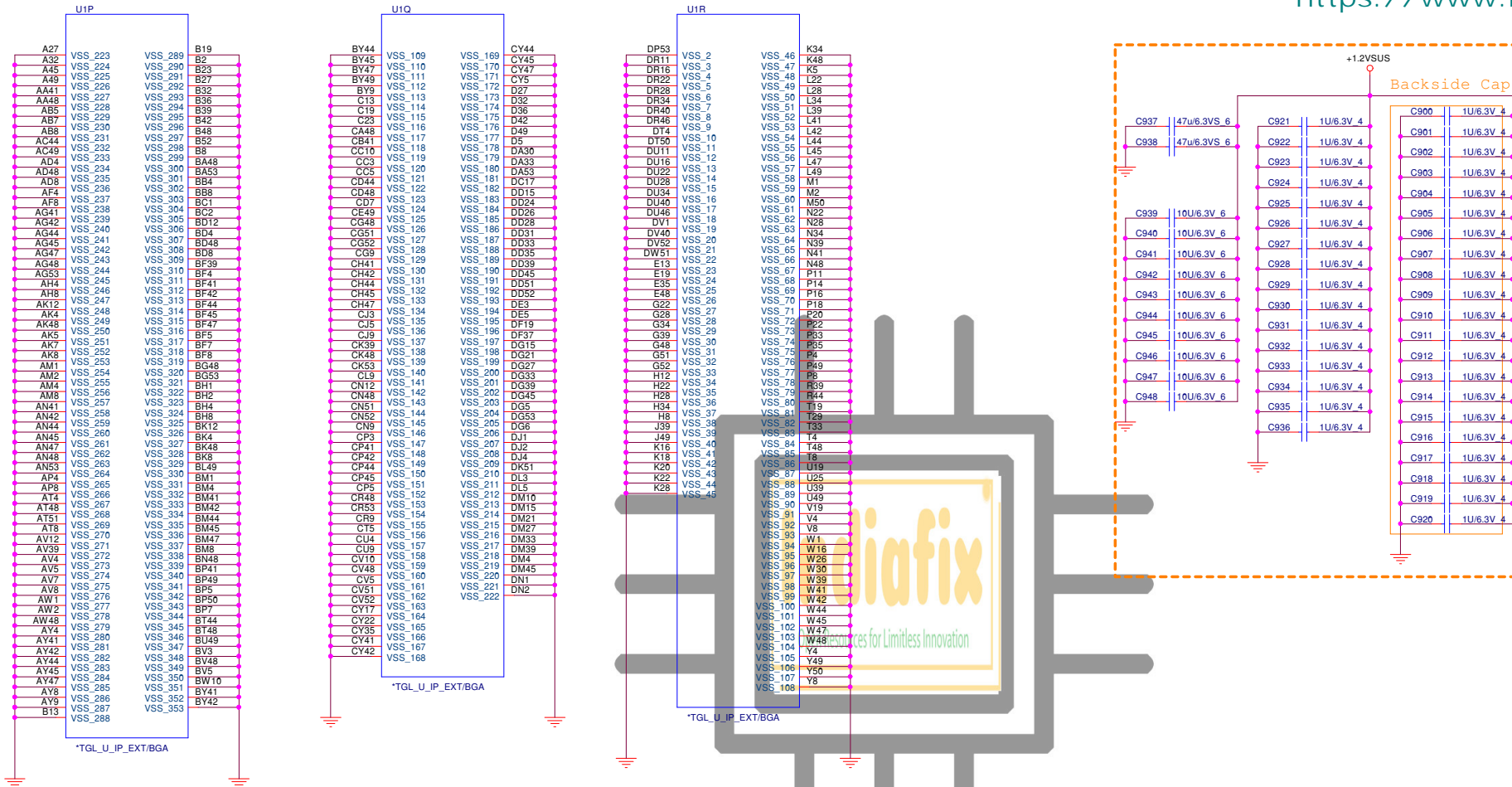


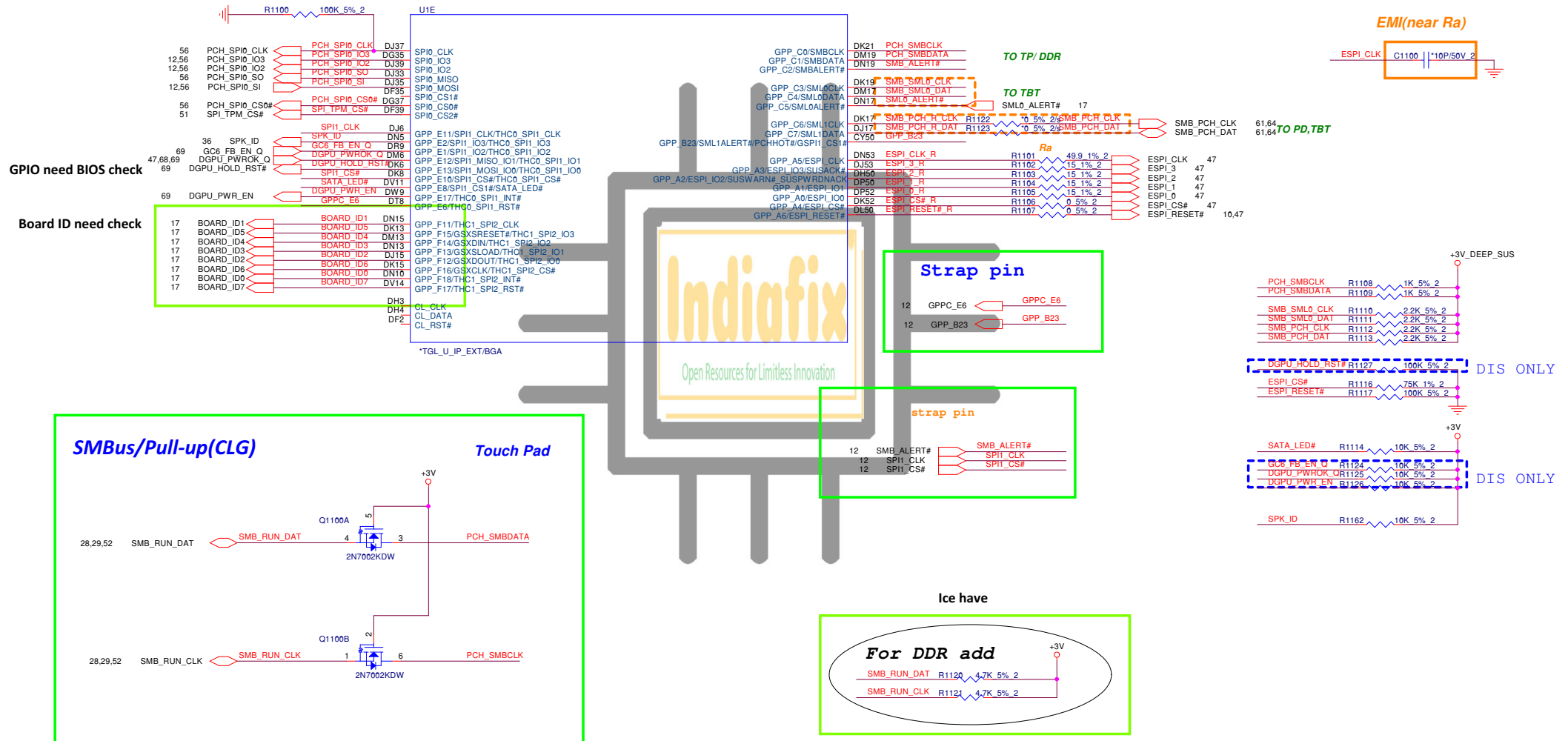
+VCCIN 88,89

+VCCIO_OUT 10
+VCCST 6,15,18,88,95
+VCCSTG 6,18,47,95

NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number TGL-U 4/14 (VCCIN/VDDQ)	Rev 1A
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 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number TGL-U 5/14 (I2C/ISH)	Rev 1A
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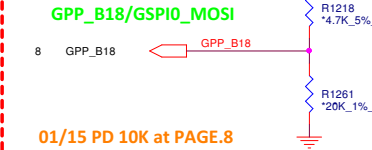


TOP SWAP OVERRIDE

High: TOP SWAP ENABLED
Low: DISABLED
WEAK INTERNAL PD 20K

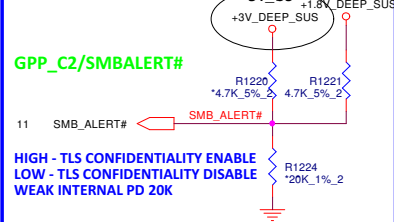
**NO REBOOT**

High: NO REBOOT
Low: REBOOT ENABLED
WEAK INTERNAL PD 20K

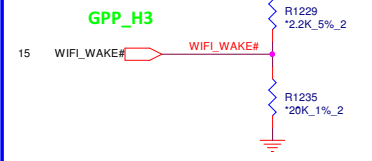
**TLS CONFIDENTIALITY**

GPP_C2/SMBALERT#

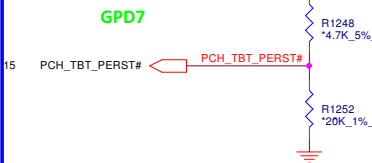
High - TLS CONFIDENTIALITY ENABLE
Low - TLS CONFIDENTIALITY DISABLE
WEAK INTERNAL PD 20K

**MAF/SAF STRAP**

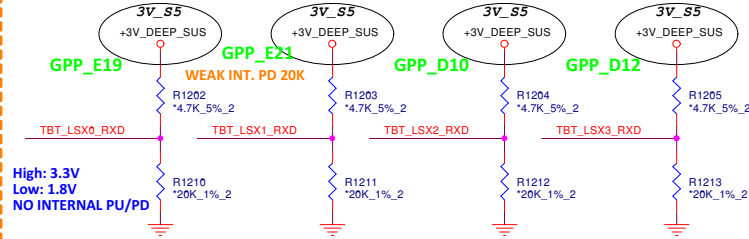
High: SAF ENABLE
Low: MAF ENABLE
WEAK INTERNAL PD 20K

**(RSVD) XTAL INPUT MODE**

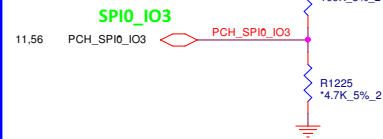
High: XTAL INPUT IS SINGLE ENDED
Low: XTAL IS ATTACHED
WEAK INTERNAL PD 20K

**TBT LSX PINS VCCIO CONFIGURATION**

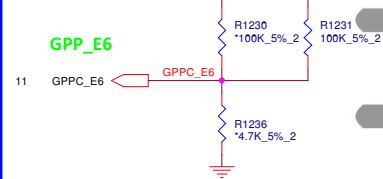
High: 3.3V
Low: 1.8V
NO INTERNAL PU/PD

**(RSVD) A0 PERSONALITY STRAP**

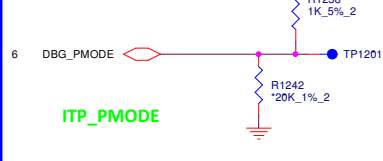
High: DISABLE
Low: ENABLE
External pull-up is required.

**(RSVD) JTAG ODT DISABLE**

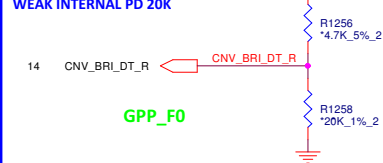
High: JTAG ODT Enable
Low: JTAG ODT Disable
External pull-up is required.

**(RSVD) ITP PMODE**

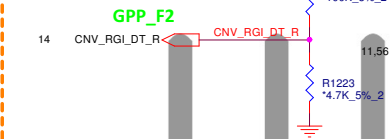
High: DFXTESTMODE DISABLED(DEFAULT)
Low: DFXTESTMODE ENABLED
WEAK INTERNAL PU 20K

**XTAL FREQUENCY SEL**

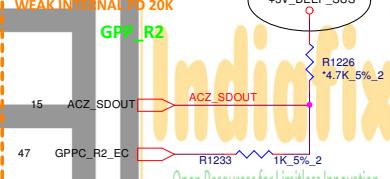
High: 24MHZ
(25 MHZ WHEN XTAL FREQ DIVIDER NON ZERO)
Low: 38.4MHZ (DEFAULT)
WEAK INTERNAL PD 20K

**M.2 CNVi Mode Select**

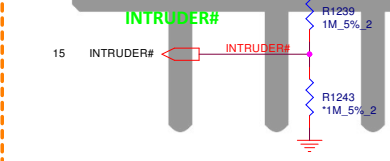
High: Integrated CNVi disabled
Low: Integrated CNVi enabled
An external pull-up or pull-down is required

**Flash Descriptor Security Override**

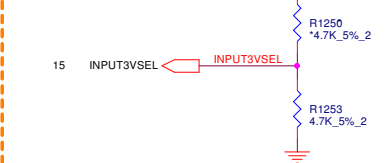
High: DISABLE
Low: ENABLE
WEAK INTERNAL PD 20K

**Reduce leakage from Coin Cell Batt**

High: ENABLE
Low: DISABLE

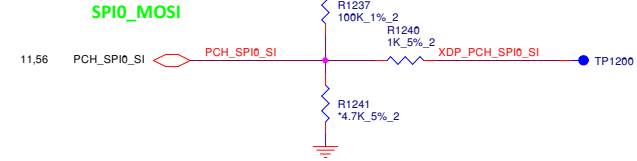
**3V SELECT STRAP**

0 = SPI voltage is 3.3V (4.7K ohm pull-down to GND)
1 = SPI voltage is 1.8V (4.7K ohm pull-up to DSW_PWROK)

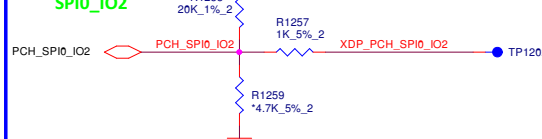
**(RSVD) BOOT HALT**

High: DISABLE
Low: ENABLE

External pull-up is required.

**(RSVD) CONSENT STRAP**

High: DISABLE
Low: ENABLE
External pull-up is required.

**THC0_SPI1_CLK**

THC0_SPI1 Clock: THC SPI1 clock output from PCH. Supports 20 MHz, 33 MHz and 50 MHz.

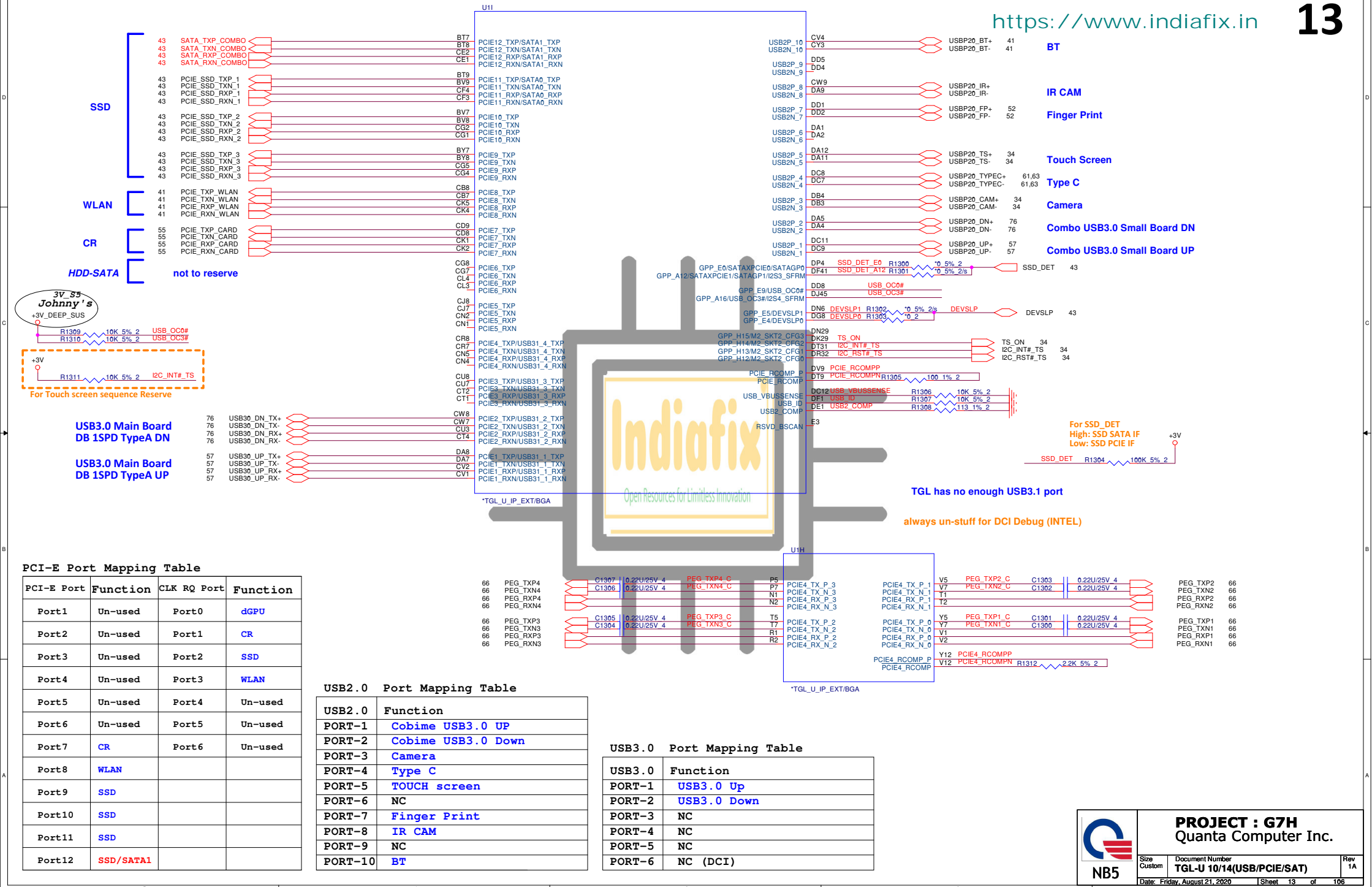
**THC0_SPI1_CS#**

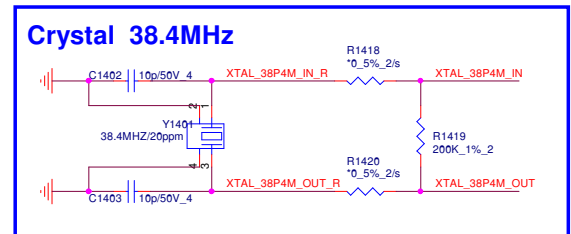
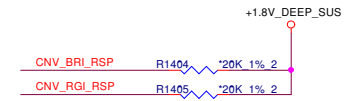
THC0_SPI1 Chip Select: Used to select the touch devices if it is connected to THC0_SPI1 interface.

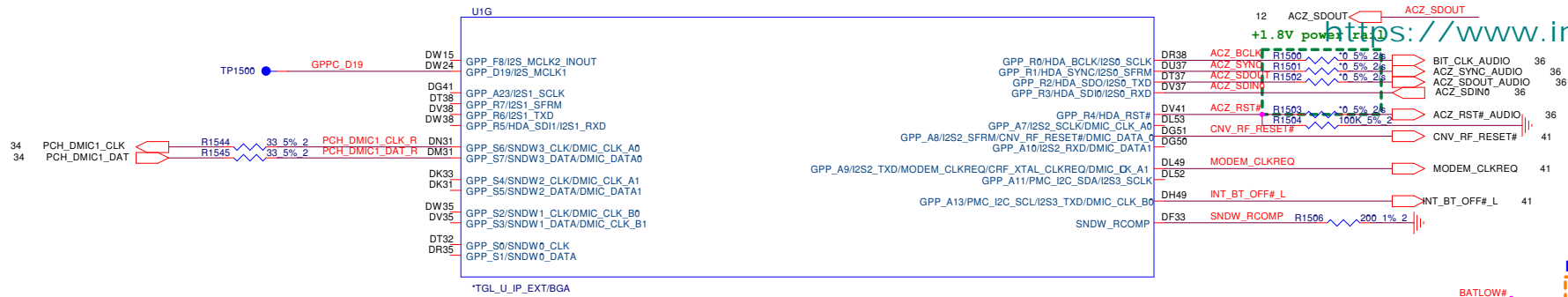


Because page size limited, some strap pin move to page 17

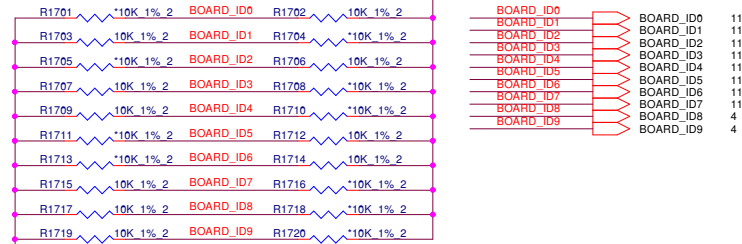




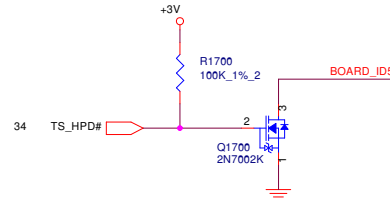




+3V_DEEP_SUS



For Touch screen Determination



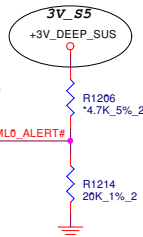
Model	BOARD_ID[9:7]	BOARD_ID6	Board ID5	Board ID4	BOARD_ID[3:1]	BOARD_ID0
GoT ICE	MD Sku only 000:Samsung 001:Hynix 010:Micron 100:Sam 16G K4AAG165WB-MCRO 110:Sam 16G K4AAG165WB-MCTD 111:XXX	0: N17S 1: N18S	0: Non-I2C TS 1: I2C TS active by LCD cable	0: VRAM 2G 1: VRAM 4G	000: Able 001:Dezik 010:Strelka	0: UMA 1: DIS

Boot Strap 0

WEAK INTERNAL PD 20K

GPP_C5/SMLOALERT#

11 SML0_ALERT#

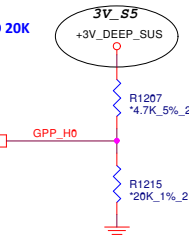


Boot Strap 1

WEAK INTERNAL PD 20K

GPP_H0

6 GPP_H0

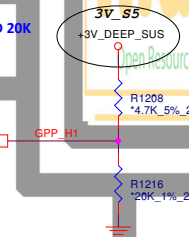


Boot Strap 2

WEAK INTERNAL PD 20K

GPP_H1

6 GPP_H1

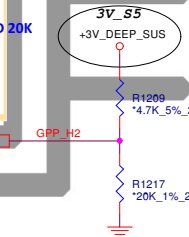


Boot Strap 3

WEAK INTERNAL PD 20K

GPP_H2

6 GPP_H2



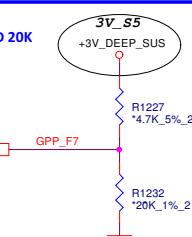
Boot Strap	4-bit boot strap configuration encodings
0000	BIOS/CSME on SPI & eSPI is enabled
0010	BIOS/CSME on SPI & eSPI is disabled
0100	BIOS on eSPI Peripheral Channel; CSME on master SP
1000	BIOS/CSME on eSPI
1100	BIOS on eSPI peripheral Channel; CSME on slave SPI

Reserved

WEAK INTERNAL PD 20K

GPP_F7

6 GPP_F7

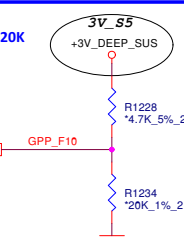


Reserved

WEAK INTERNAL PD 20K

GPP_F10

6 GPP_F10



RING OSCILLATOR BYPASS
High: BYPASS MODE ENABLED
Low: RING OSCILLATOR
(QUALIFIED BY DFTESTMODE)
NO INTERNAL PU/PD

GPP_H3



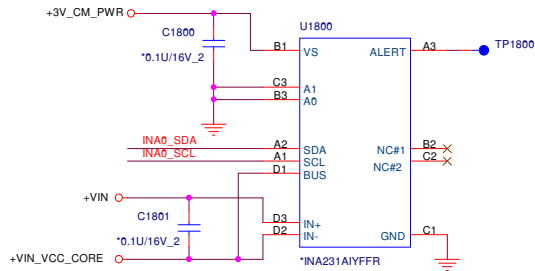
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+VCCIN

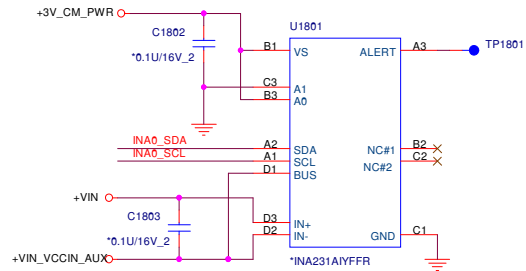
Slave Addresses:1000000 Placed close to PR8708

INA0_SCL/SDA

**+VCCIN_AUX**

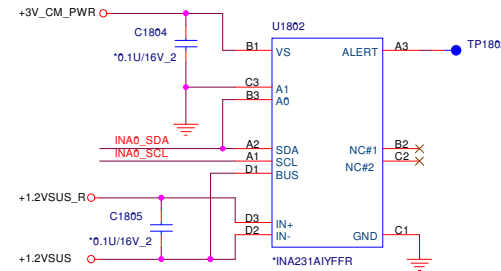
Slave Addresses:1000001 Placed close to PR9204

INA0_SCL/SDA

**+1.2VSUS**

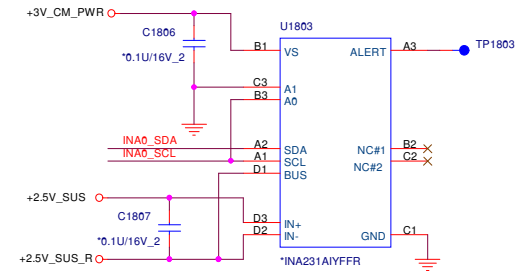
Slave Addresses:1000010 Placed close to PR8705

INA0_SCL/SDA

**+2.5V_SUS**

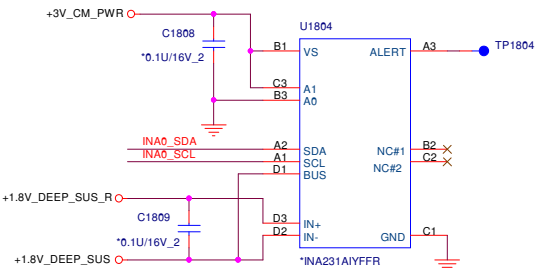
Slave Addresses:1000011 Placed close to PR8701

INA0_SCL/SDA

**+1.8V_DEEP_SUS**

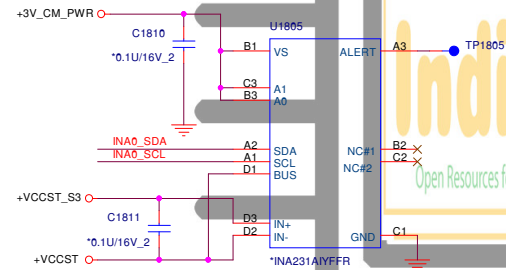
Slave Addresses:1000100 Placed close to PR9302

INA0_SCL/SDA

**+VCCST**

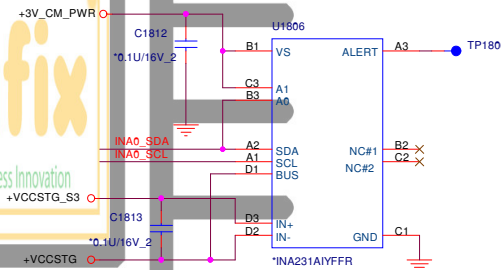
Slave Addresses:1000101

INA0_SCL/SDA

**+VCCSTG**

Slave Addresses:1000110

INA0_SCL/SDA

**+VAD**

Slave Addresses:1000111 Placed close to PR8201

INA0_SCL/SDA

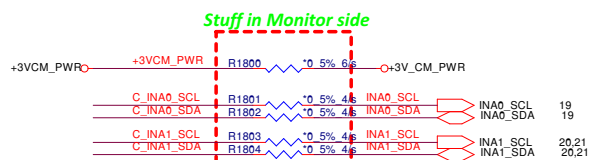
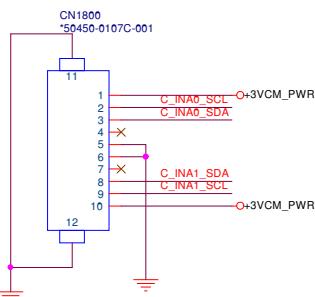


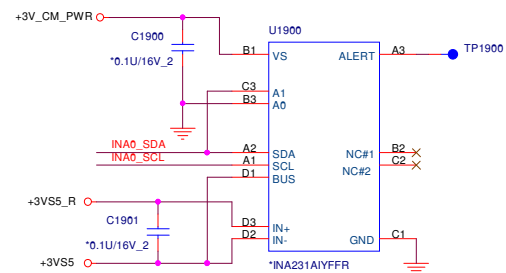
Table 7. INA231 Address Pins and Slave Addresses

A1	A0	SLAVE ADDRESS
GND	GND	1000000
GND	V _S	1000001
GND	SDA	1000010
GND	SCL	1000011
V _S	GND	1000100
V _S	V _S	1000101
V _S	SDA	1000110
V _S	SCL	1000111
SDA	GND	1001000
SDA	V _S	1001001
SDA	SDA	1001010
SDA	SCL	1001011
SCL	GND	1001100
SCL	V _S	1001101
SCL	SDA	1001110
SCL	SCL	1001111

+3VS5

Slave Addresses:1001000 Placed close to PR8604

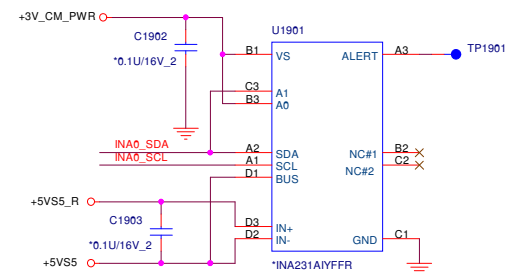
INA0_SCL/SDA



+5VS5

Slave Addresses:1001001 Placed close to PR8618

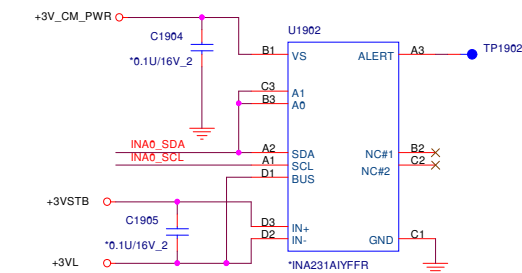
INA0_SCL/SDA



EC

Slave Addresses:1001010 Placed close to L4704

INA0_SCL/SDA



Battery (DC mode)

Slave Addresses:1001011 Placed close to PR8200

INA0_SCL/SDA



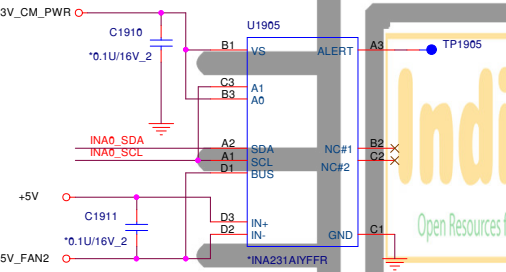
Slave Addresses:1001100 Placed close to PR9601

INA0_SCL/SDA

FAN2

Slave Addresses:1001101 Placed close to PR9602

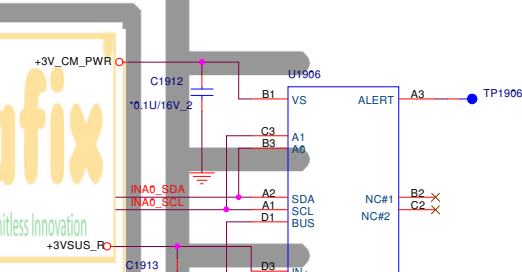
INA0_SCL/SDA



+3VSUS

Slave Addresses:1001110 Placed close to PR9600

INA0_SCL/SDA



+1.8V

Slave Addresses:1001111 Placed close to PR9608

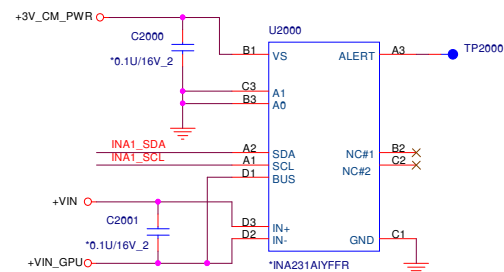
INA0_SCL/SDA



NVDD

Slave Addresses:100000 Placed close to PR9802

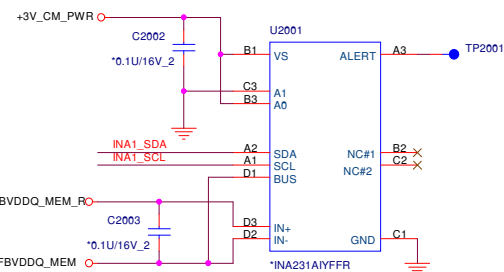
INA1_SCL/SDA



FBVDDQ_MEM

Slave Addresses:1000001 Placed close to PR10203

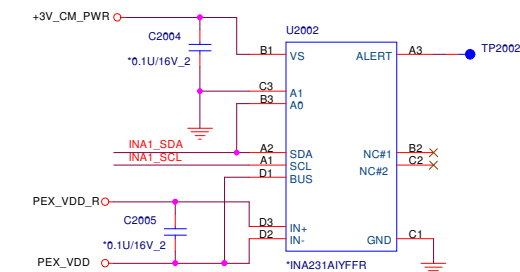
INA1_SCL/SDA



PEX_VDD

Slave Addresses:1000010 Placed close to PR10505

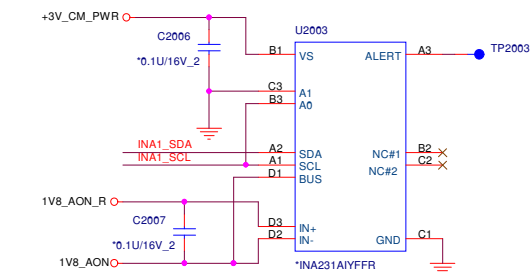
INA1_SCL/SDA



1V8_AON

Slave Addresses:1000011 Placed close to PR10514

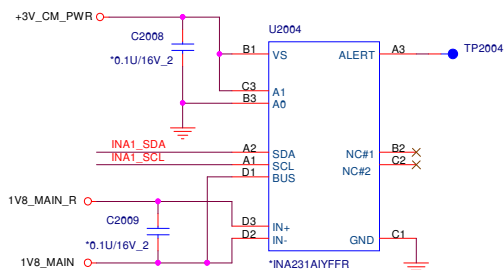
INA1_SCL/SDA



1V8_MAIN

Slave Addresses:1000100 Placed close to PR10513

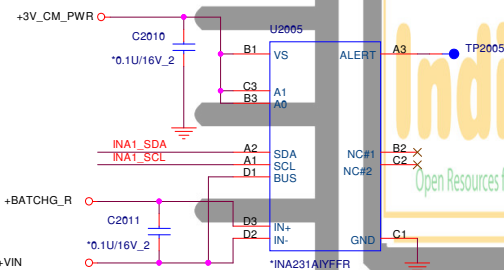
INA1_SCL/SDA



Battery

Slave Addresses:1000101 Placed close to L3407

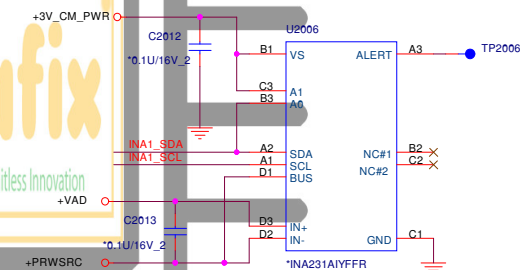
INA1_SCL/SDA



charge

Slave Addresses:1000110 Placed close to L3406

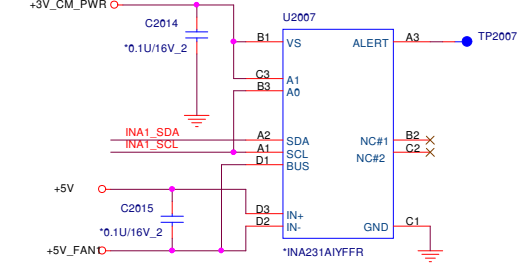
INA1_SCL/SDA




FAN1

Slave Addresses:1000111 Placed close to R3433

INA1_SCL/SDA



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WLAN

Slave Addresses:1001000 Placed close to R4128

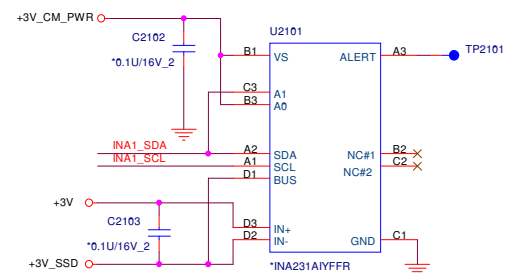
INA1_SCL/SDA



SSD

Slave Addresses:1001001 Placed close to R4300

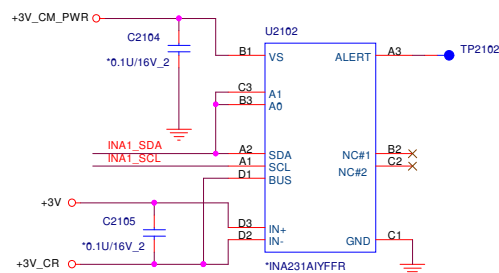
INA1_SCL/SDA



Card Reader

Slave Addresses:1001010 Placed close to R5500

INA1_SCL/SDA



SPI ROM

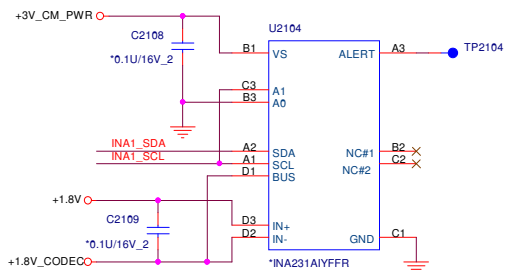
Slave Addresses:1001011 Placed close to R5621

INA1_SCL/SDA

Codec +1.8V

Slave Addresses:1001100 Placed close to R3616

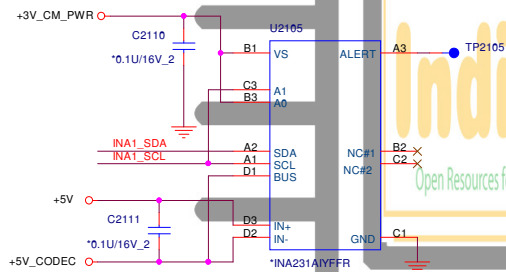
INA1_SCL/SDA



Codec +5V

Slave Addresses:1001101 Placed close to R3617

INA1_SCL/SDA



CAMERA


Slave Addresses:1001110 Placed close to R3434

INA1_SCL/SDA

IR CAM PWR

Slave Addresses:1001111 Placed close to R3435

INA1_SCL/SDA

 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number CURRENT SENSOR-4	Rev 1A
	Date: Friday, August 21, 2020	Sheet 21 of 106	

PD
Slave Addresses:1000000 Placed close to R6131
INB0_SCL/SDA

PD_ROM
Slave Addresses:1000001 Placed close to R6100
INB0_SCL/SDA

FAN1
Slave Addresses:1000010 Placed close to R5402
INB0_SCL/SDA

FAN2
Slave Addresses:1000011 Placed close to R5403
INB0_SCL/SDA

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22

Finger Print
Slave Addresses:1000100 Placed close to R5225
INB0_SCL/SDA

TPM
Slave Addresses:1000101 Placed close to R5100
INB0_SCL/SDA

TBD
Slave Addresses:1000110 Placed close to
INB0_SCL/SDA

TBD
Slave Addresses:1000111 Placed close to
INB0_SCL/SDA

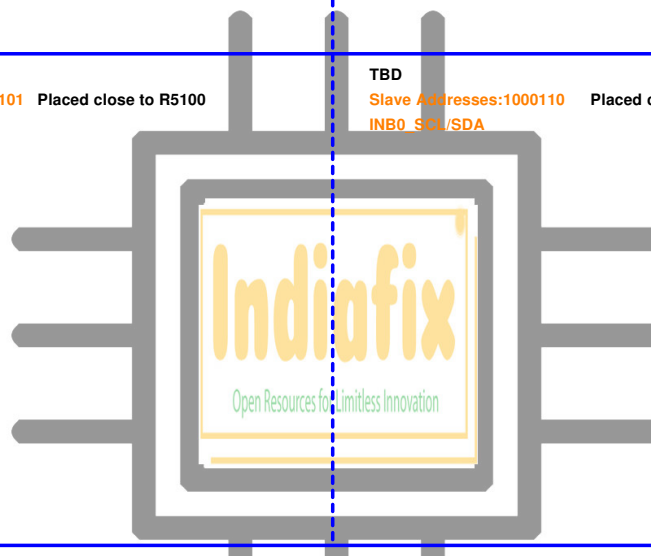



Table 7. INA231 Address Pins and Slave Addresses

A1	A0	SLAVE ADDRESS
GND	GND	1000000
GND	V _S	1000001
GND	SDA	1000010
GND	SCL	1000011
V _S	GND	1000100
V _S	V _S	1000101
V _S	SDA	1000110
V _S	SCL	1000111
SDA	GND	1001000
SDA	V _S	1001001
SDA	SDA	1001010
SDA	SCL	1001011
SCL	GND	1001100
SCL	V _S	1001101
SCL	SDA	1001110
SCL	SCL	1001111

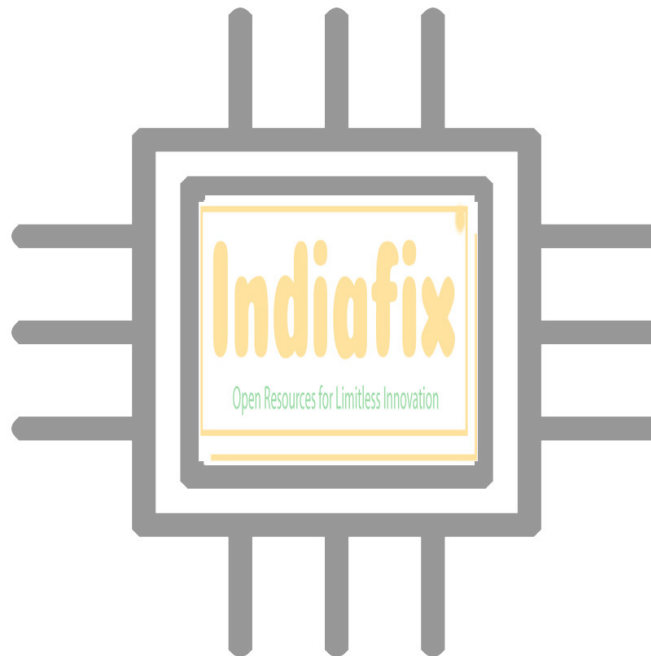
**NB5**


PROJECT : G7H
Quanta Computer Inc.

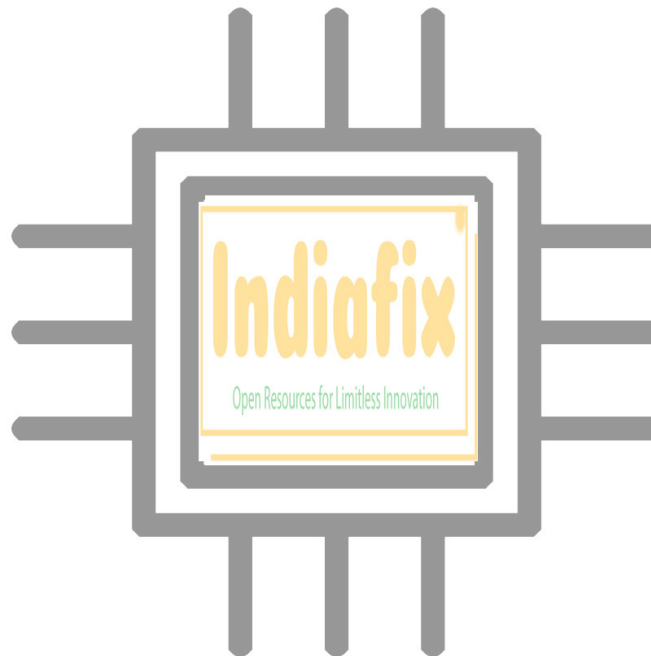
Size Custom	Document Number CURRENT SENSOR-5	Rev 1A
Date: Friday, August 21, 2020		


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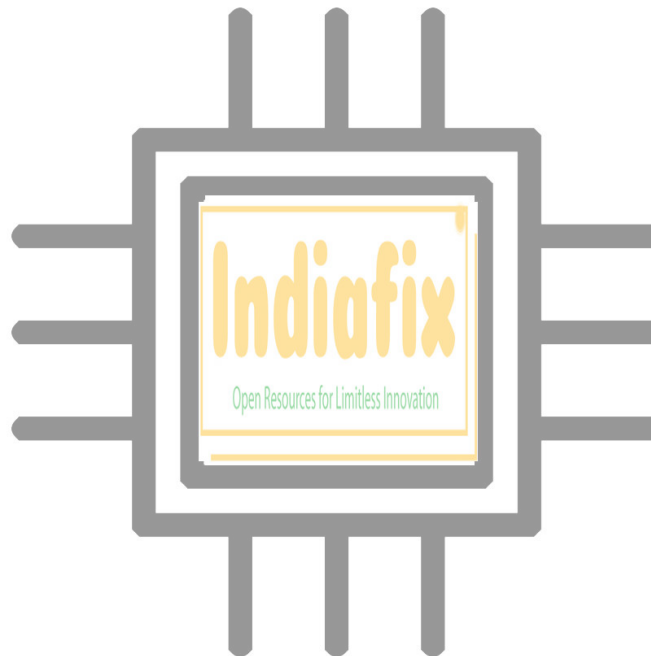
<https://www.indiafix.in>




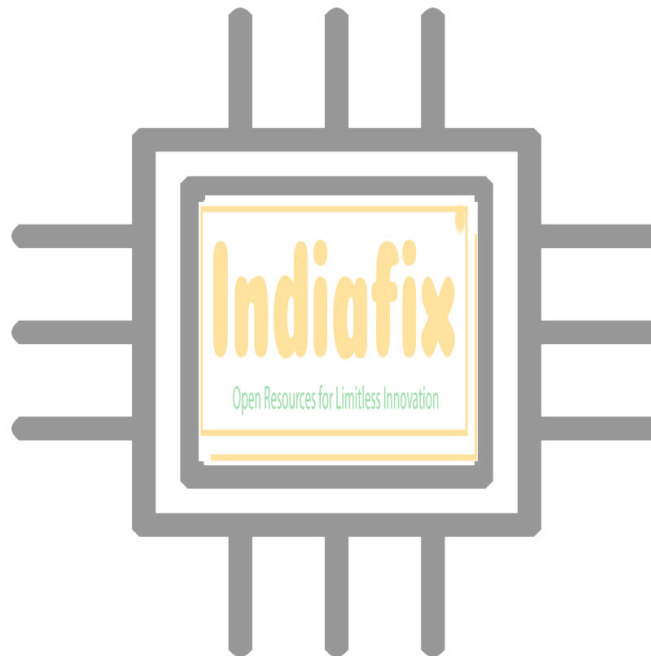
 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number PCH /NC	Rev 1A
	Date: Friday, August 21, 2020		Sheet 23 of 106




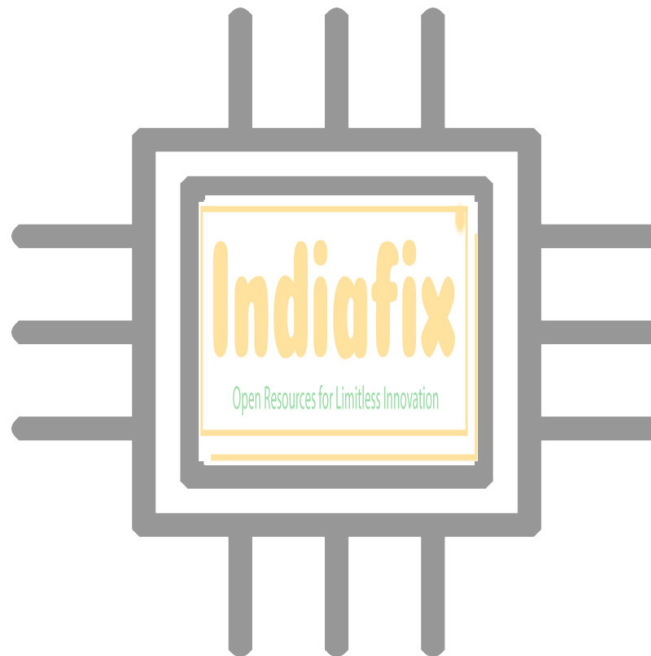
 NB5			PROJECT : G7H Quanta Computer Inc.		
Size Custom	Document Number PCH /NC		Date: Friday, August 21, 2020	Sheet 24 of 106	Rev 1A




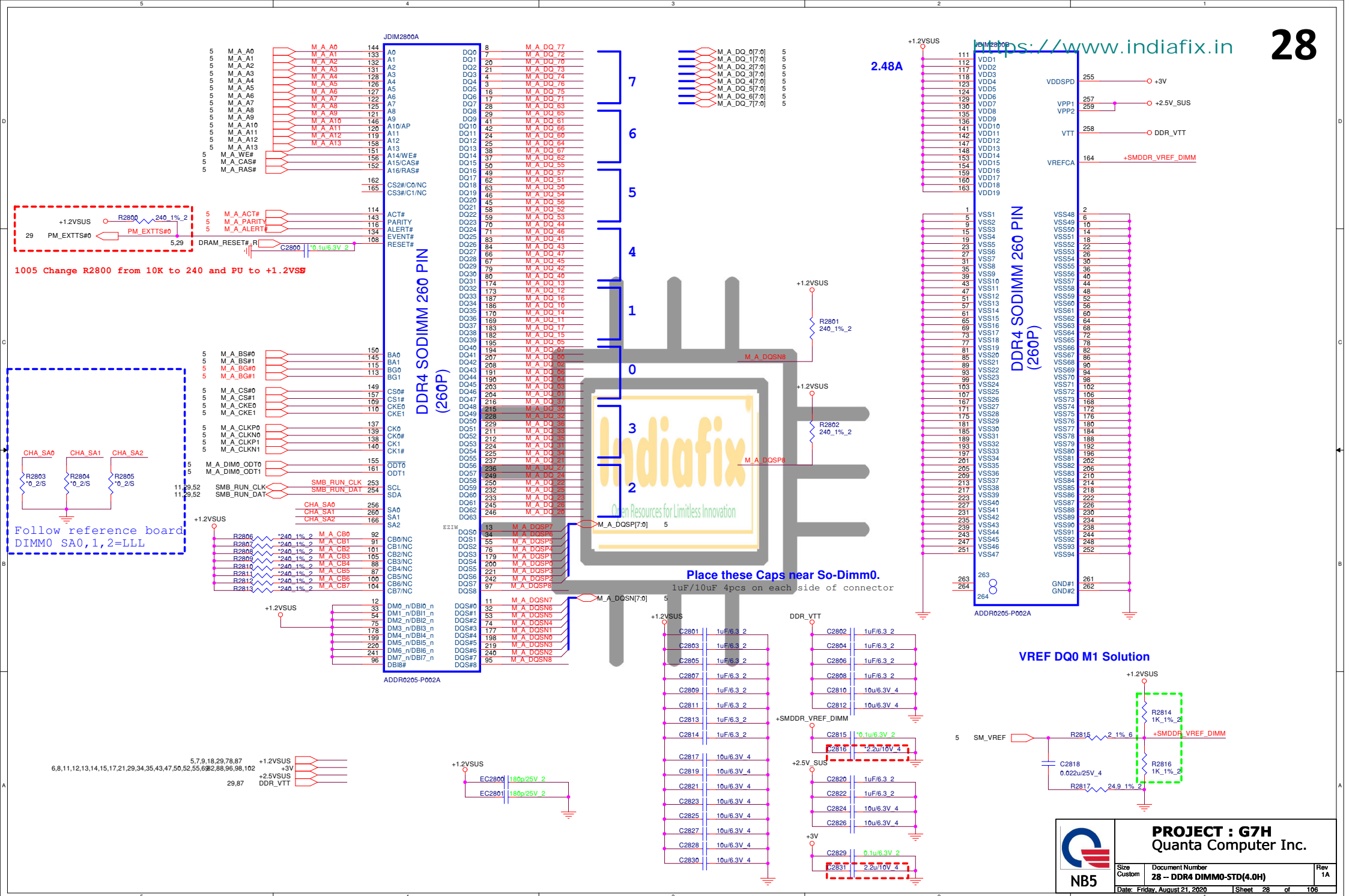
 NB5	PROJECT : G7H Quanta Computer Inc.				
	Size	Document Number			Rev
		PCH8			1A
	Date: Friday, August 21, 2020	Sheet	25	of	106

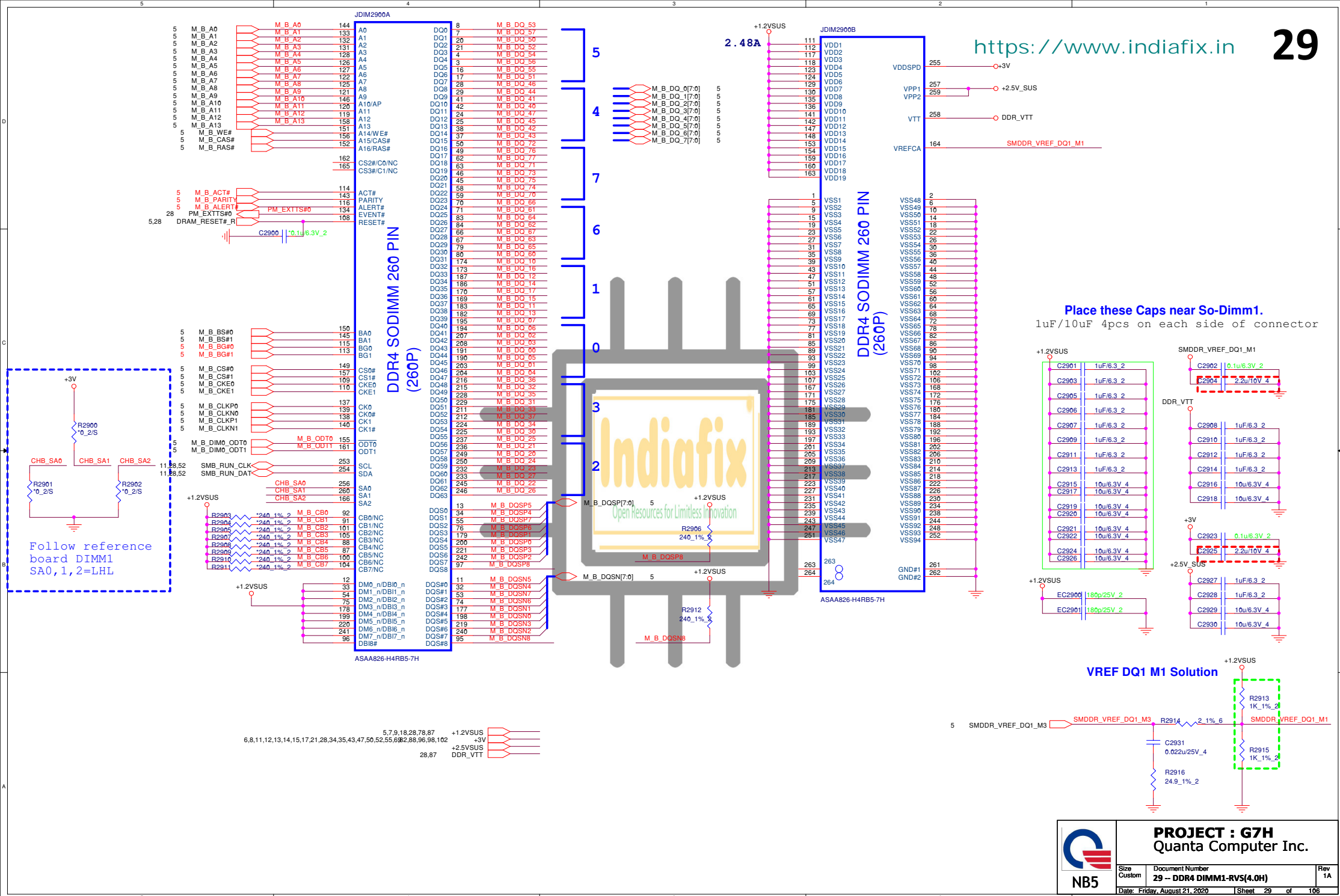


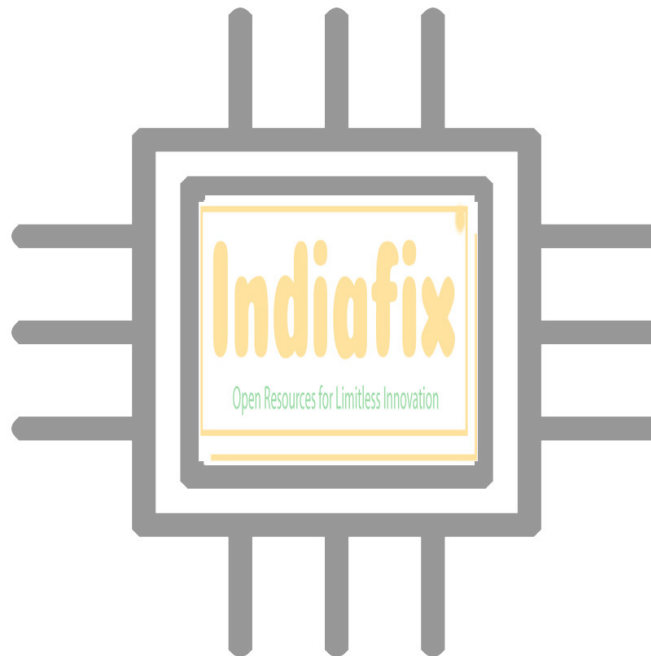
 NB5			PROJECT : G7H Quanta Computer Inc.		
Size		Document Number			Rev
		PCH9			1A
Date: Friday, August 21, 2020		Sheet 26		of 106	




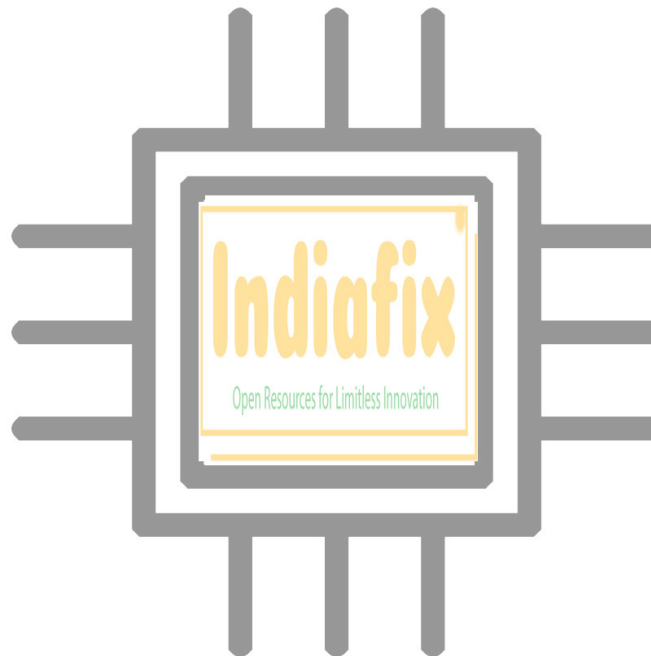
 NB5	PROJECT : G7H Quanta Computer Inc.	Rev 1A
Size Custom	Document Number PCH STRAP	
Date: Friday, August 21, 2020		Sheet 27 of 106




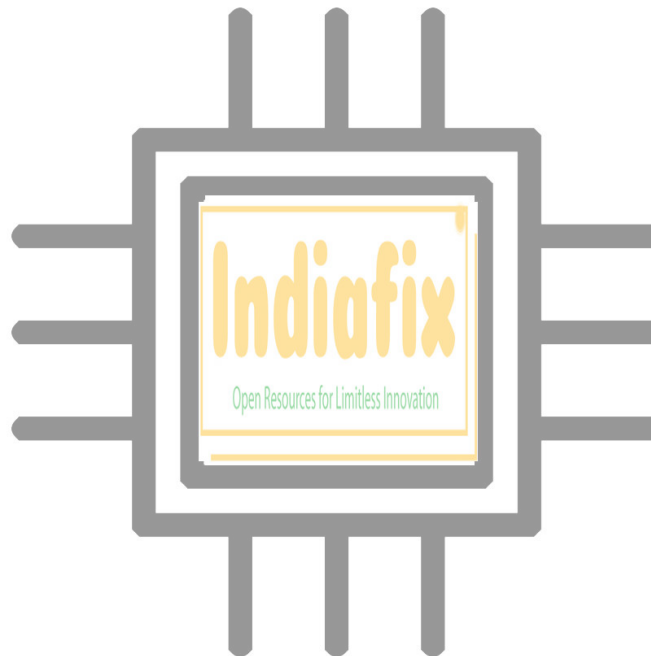





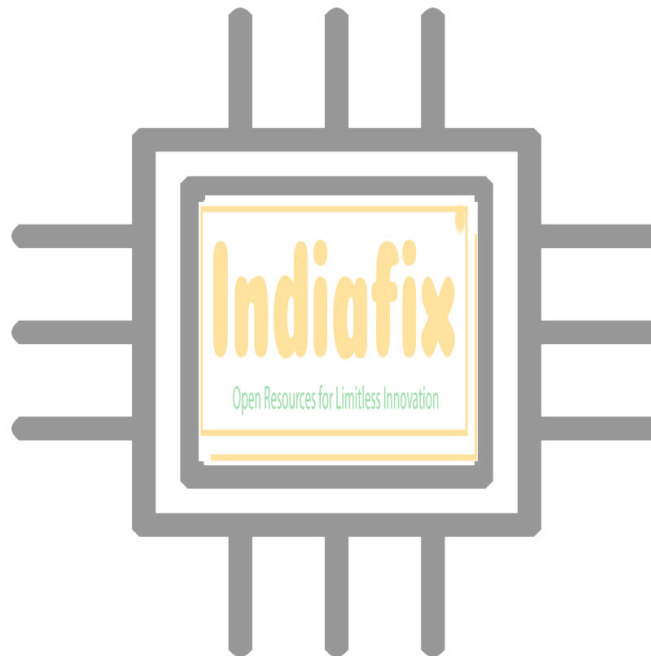
 NB5	PROJECT : G7H Quanta Computer Inc.				
	Size	Document Number			Rev
	MEMORY3			1A	
Date: Friday, August 21, 2020		Sheet	30	of	106




 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size	Document Number	Rev
	MEMORY4		1A
	Date: Friday, August 21, 2020		
	Sheet 31 of 106		

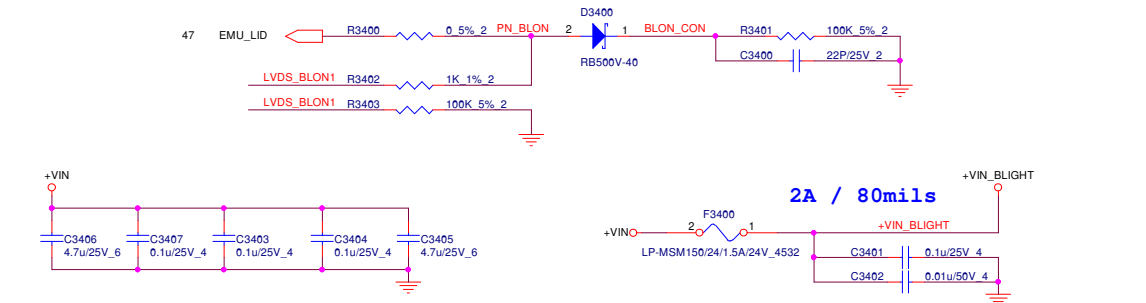


 NB5			PROJECT : G7H Quanta Computer Inc.		
Size		Document Number			Rev
		MEMORY5			1A
Date: Friday, August 21, 2020		Sheet 32		of 106	

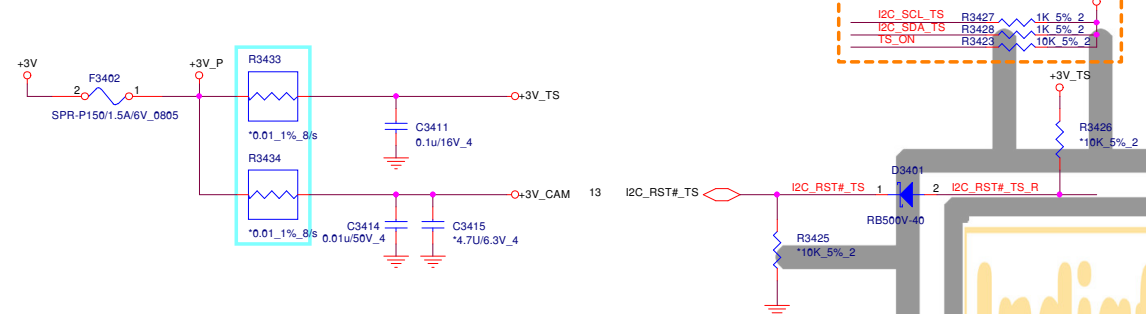


 NB5	PROJECT : G7H Quanta Computer Inc.	Rev 1A
	Size	
Document Number System Power Enable		
Date: Friday, August 21, 2020		Sheet 33 of 106

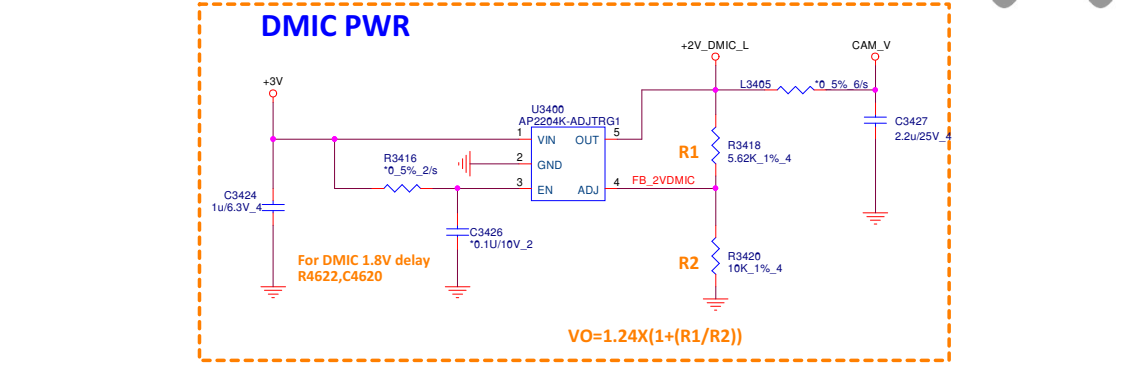
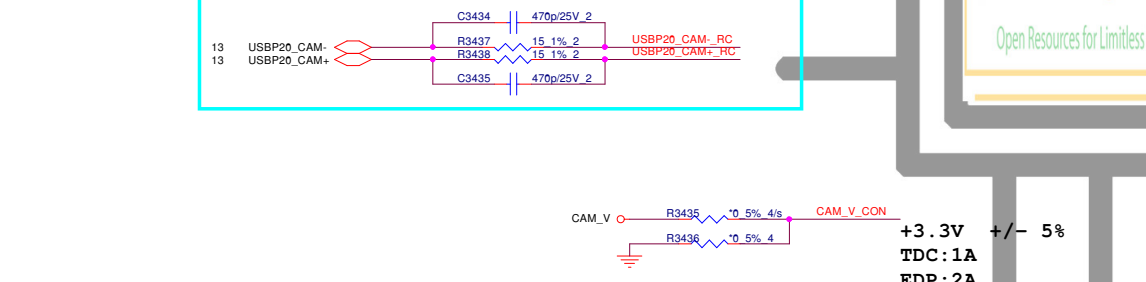
LID Switch



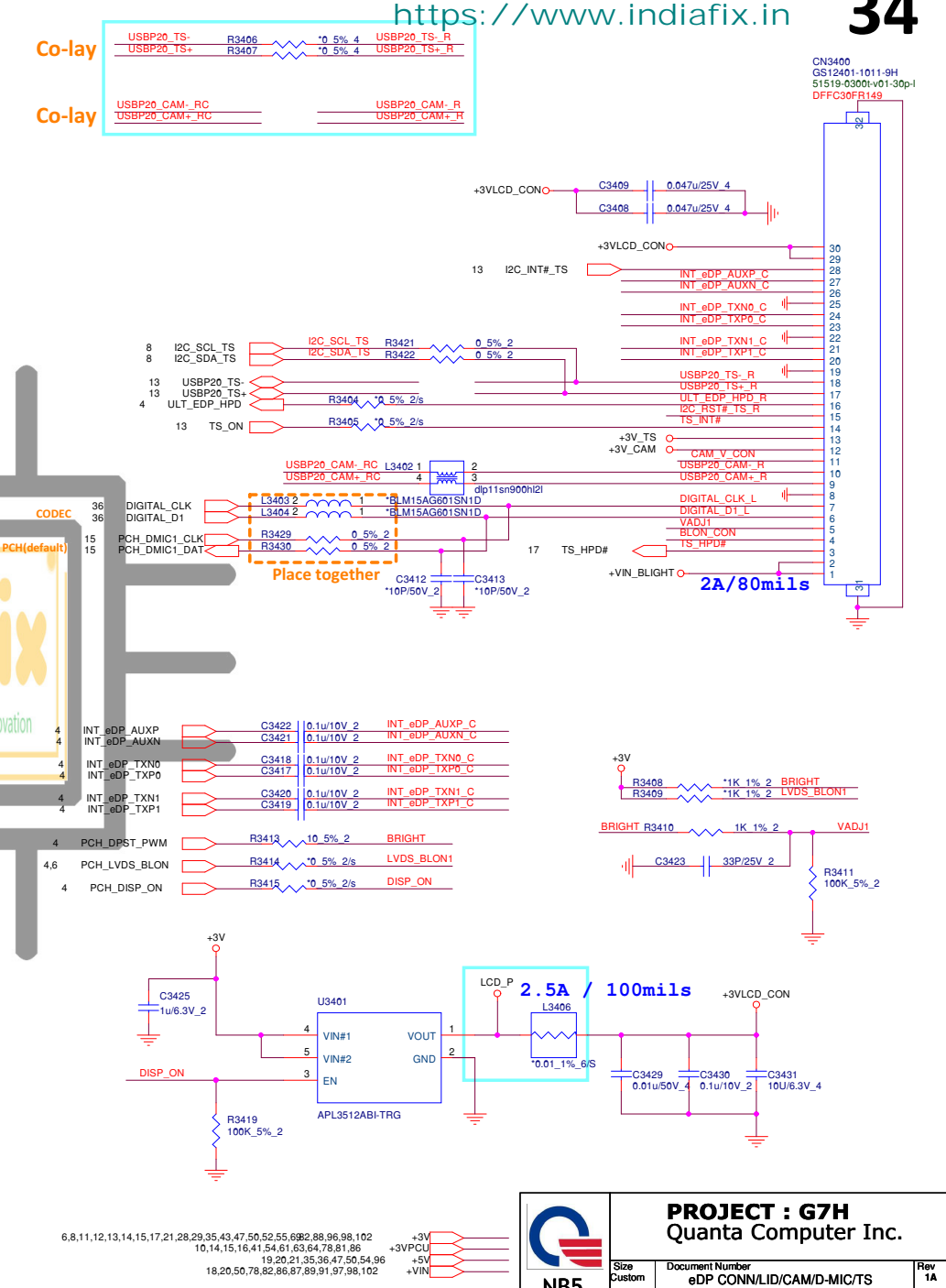
Touch screen

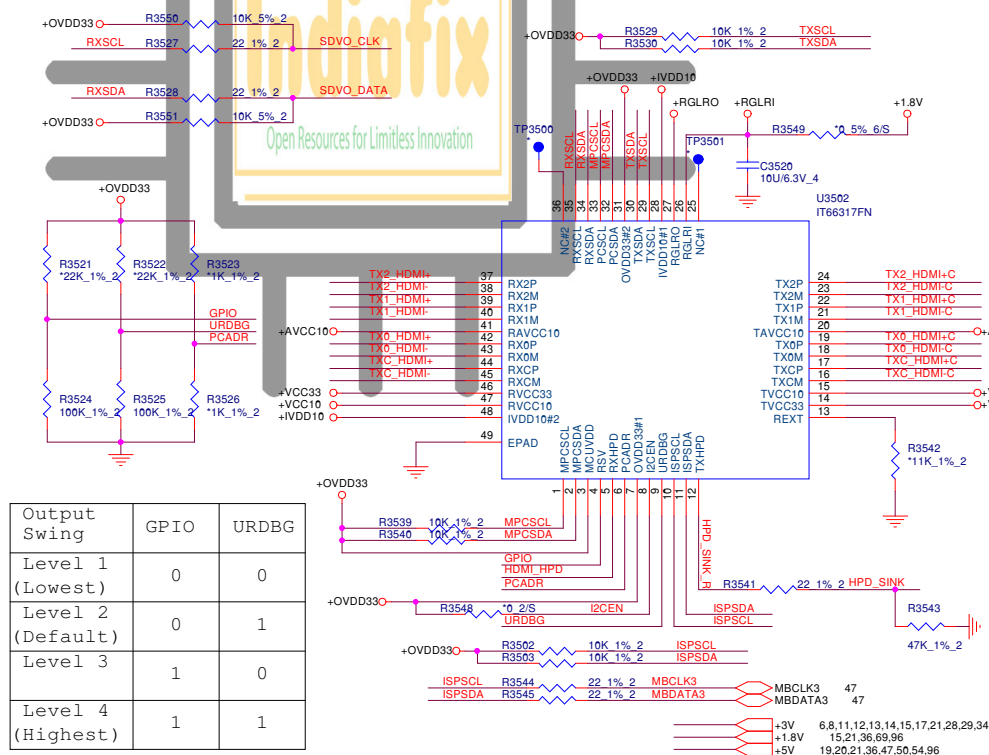
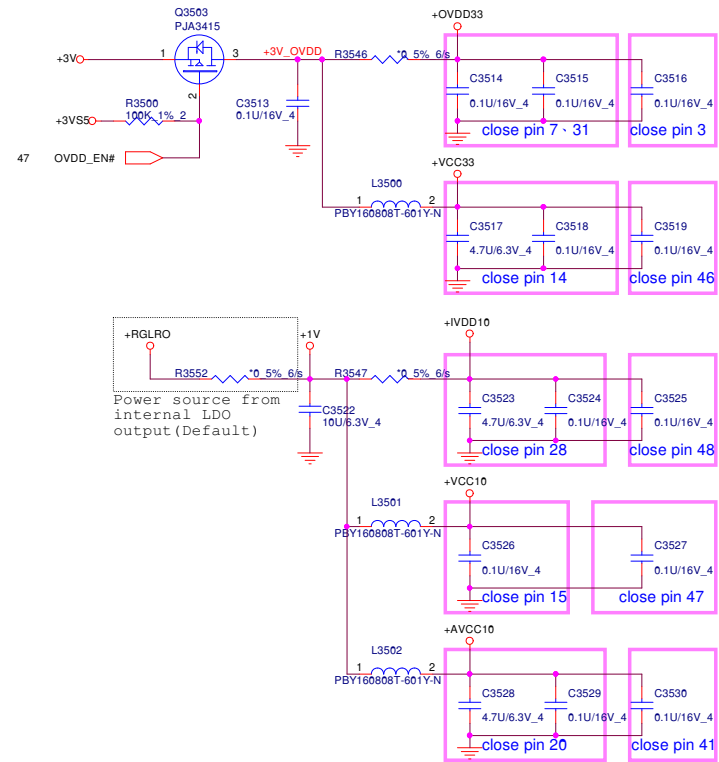
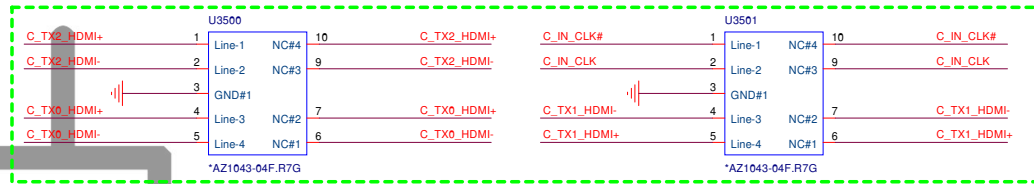
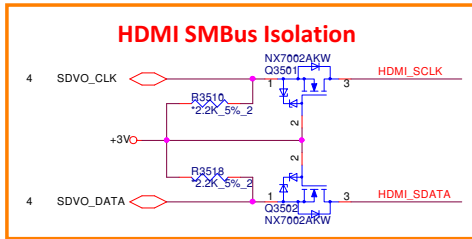
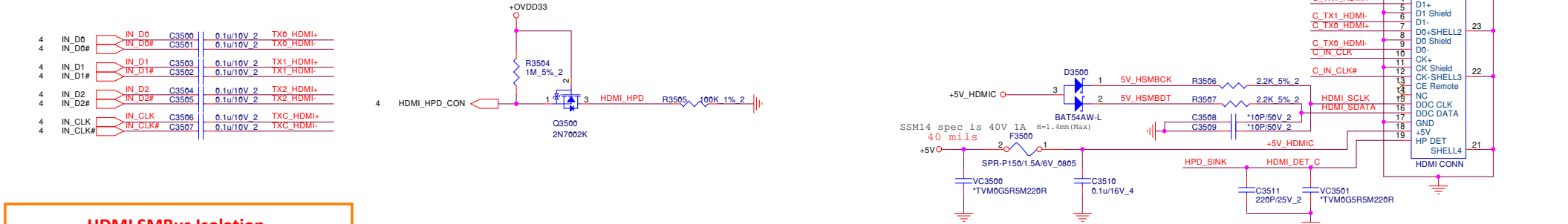


CAM

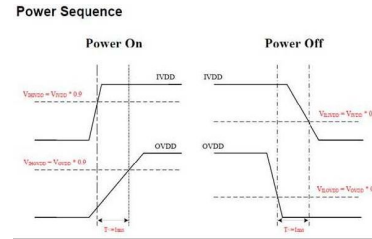
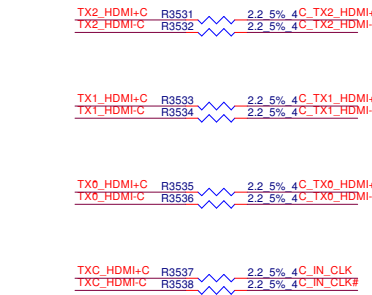


eDP Conn.





Output Swing	GPIO	URDBG
Level 1 (Lowest)	0	0
Level 2 (Default)	0	1
Level 3	1	0
Level 4 (Highest)	1	1



PROJECT : G7H
Quanta Computer Inc.

Document Number
HDMI

Rev 1A

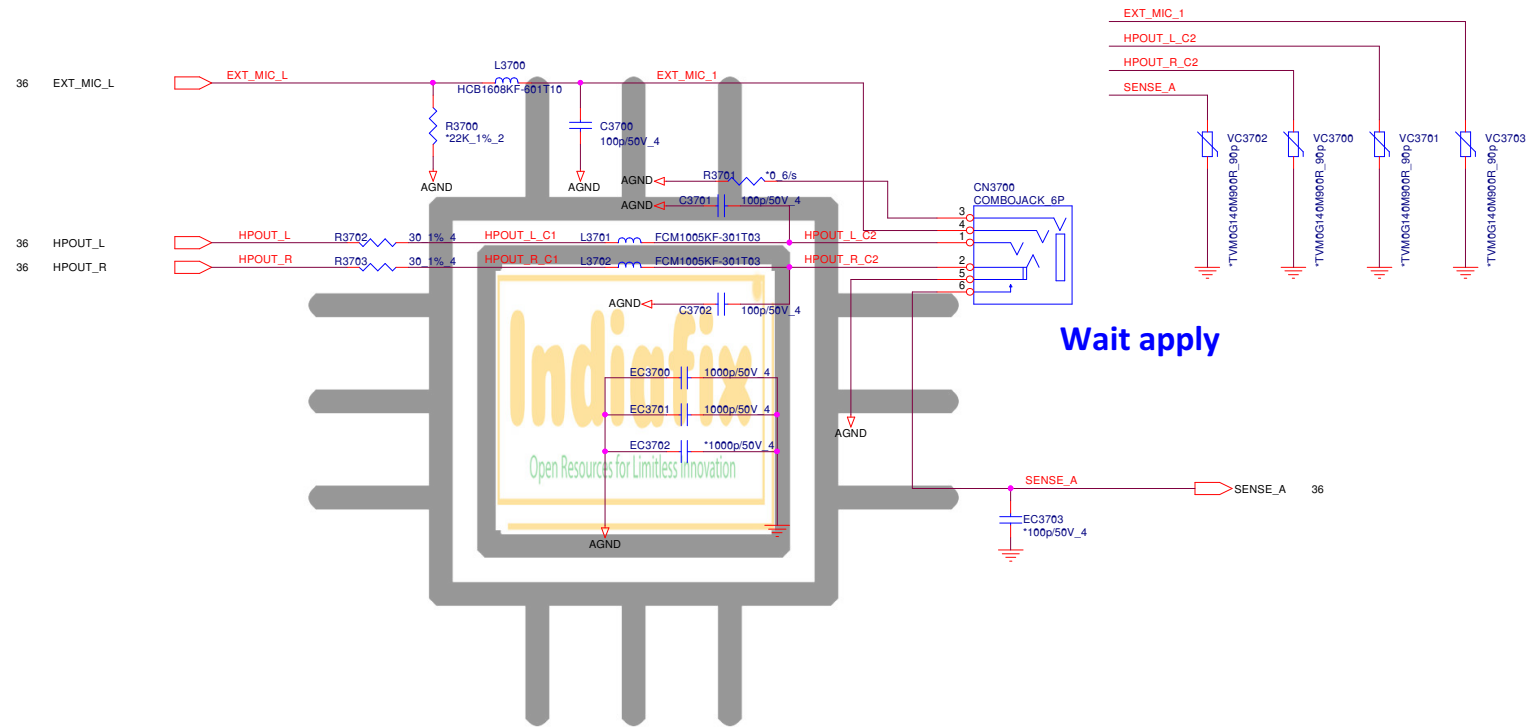
Date: Friday, August 21, 2020

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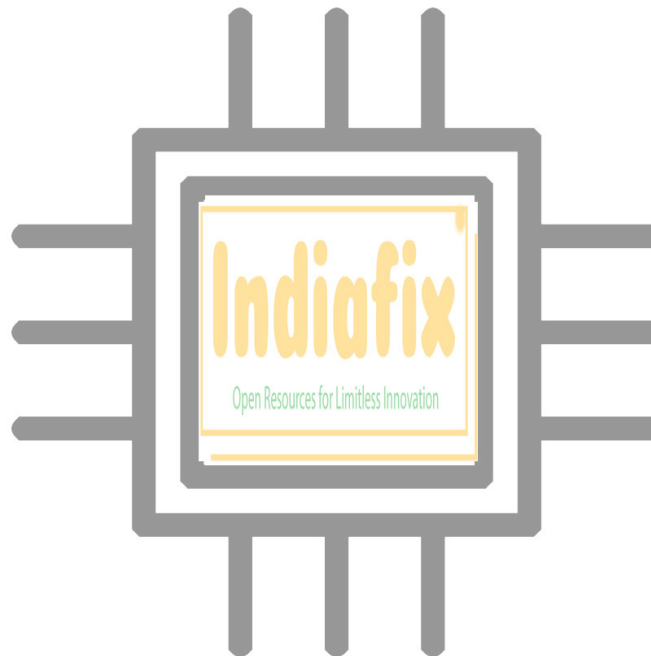
36


<https://www.indiafix.in>

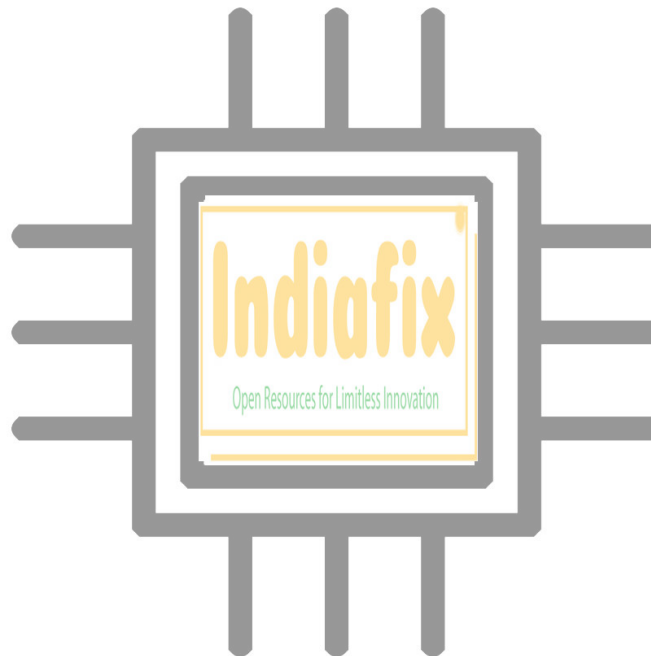
Audio JACK ESD




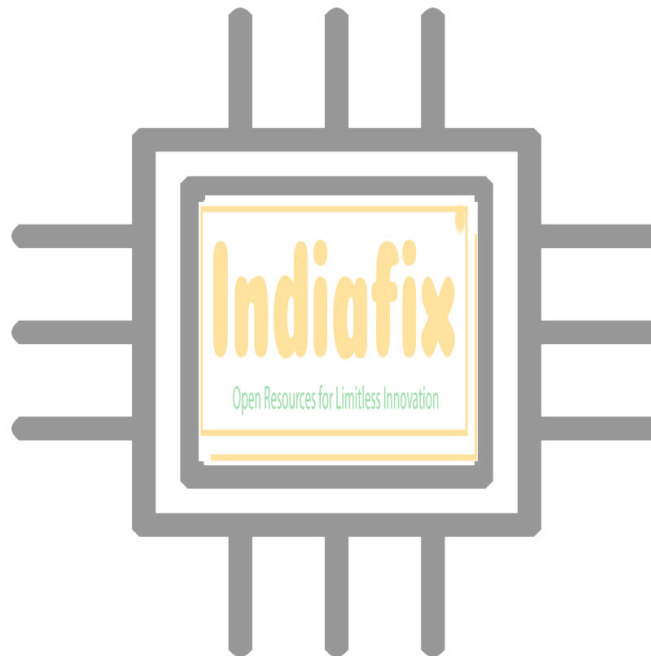
Wait apply




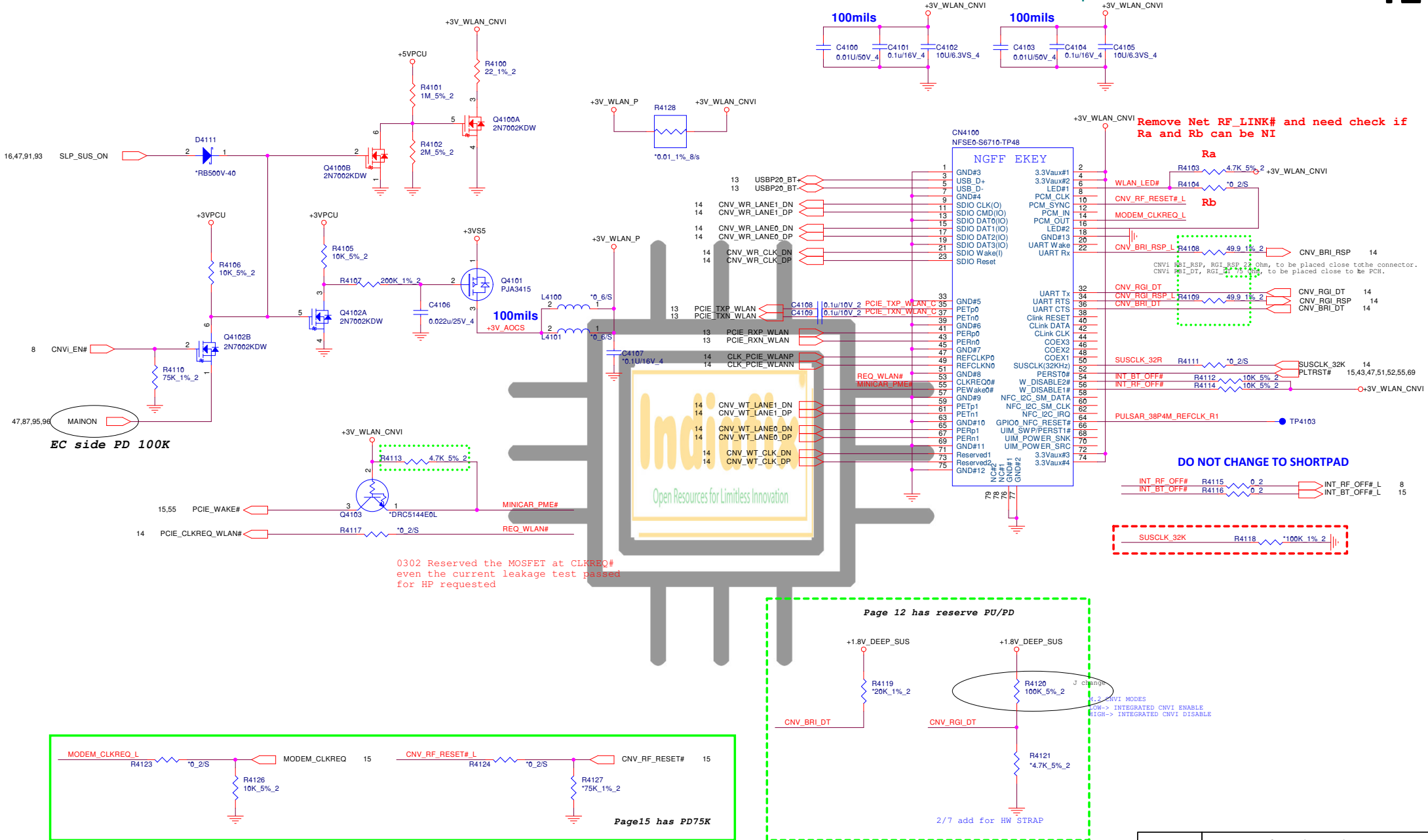
 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number Audio	Rev 1A
	Date: Friday, August 21, 2020 Sheet 38 of 106		



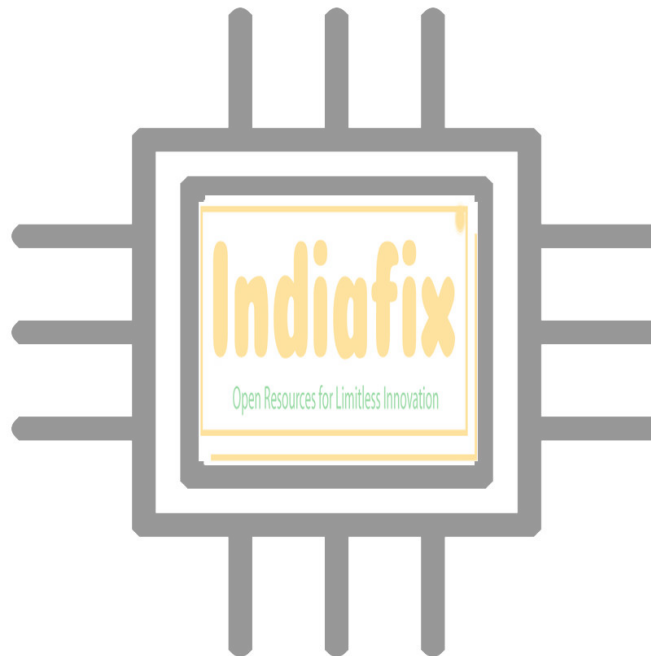
 NB5			PROJECT : G7H Quanta Computer Inc.		
Size Custom		Document Number Audio			Rev 1A
Date: Friday, August 21, 2020		Sheet 39		of 106	




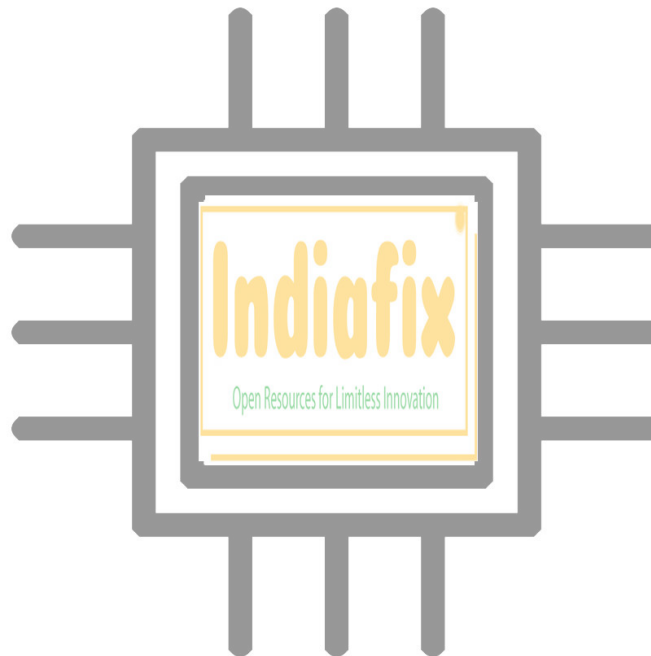
 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number LAN RTL8166EH/RTL8111HSH	Rev 1A
Date: Friday, August 21, 2020	Sheet 40 of 106		




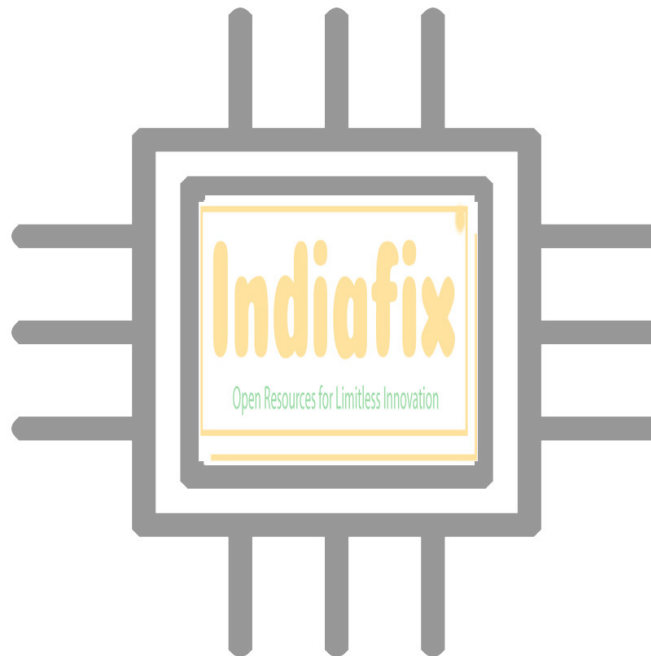
81,86,95,96,105
+5VPCU
+3VSS
+3VPCU
+1.8V_DEEP_SUS




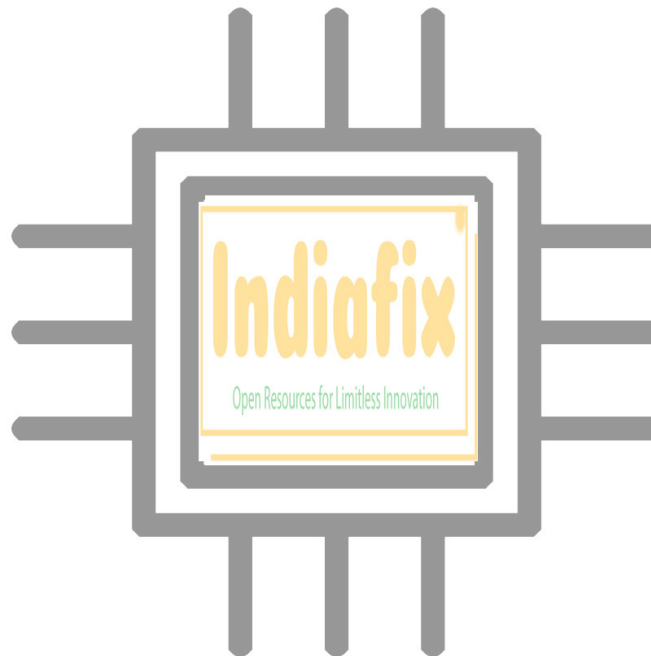
 NB5	PROJECT : G7H Quanta Computer Inc.				
	Size Custom	Document Number WWAN(NGFF)			Rev 1A
	Date: Friday, August 21, 2020			Sheet 42 of	106




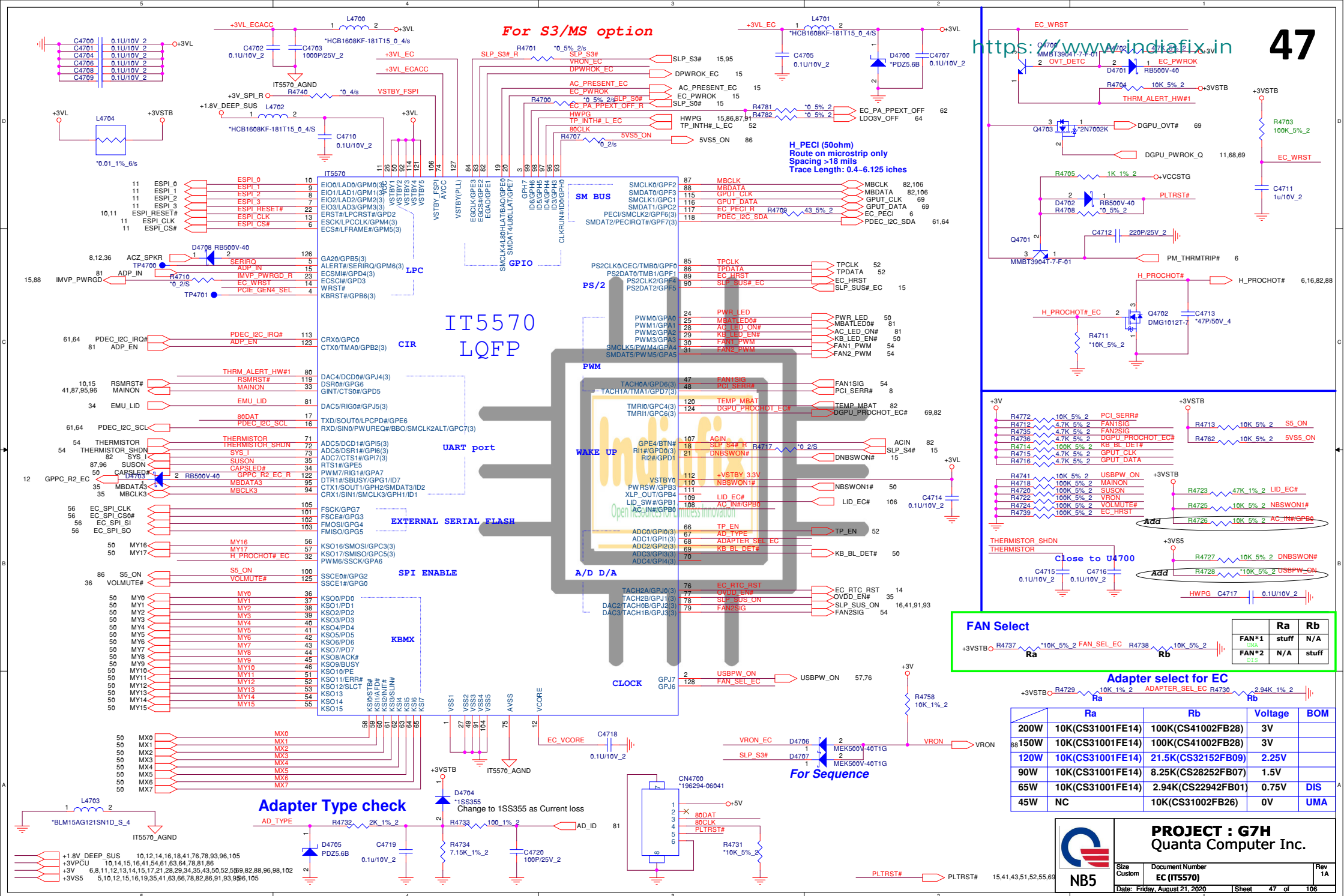
 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number HDD	Rev 1A
	Date: Friday, August 21, 2020 Sheet 44 of 106		

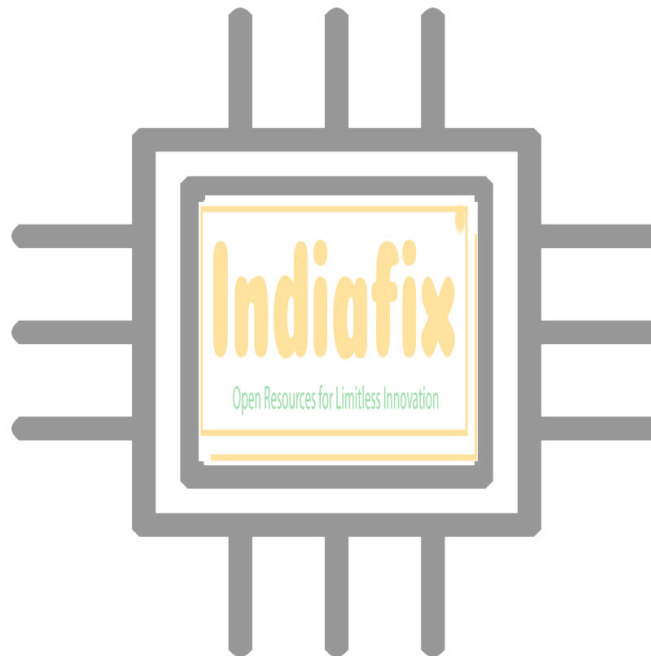



 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number EMMC	Rev 1A
Date: Friday, August 21, 2020		Sheet	45 of 106

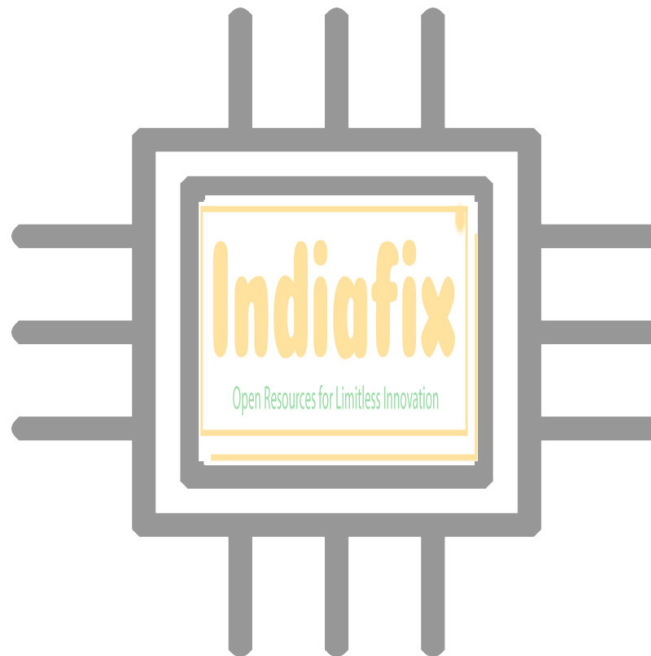



 NB5	PROJECT : G7H Quanta Computer Inc.			
	Size Custom	Document Number Camera		Rev 1A
Date: Friday, August 21, 2020		Sheet	46	of 106



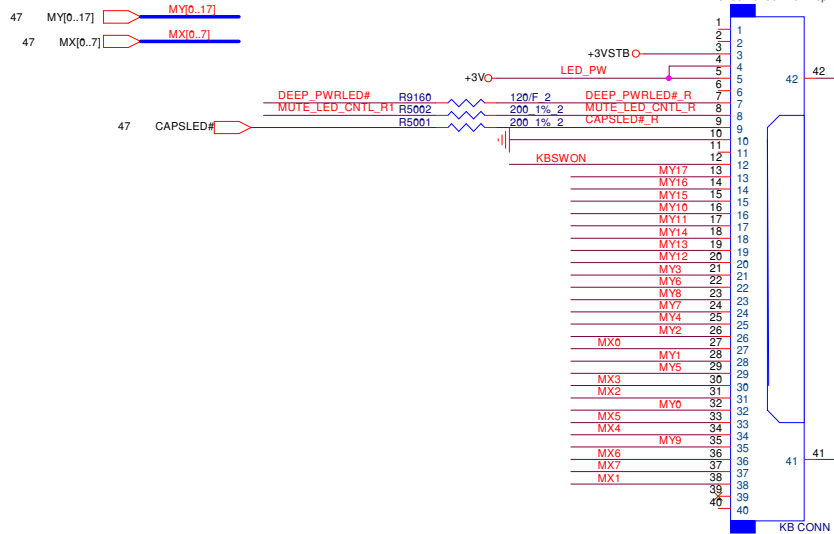


 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number EC1	Rev 1A
	Date: Friday, August 21, 2020		Sheet 48 of 106

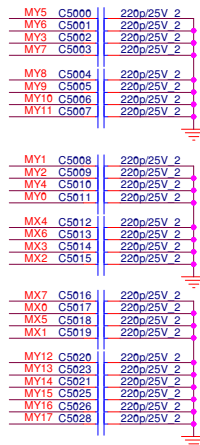
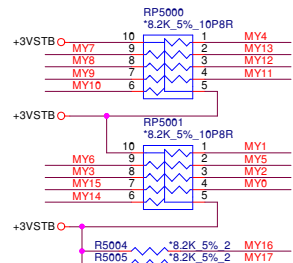


 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number EC2	Rev 1A
	Date: Friday, August 21, 2020		Sheet 49 of 106

KEYBOARD Con.



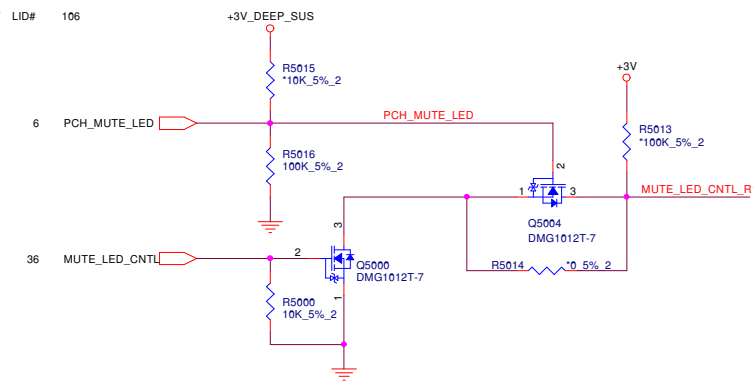
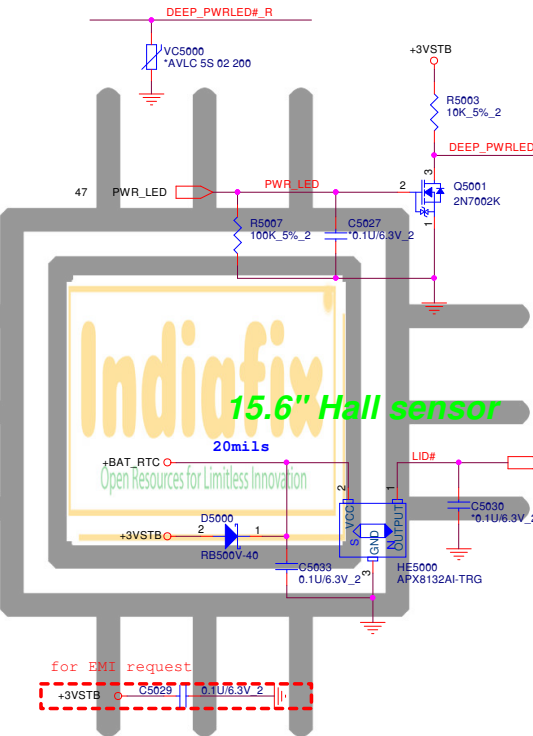
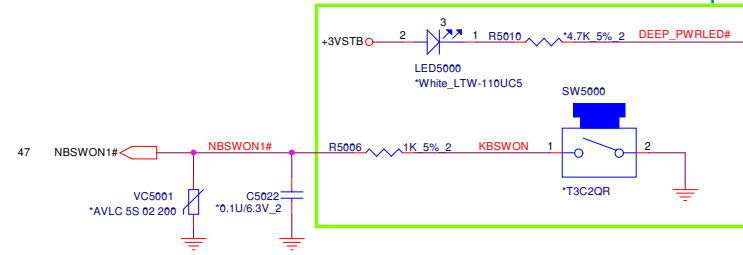
KEYBOARD PULL-UP



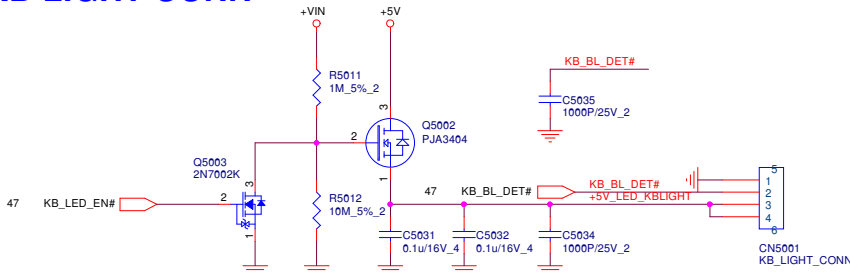
PWR Button & LED & HALL IC

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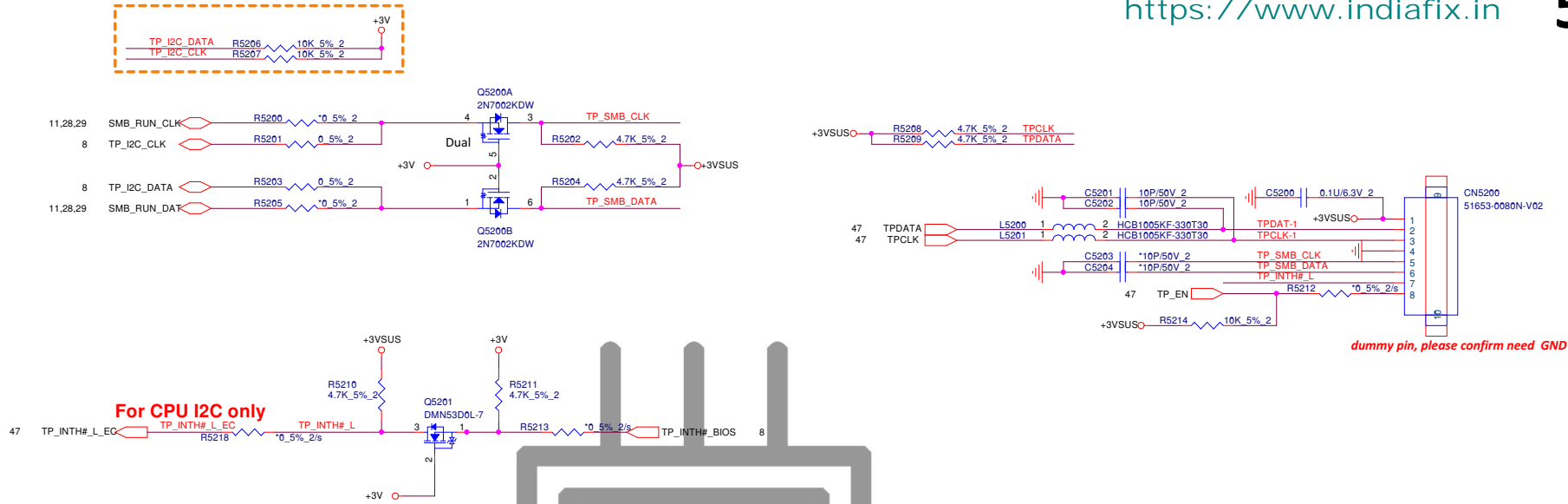


KB LIGHT CONN

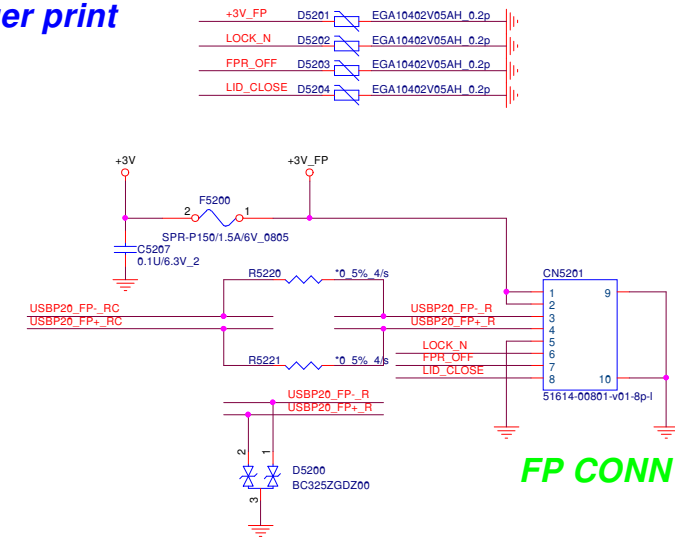


NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number	Rev 1A
	KB/KB LIGHT		
	Date: Friday, August 21, 2020	Sheet 50 of 106	

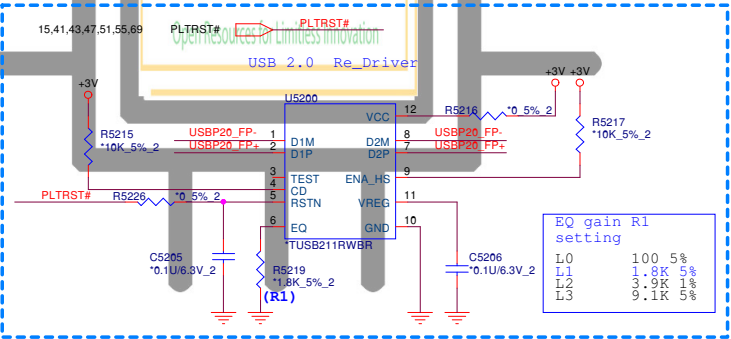
<https://www.indiafix.in>



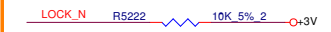
Finger print



FP CONN



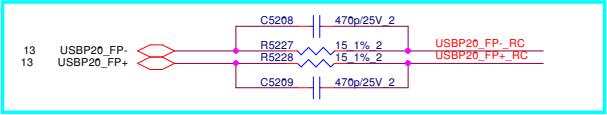
Lock_N for lock ROM data, pull high is no lock and PCH pull low to lock

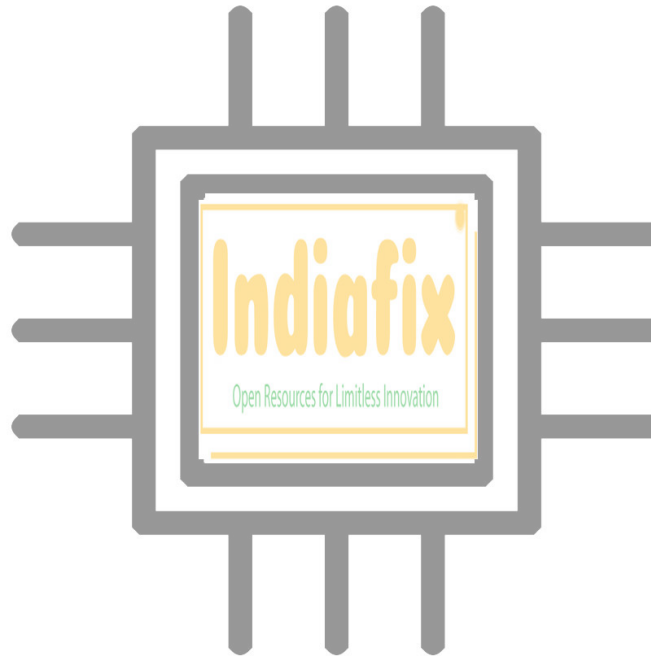



FPR not recommend to use SLEEP mode, just only pulhigh



20200513 Add by Caster request

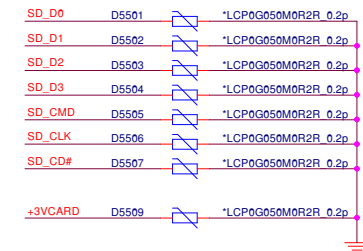
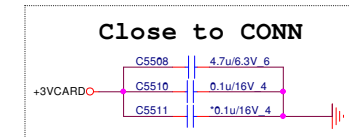
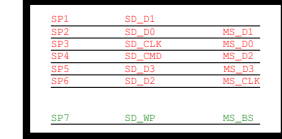




 NB5			PROJECT : G7H Quanta Computer Inc.		
Size Custom	Document Number G-Sensor		Date: Friday, August 21, 2020	Sheet 53 of 106	Rev 1A

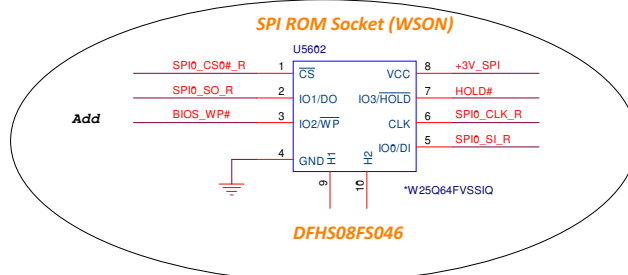
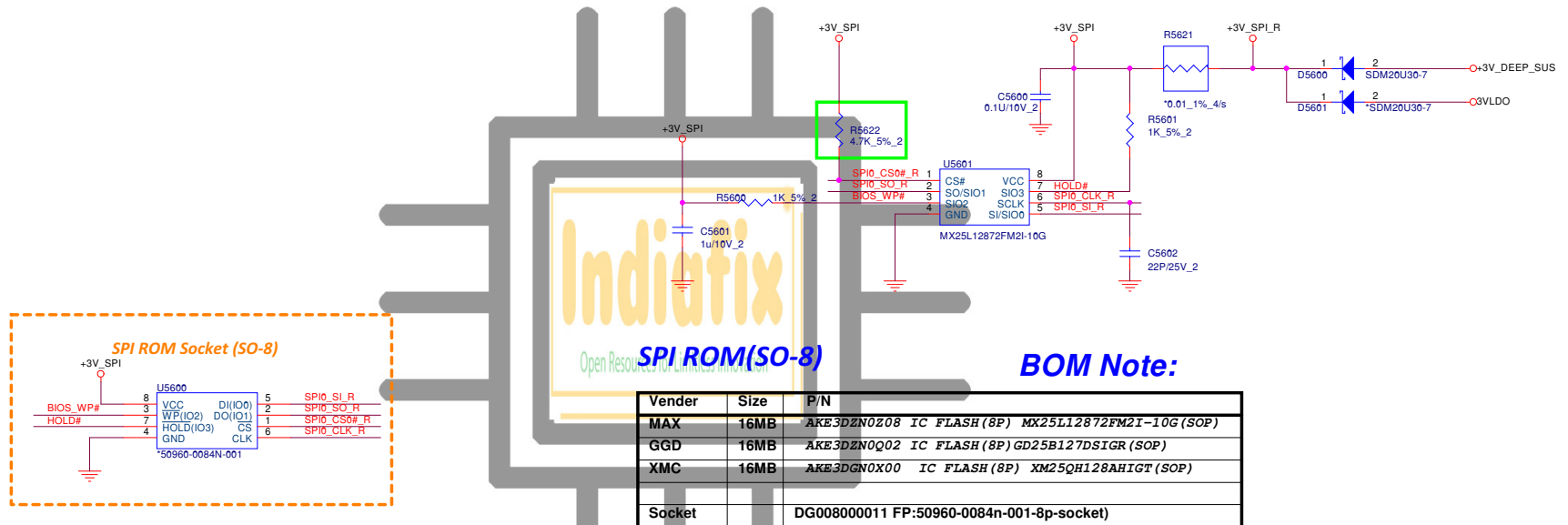
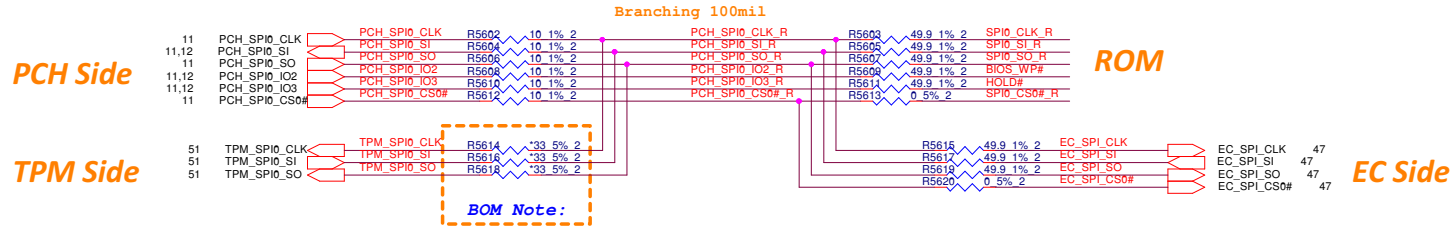
For 75 degree, 1.2V limit, (HW)

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TP6600 1 SPI0_CS0#_R
 TP6601 1 SPI0_SI_R
 TP5602 1 SPI0_SO_R
 TP5603 1 BIOS_WP#
 TP5604 1 HOLD#
 TP5605 1

TPX72~TPX77 need place to All BOT or All TOP



SPI ROM(WS0N)

BOM Note:

Vender	Size	P/N
MAX	16MB	AKE3D2NKZ00 IC FLASH(8P) MX25L12872FZNI-10G(WS0N)
XMC	16MB	AKE3DGN0X01 IC FLASH(8P) XM25QH128AWIGT(WS0N)
Socket		DFHS08FS046 FP:50951-0084n-v01-8p-socket

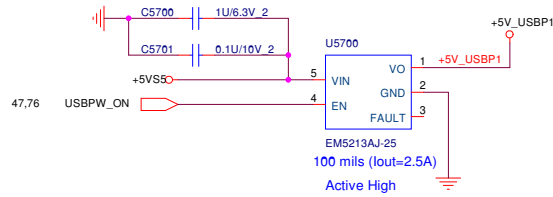


PROJECT : G7H
 Quanta Computer Inc.

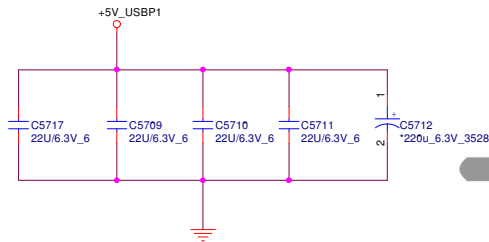
Size Custom Document Number
Flash ROM (BIOS,EC)
 Date: Friday, August 21, 2020 Sheet 56 of 106

15,19,61,63,76,86,87,88,89,91,93,96,98,102,105

+5VSS

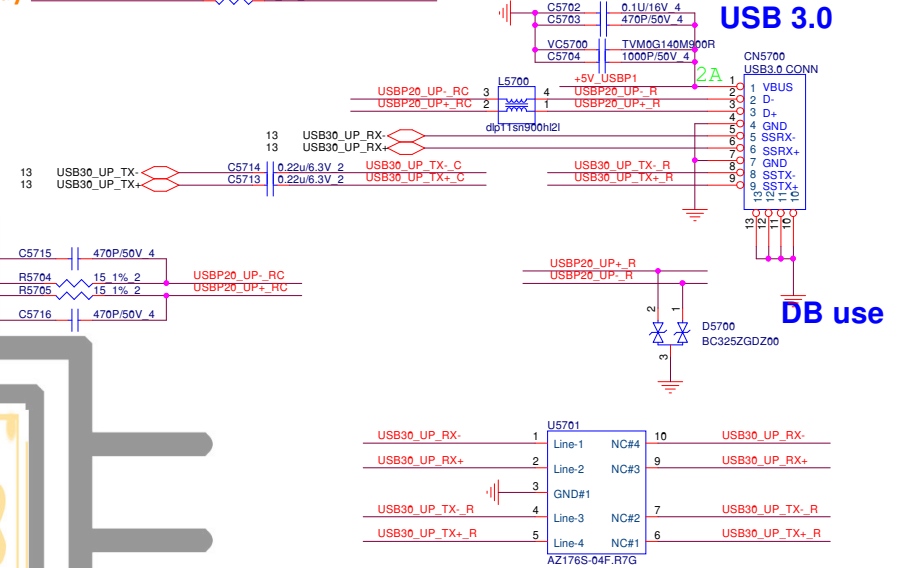


Active High




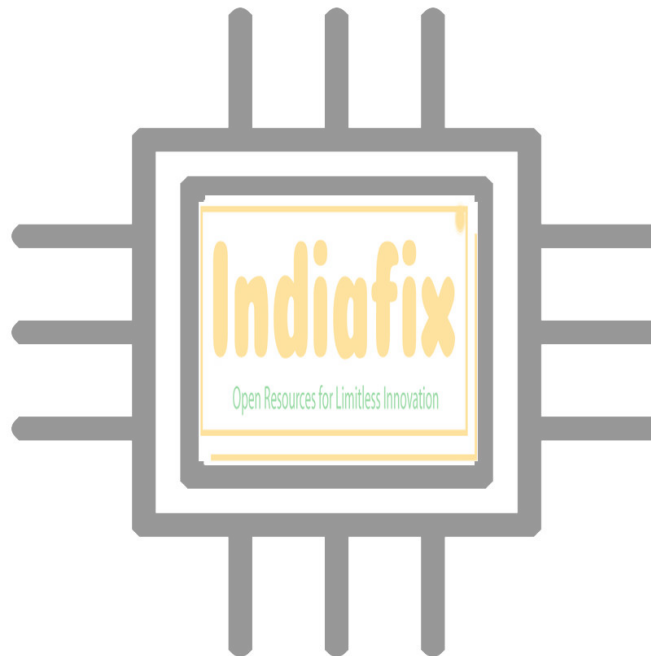
Co-layer USB20_UP- RC USB20_UP+ RC


Co-layer USB30_UP- TX- C USB30_UP- TX+ C

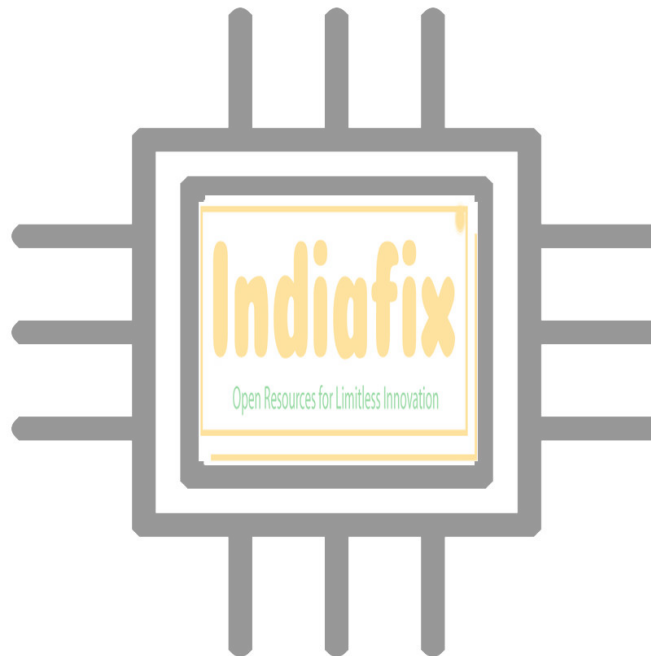

Indiafix
Open Resources for Limitless Innovation


BC003310Z00 --> MESD3310 DFN2510 3.3V

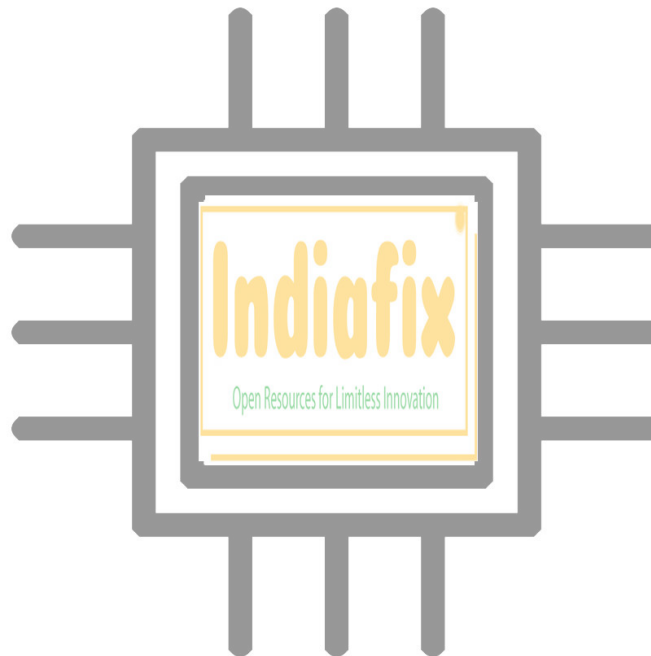
	PROJECT : G7H Quanta Computer Inc.		
	Size	Document Number USB TYPEA	Rev 1A
Date: Friday, August 21, 2020 Sheet 57 of 106			




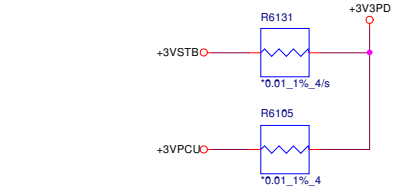
 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number Burnside Bridge Re-timer 1	Rev 1A
	Date: Friday, August 21, 2020	Sheet 58 of 106	



 NB5	PROJECT : G7H Quanta Computer Inc.				
	Size	Document Number			Rev
		TBT2			1A
	Date: Friday, August 21, 2020	Sheet	59	of	106



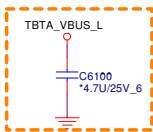
 NB5	PROJECT : G7H Quanta Computer Inc.				
	Size	Document Number			Rev
		TBT3			1A
	Date: Friday, August 21, 2020	Sheet	60	of	106



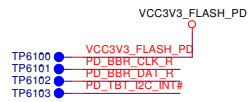
For PD debug

TP6107	PDEC_I2C_SDA
TP6108	PDEC_I2C_SCL
TP6109	SMB_PCH_DAT
TP6110	SMB_PCH_CLK
TP6111	PDEC_I2C_IRQ#
TP6112	TBT_I2C_INT#

Placed together

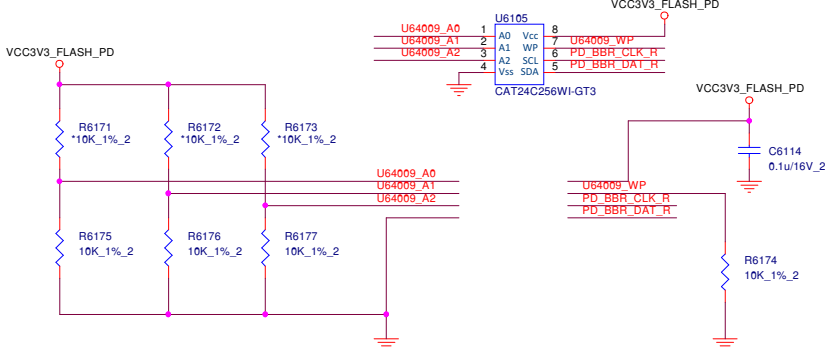


TP6100~TP6103 need place to All BOT or All TOP

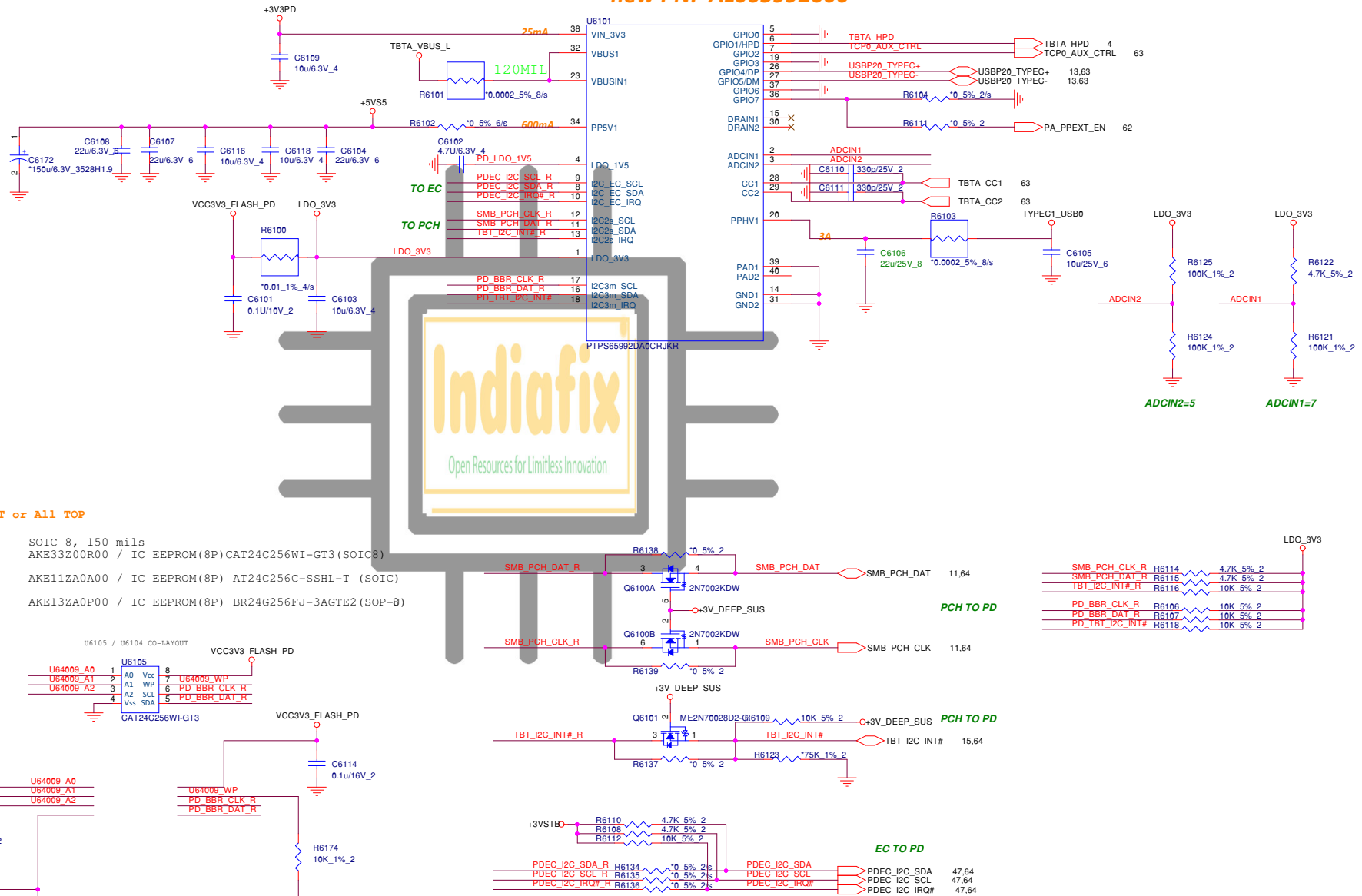


SOIC 8, 150 mils
AKE33Z00R00 / IC EEPROM(8P) CAT24C256WI-GT3 (SOIC8)
AKE11ZA0A00 / IC EEPROM(8P) AT24C256C-SSHL-T (SOIC)
AKE13ZA0P00 / IC EEPROM(8P) BR24G256FJ-3AGTE2 (SOP-8)

U6105 / U6104 CO-LAYOUT

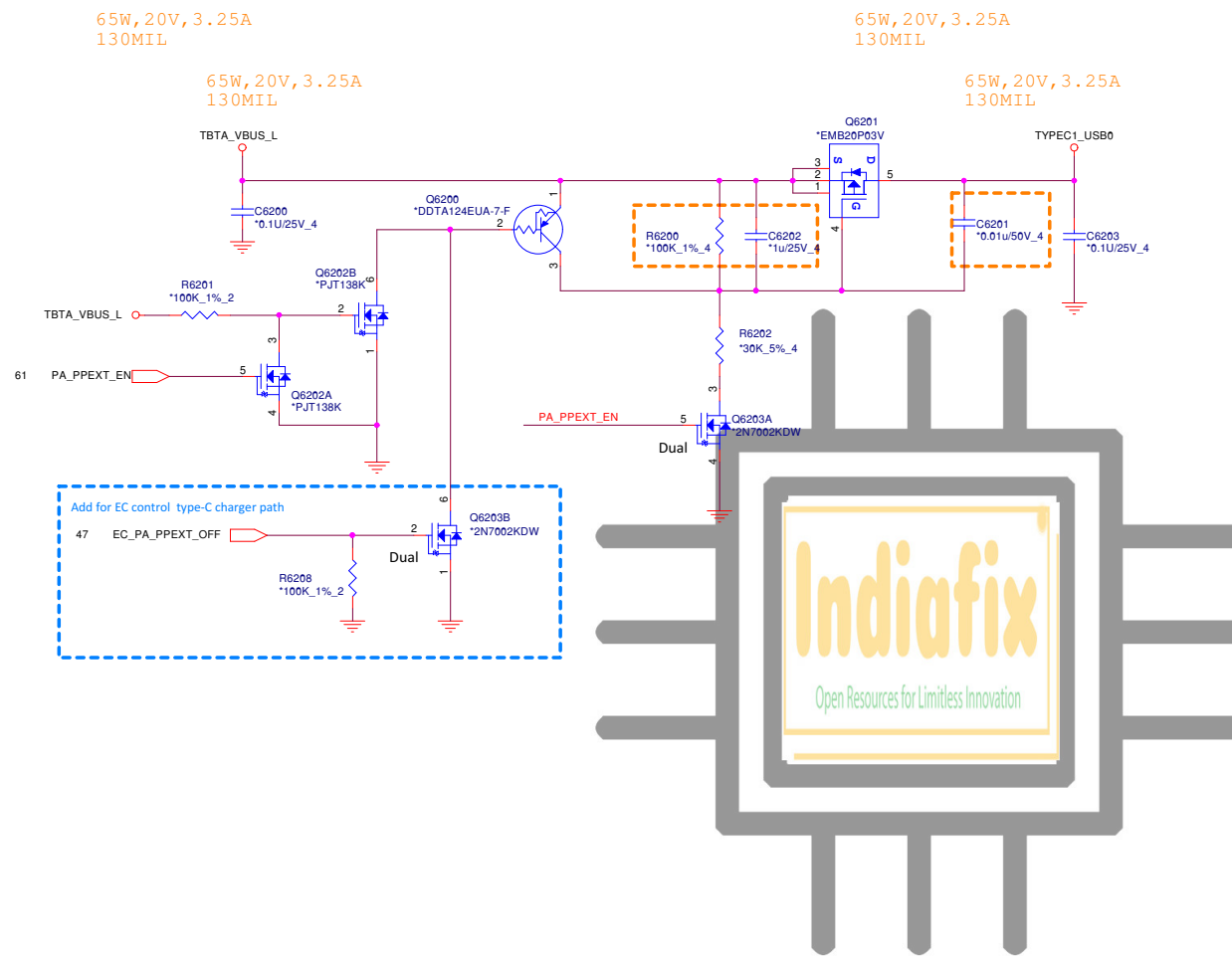


new PN: AL065992000



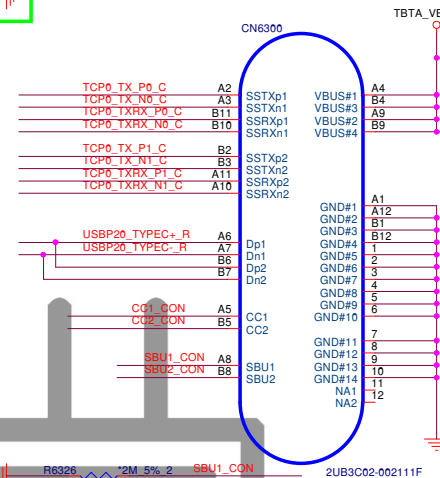
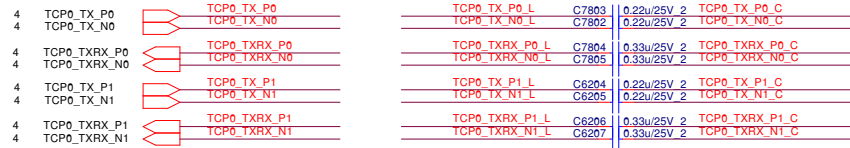
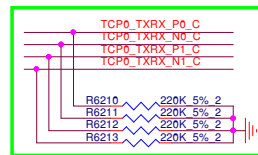
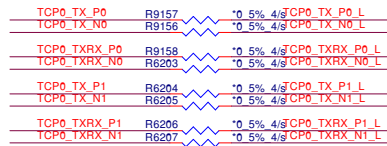
PROJECT : ABLE_TGL_13
Quanta Computer Inc.

Size Custom Document Number TPS65992D
Date: Friday, August 21, 2020 Sheet 61 of 106 Rev 1A



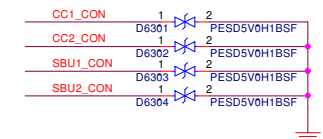
65W, 20V, 3.25A
130MIL

USB Type-C

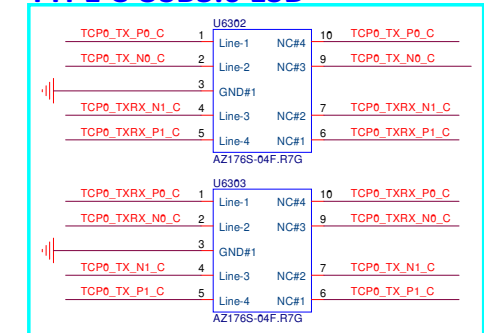


01/15 Change to DFHS24FR423 & SWAP

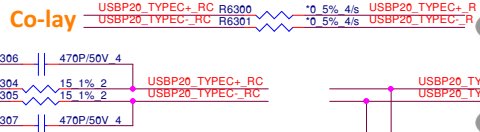
WAIT ESD FP



TYPE C USB3.0 ESD

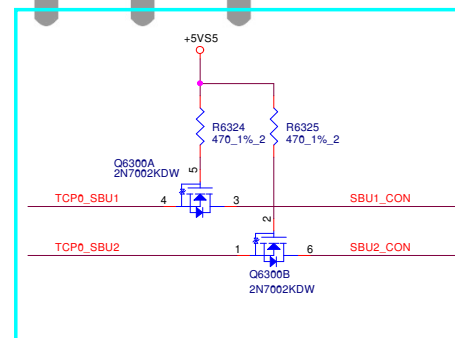
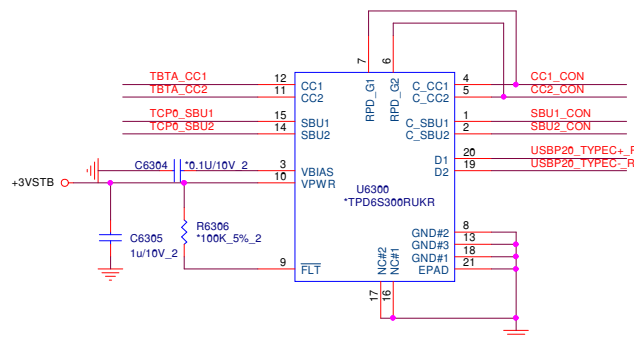


Co-lay



CC/SBU Overvoltage protection

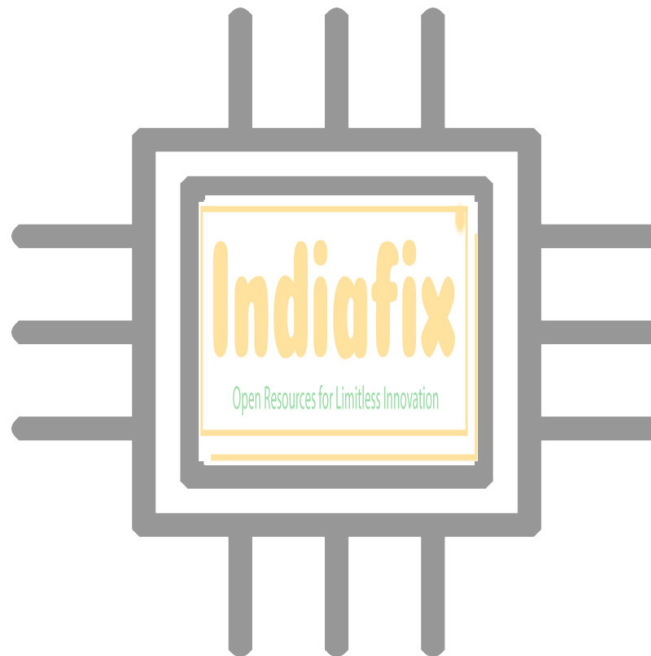
AUX MUX IC need check




0207 Add



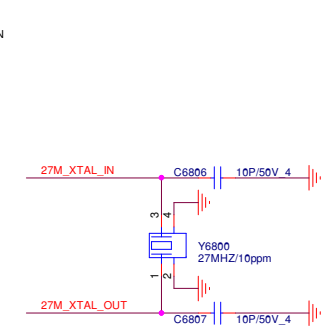
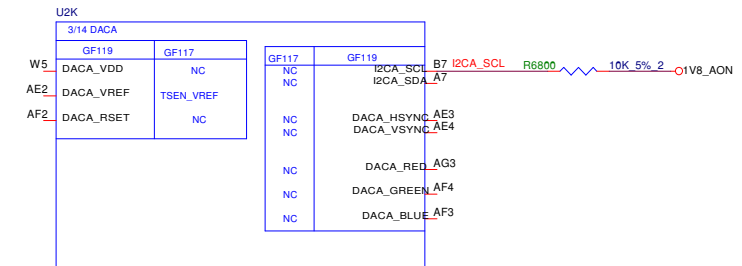
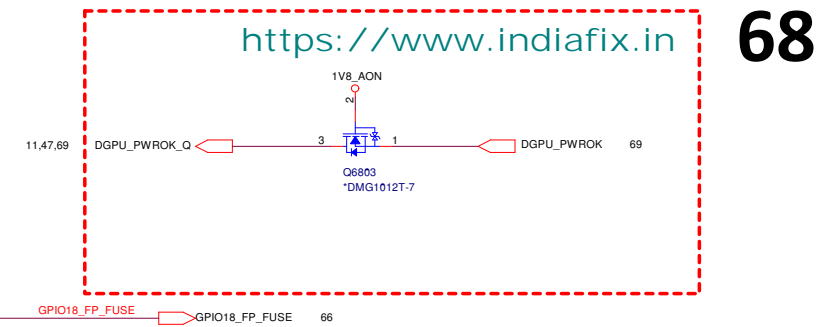
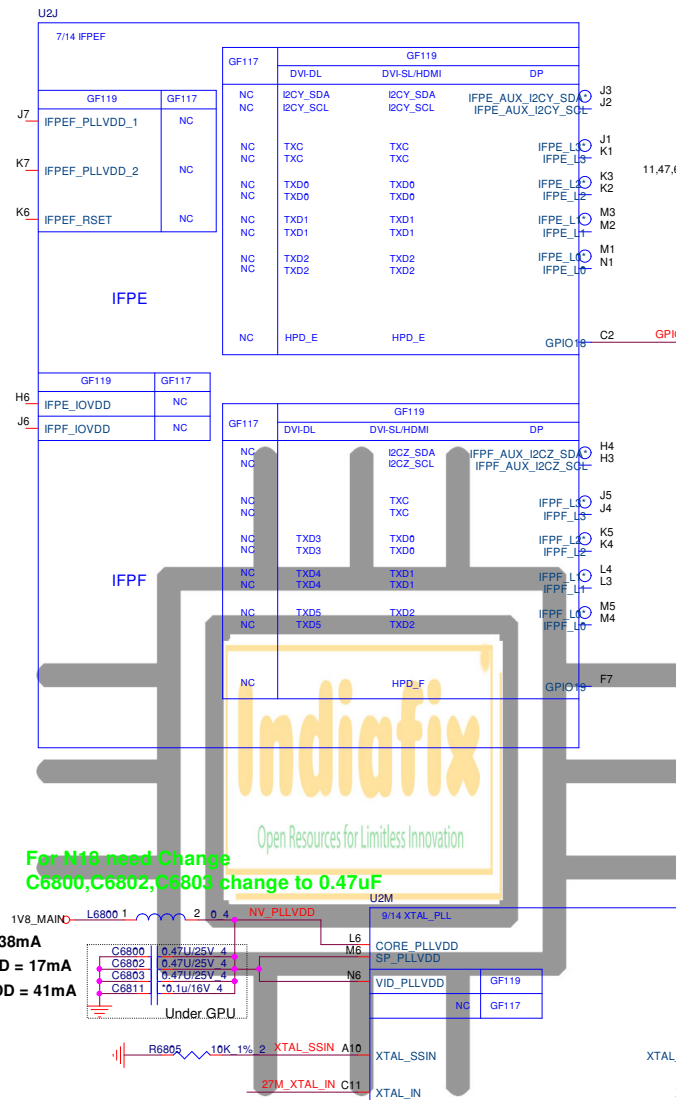
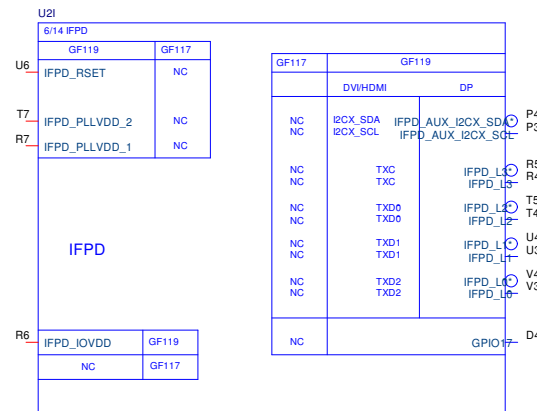
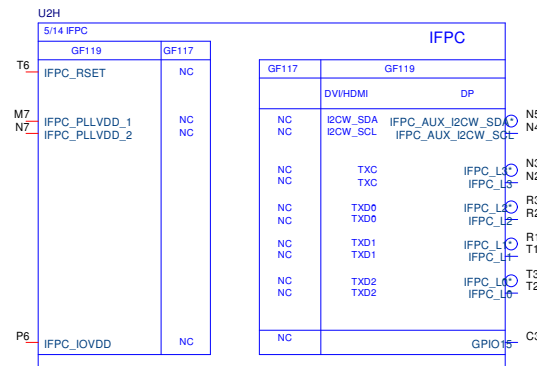
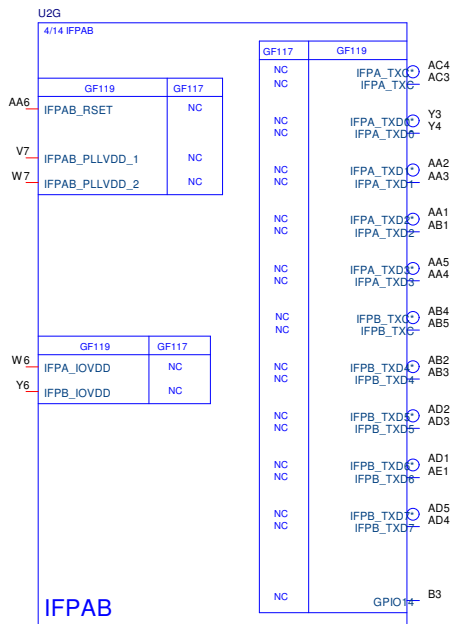
5 | <https://www.indiafix.in>

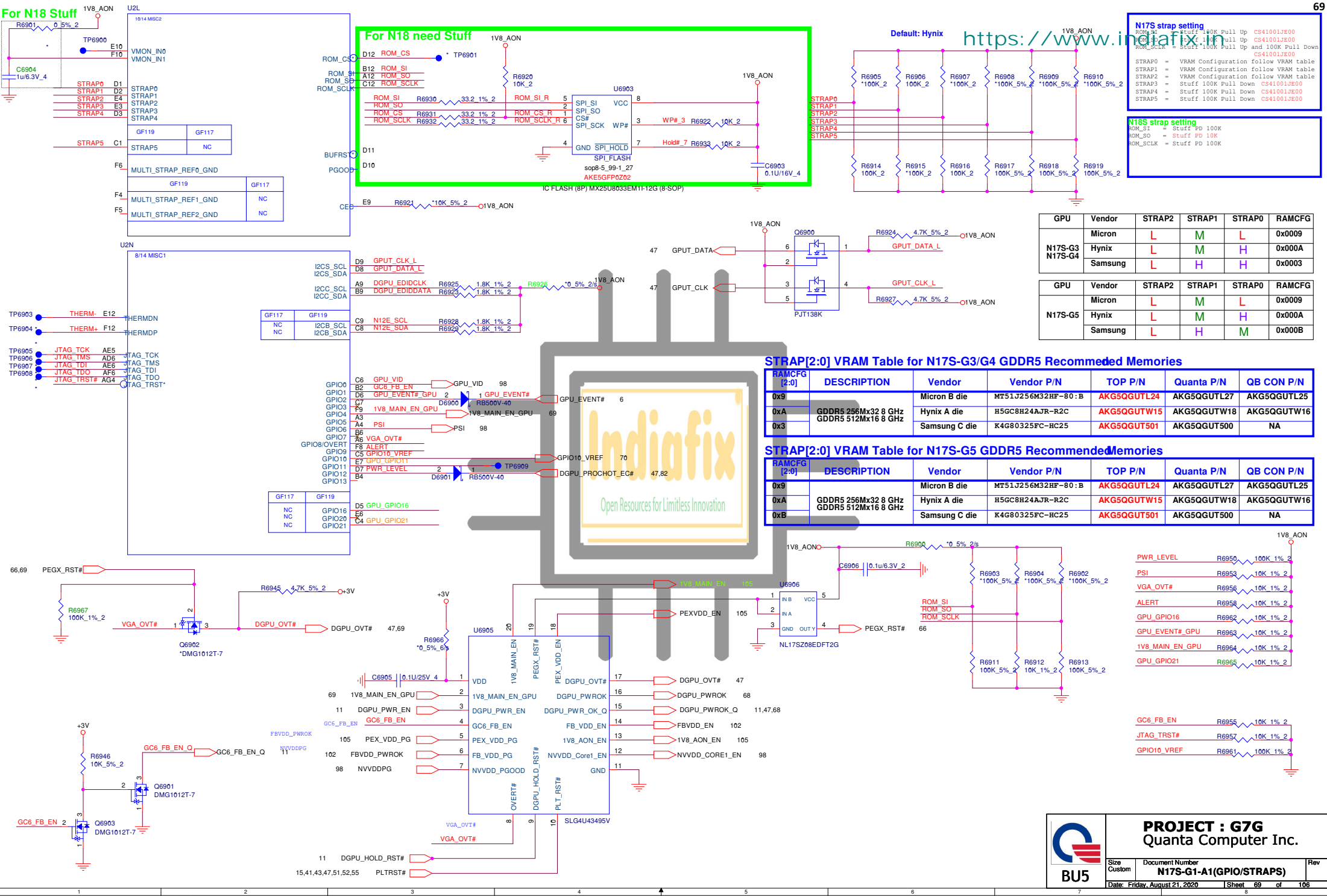


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	Size	Document Number			Rev 1A
	USB TypeC #3				
Date: Friday, August 21, 2020		Sheet 65 of 106			









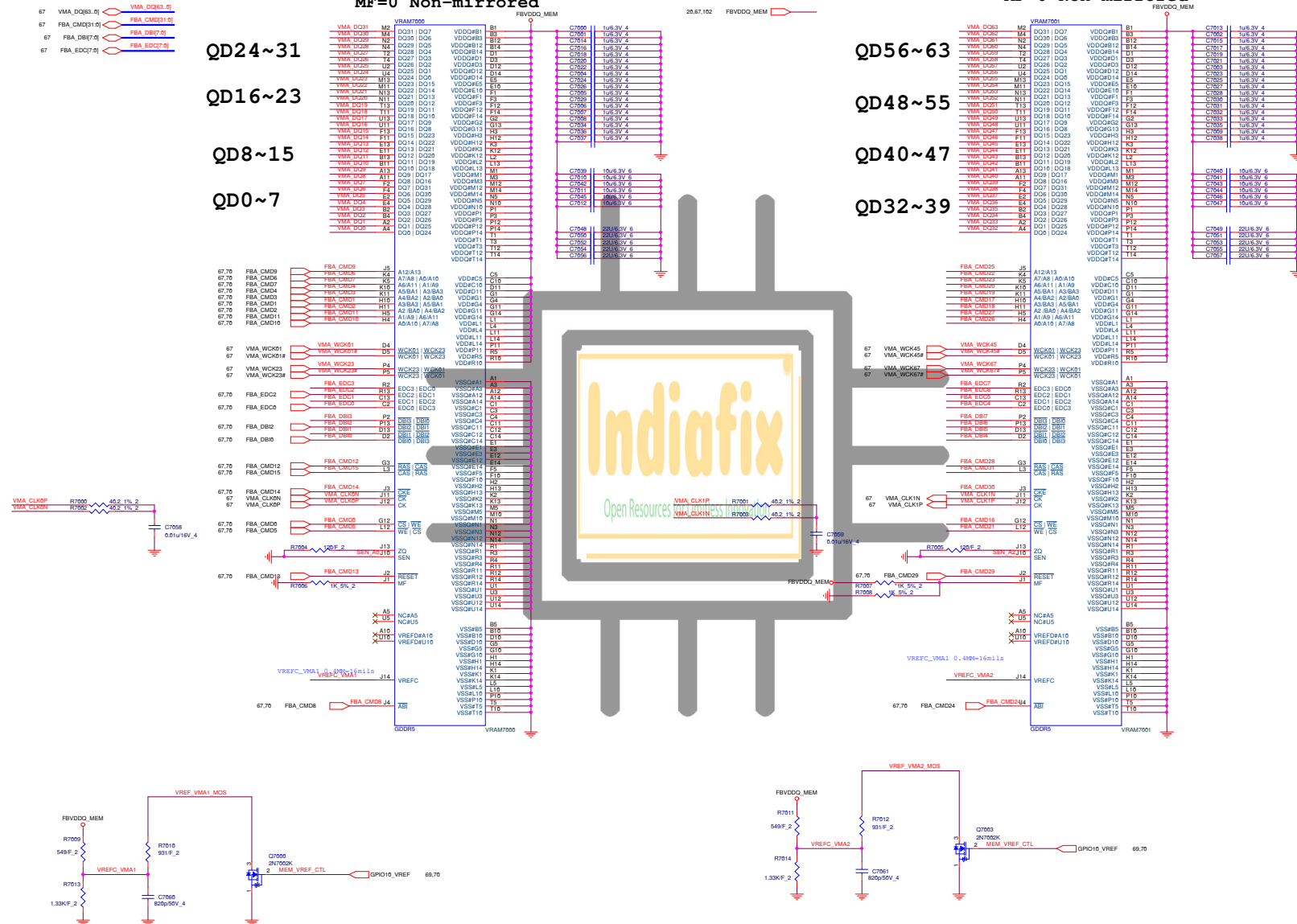
Channel A
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MF=0 Non-mirrored

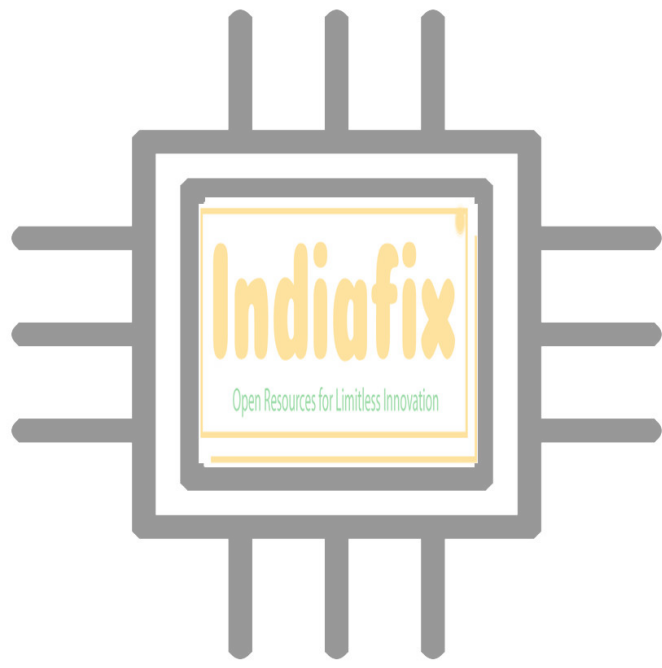
CHANNEL A: 2G GDDR5

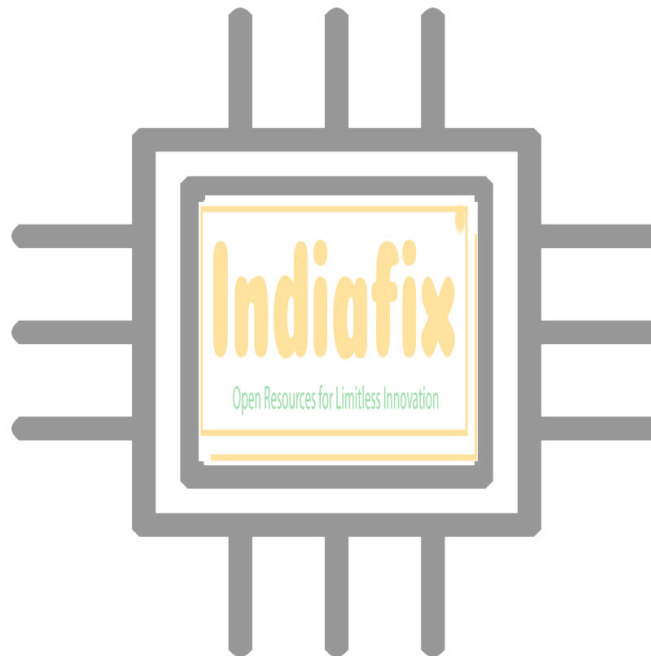
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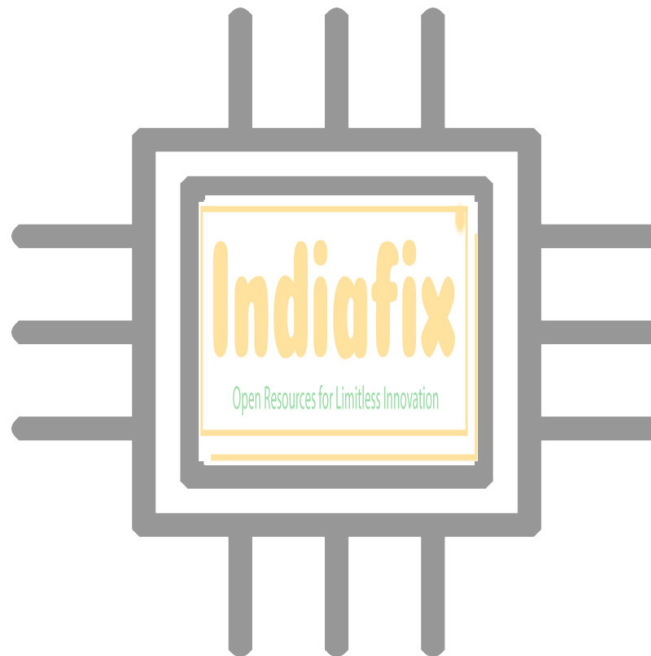



GDDR5	Hynix H5GC8H24MJR-R0C	AKG5QGUTW04	AKG5QGUTW03
256 x 32	Micron MT51J256M32HF-70:A	AKG5QGUTL07	AKG5QGUTL06
	Samsung K4G80325FD-1C2B	AKG5QGD7509	AKG5QGD7508

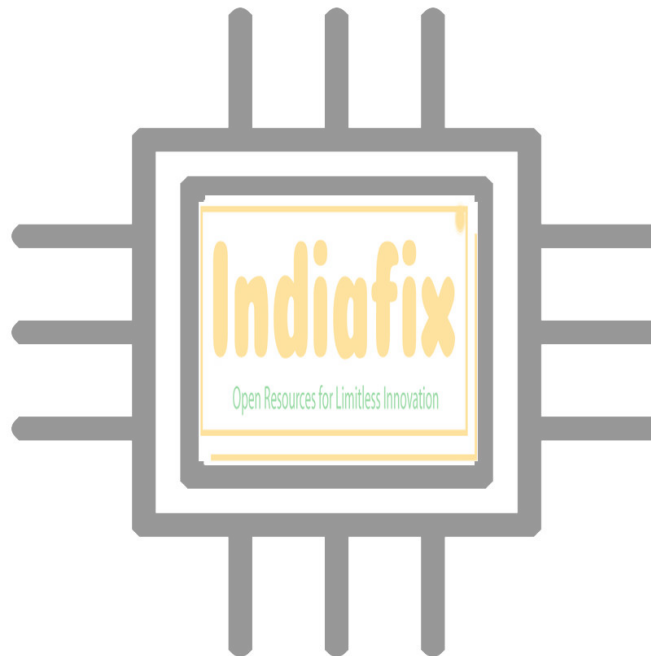





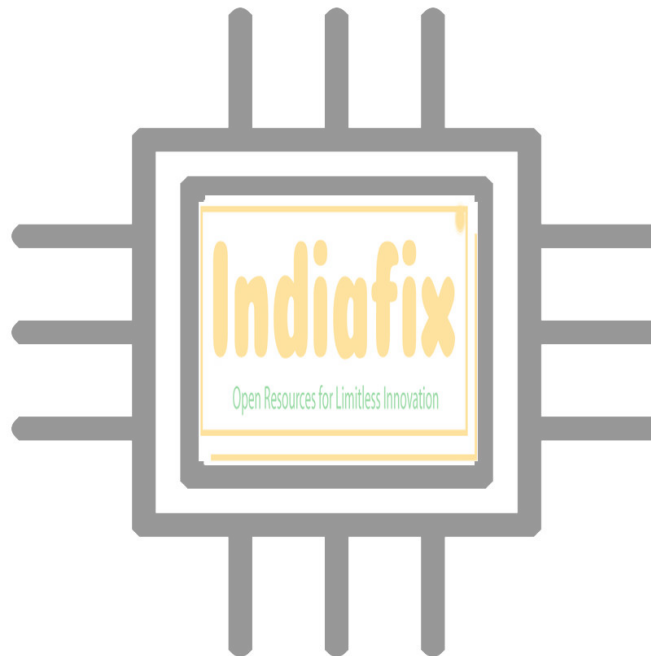
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	Size	Document Number			Rev
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	Date: Friday, August 21, 2020	Sheet	72	of	106




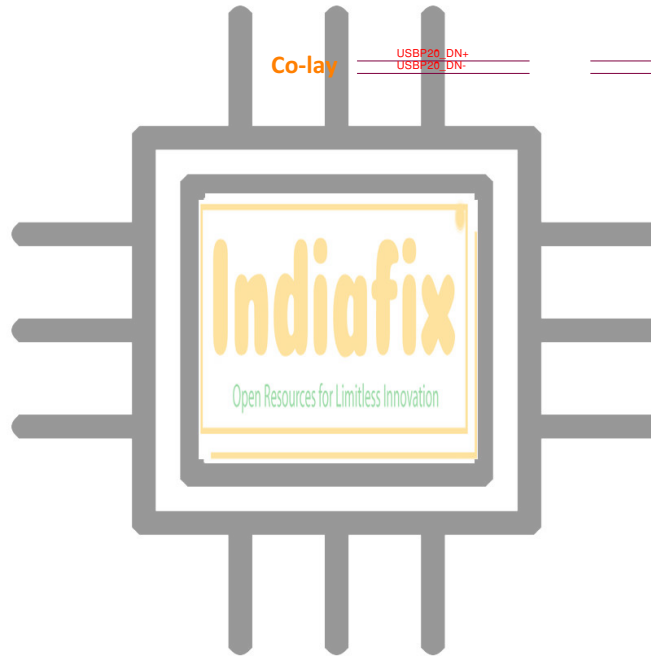
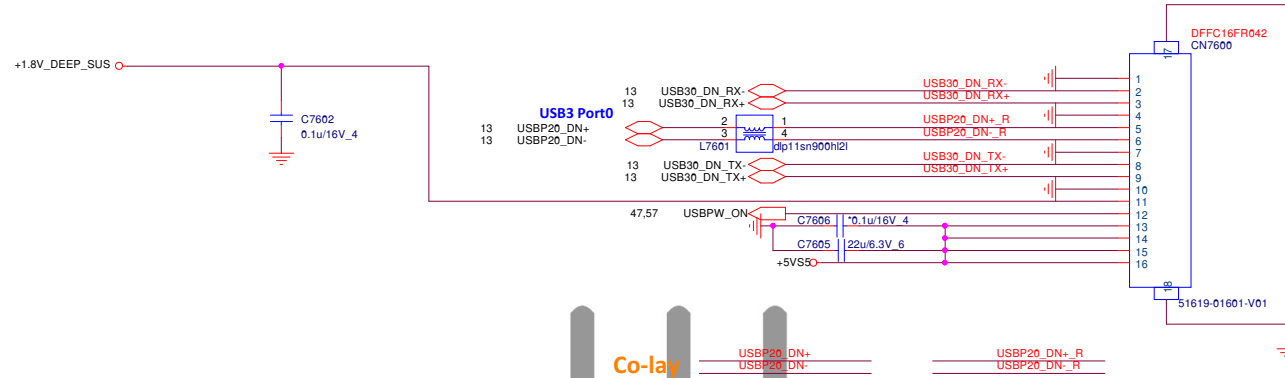
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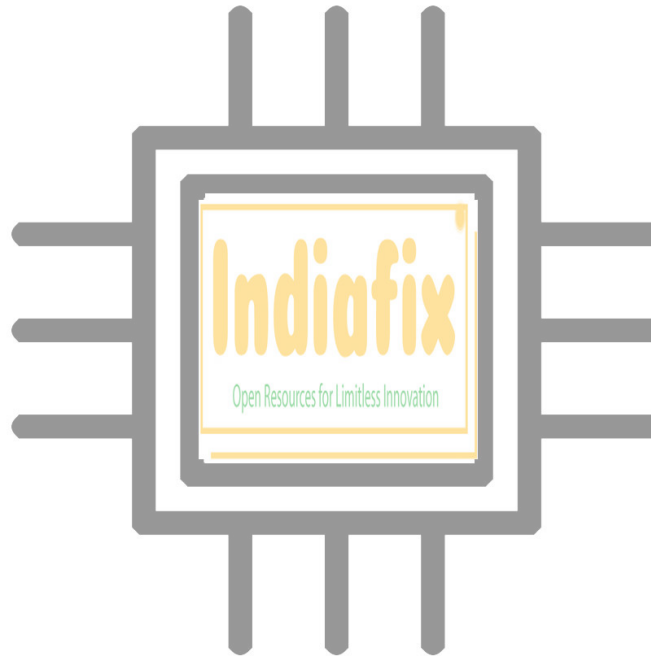



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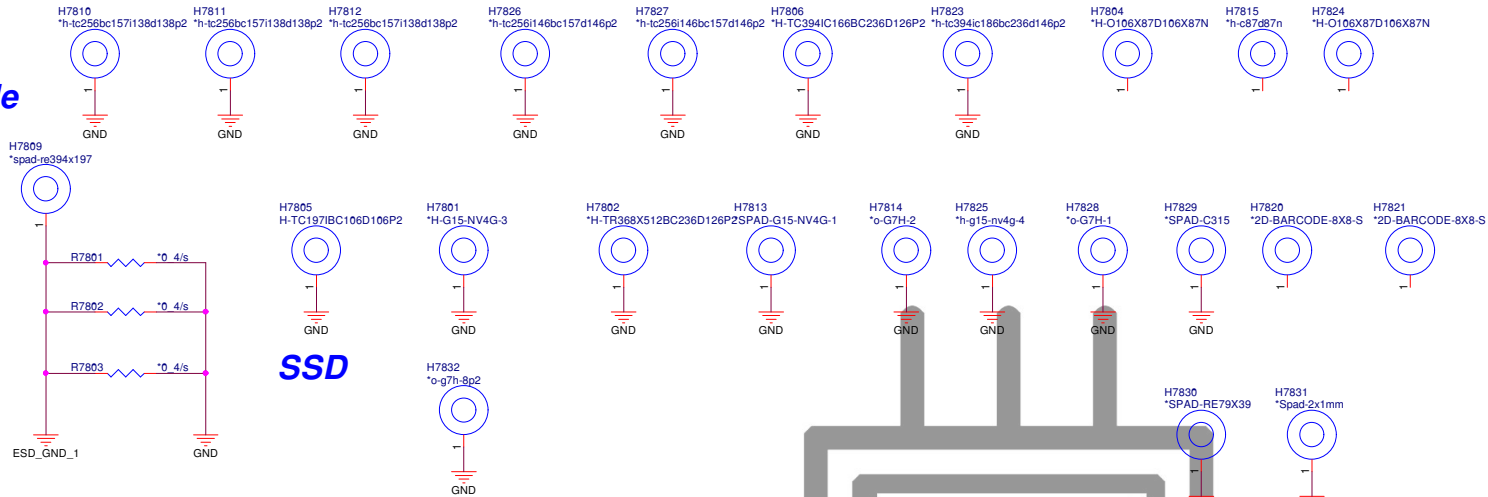
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Date: Friday, August 21, 2020			Sheet	75	of 106





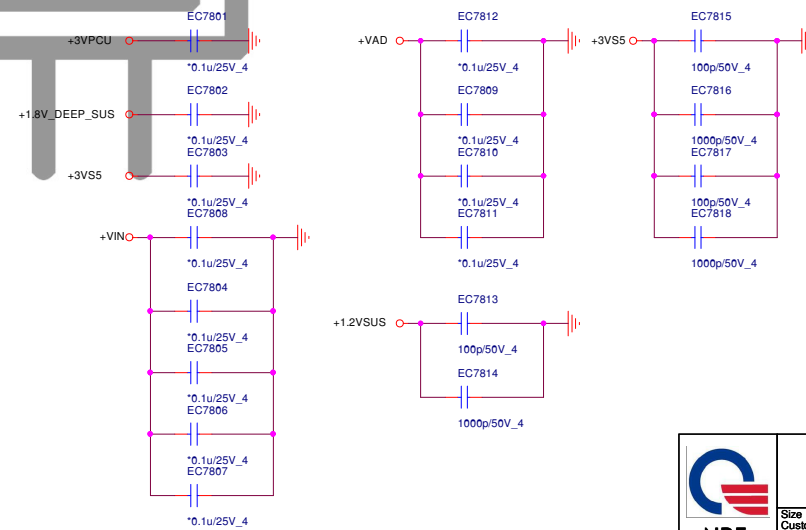
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Size	Document Number			Rev	
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Date: Friday, August 21, 2020			Sheet	77	of 106


Hole

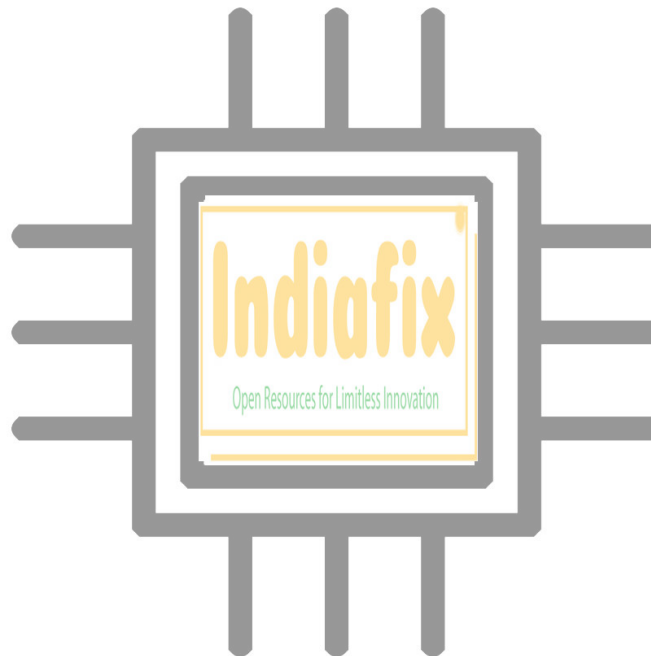



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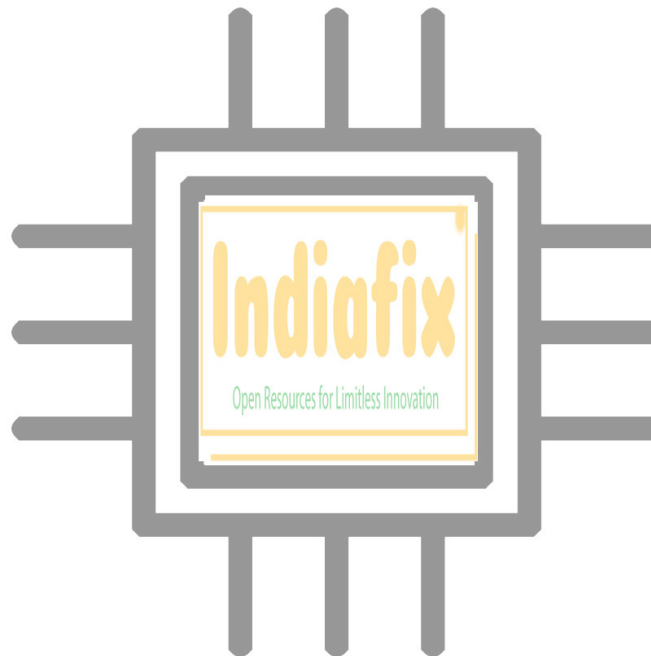
EMI Solution

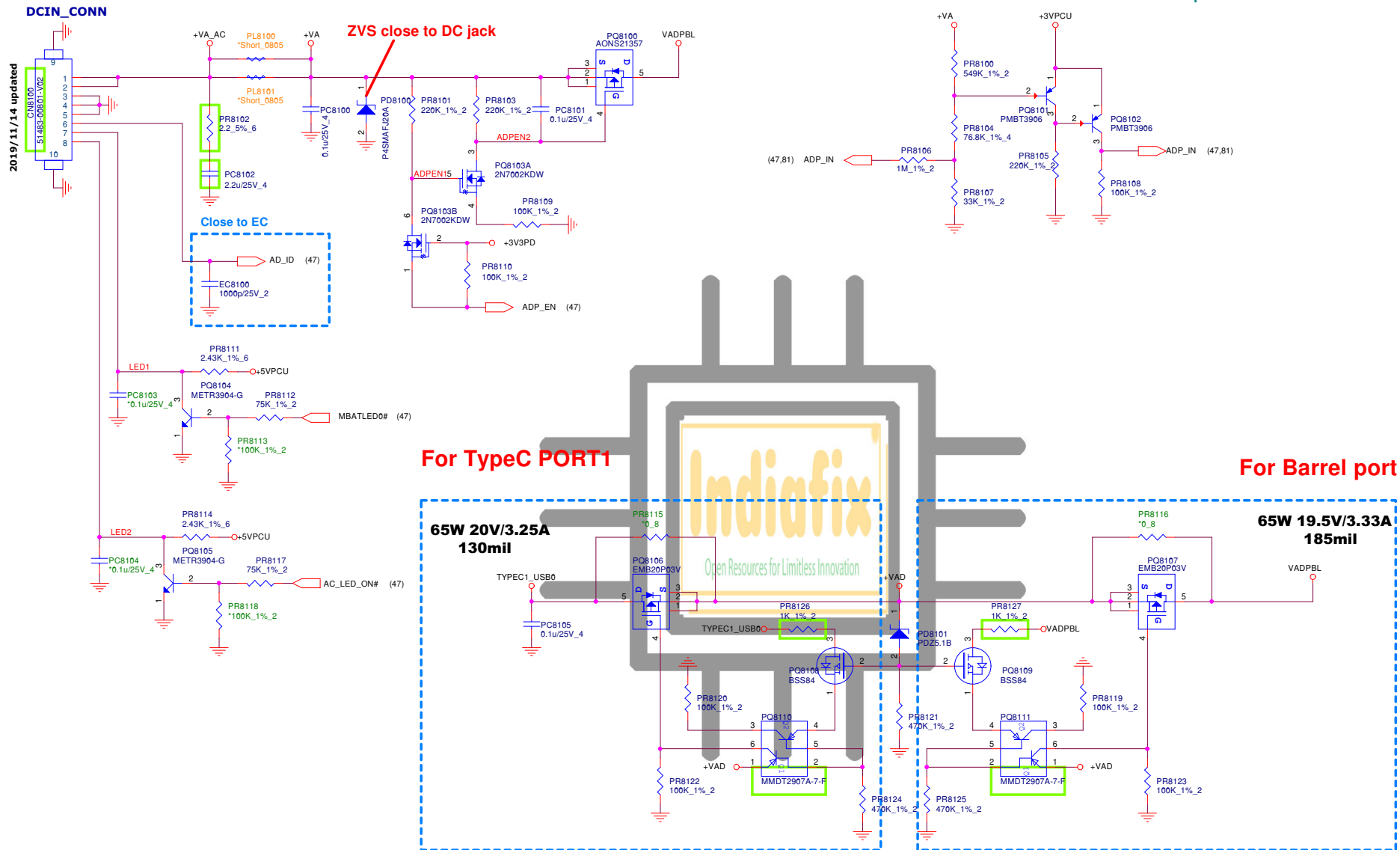


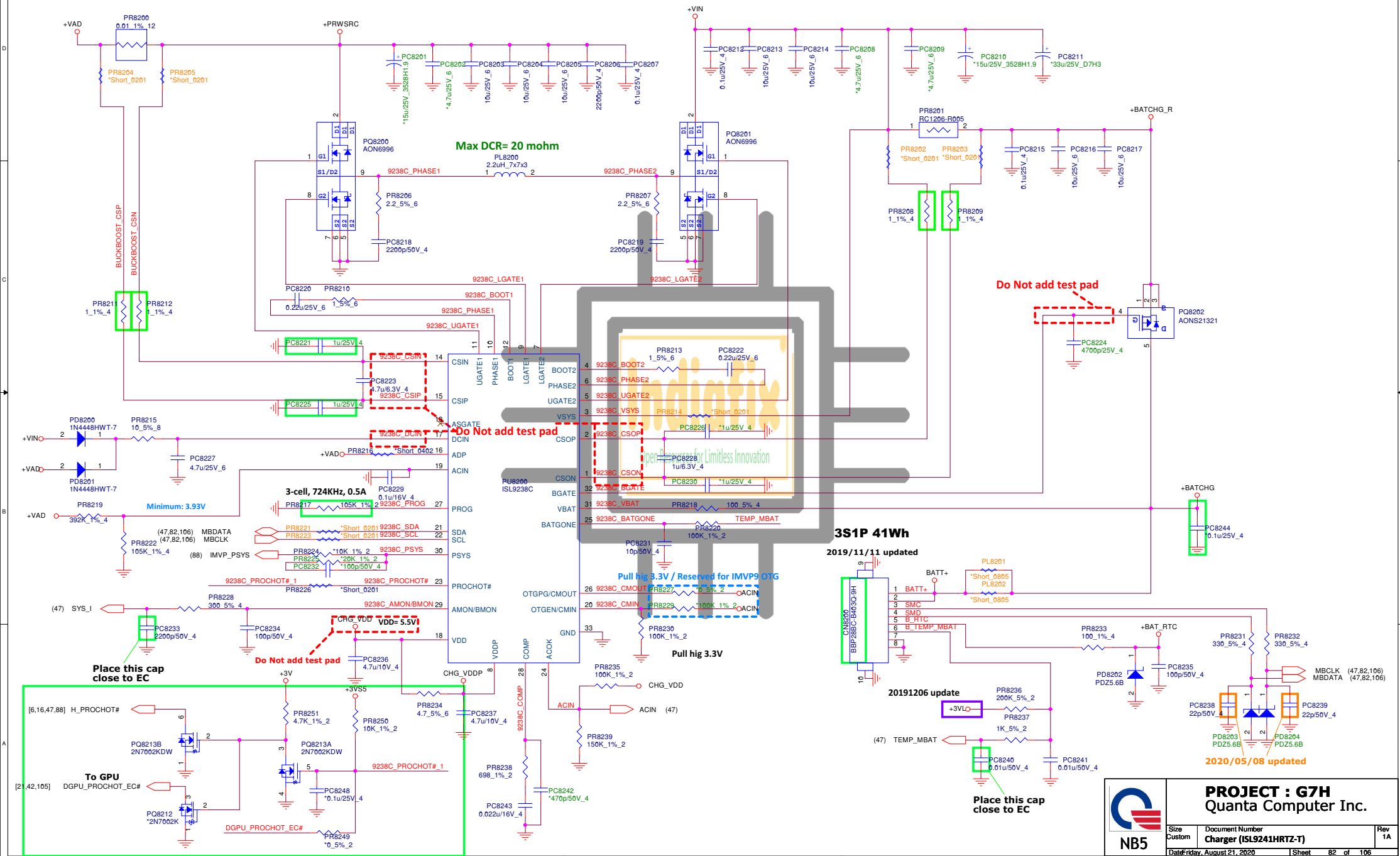
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	Quanta Computer Inc.		
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	Date: Friday, August 21, 2020	Sheet 78 of 106	

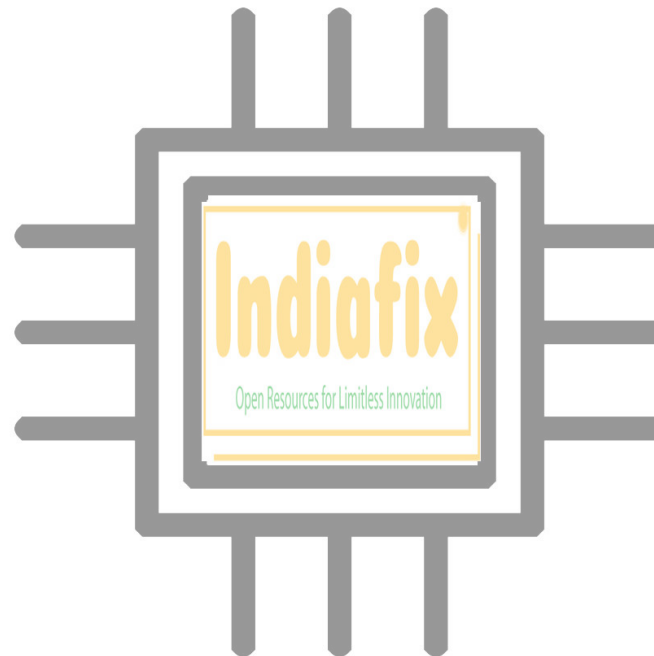



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Date: Friday, August 21, 2020 Sheet 79 of 106			

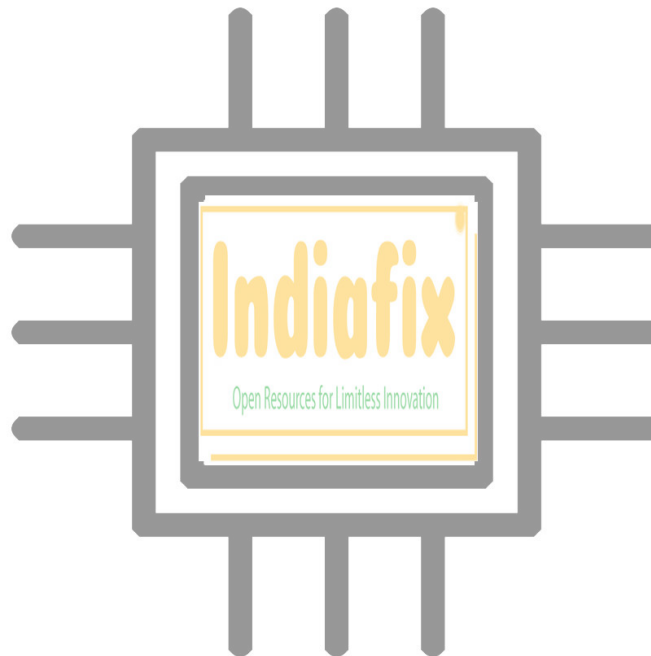





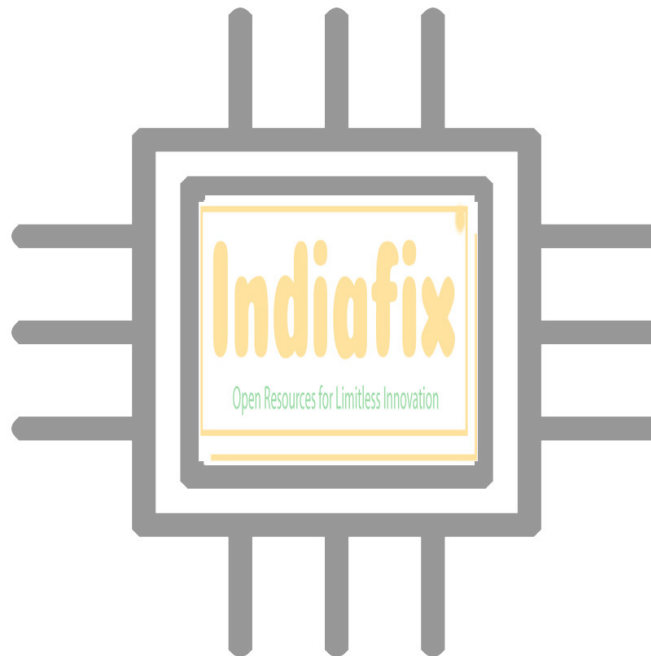





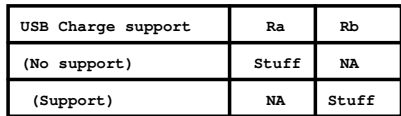
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	Size Custom	Document Number 98 -- reserve page(N.A)	Rev 1A
Date: Friday, August 21, 2020	Sheet	83of	106



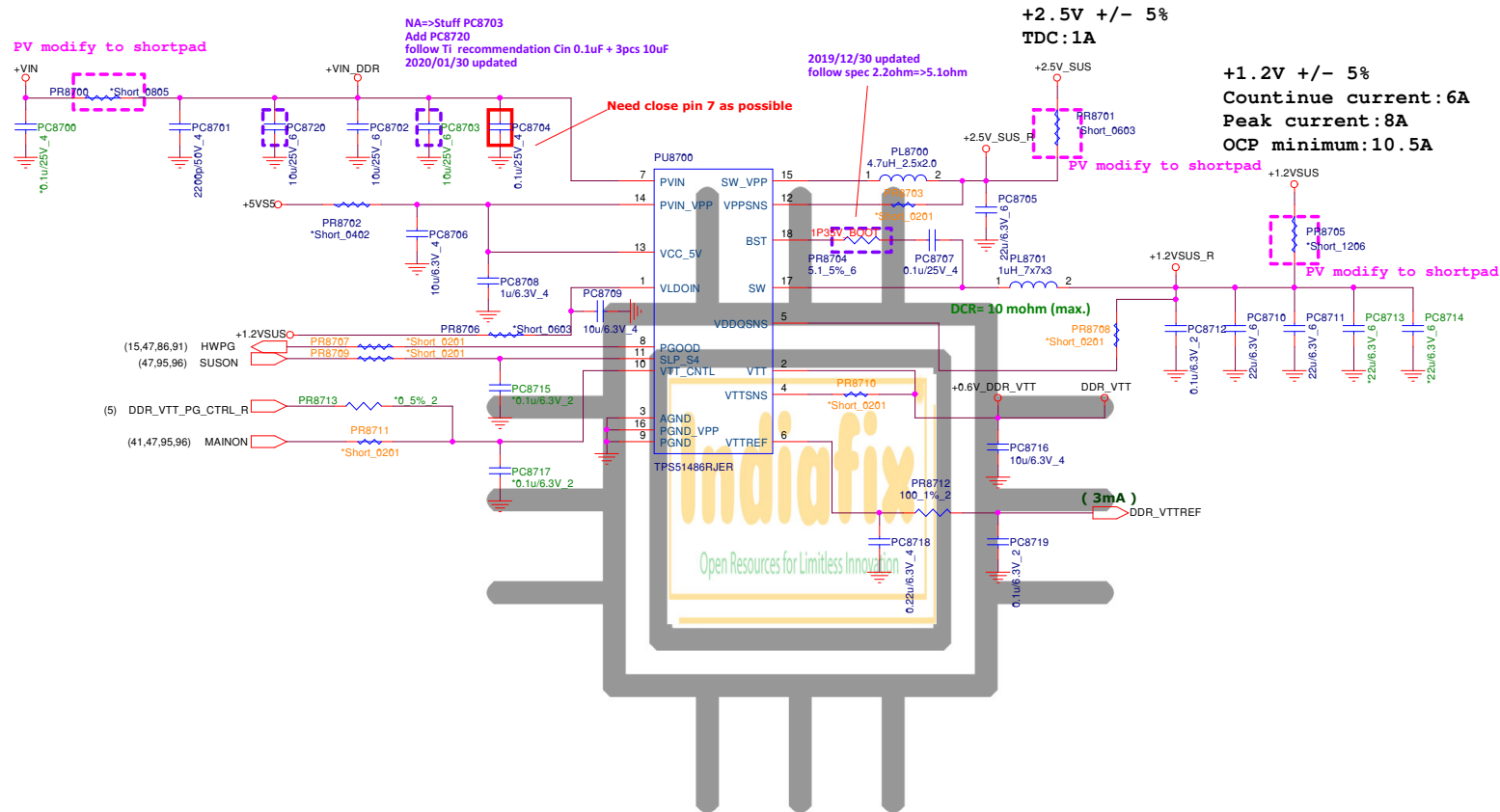
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	Size Custom	Document Number 98 -- reserve page(N.A)	Rev 1A
Date: Friday, August 21, 2020	Sheet	84 of	106

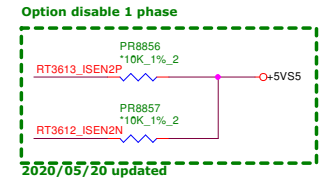
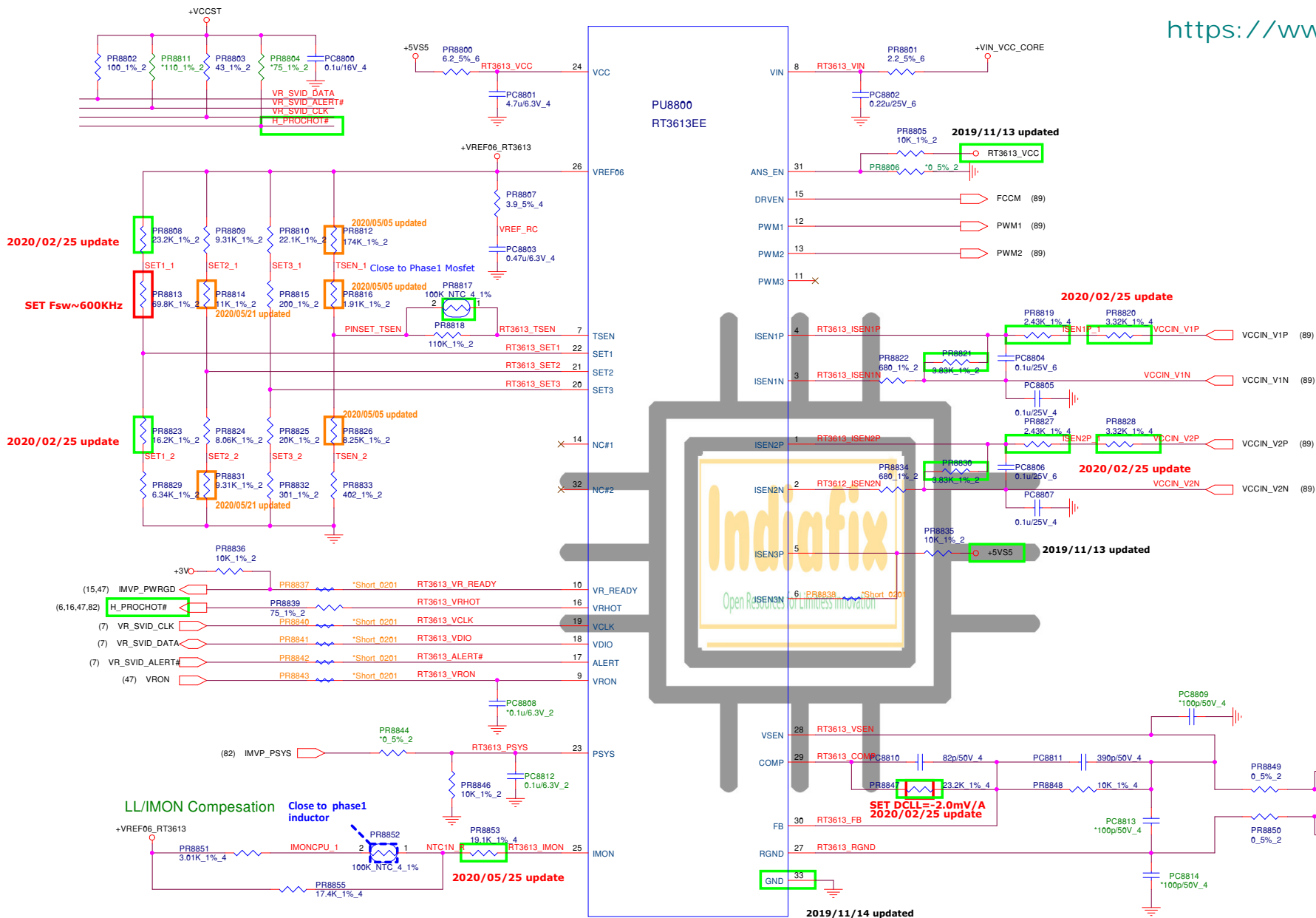


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Date: Friday, August 21, 2020		Sheet	85of 106

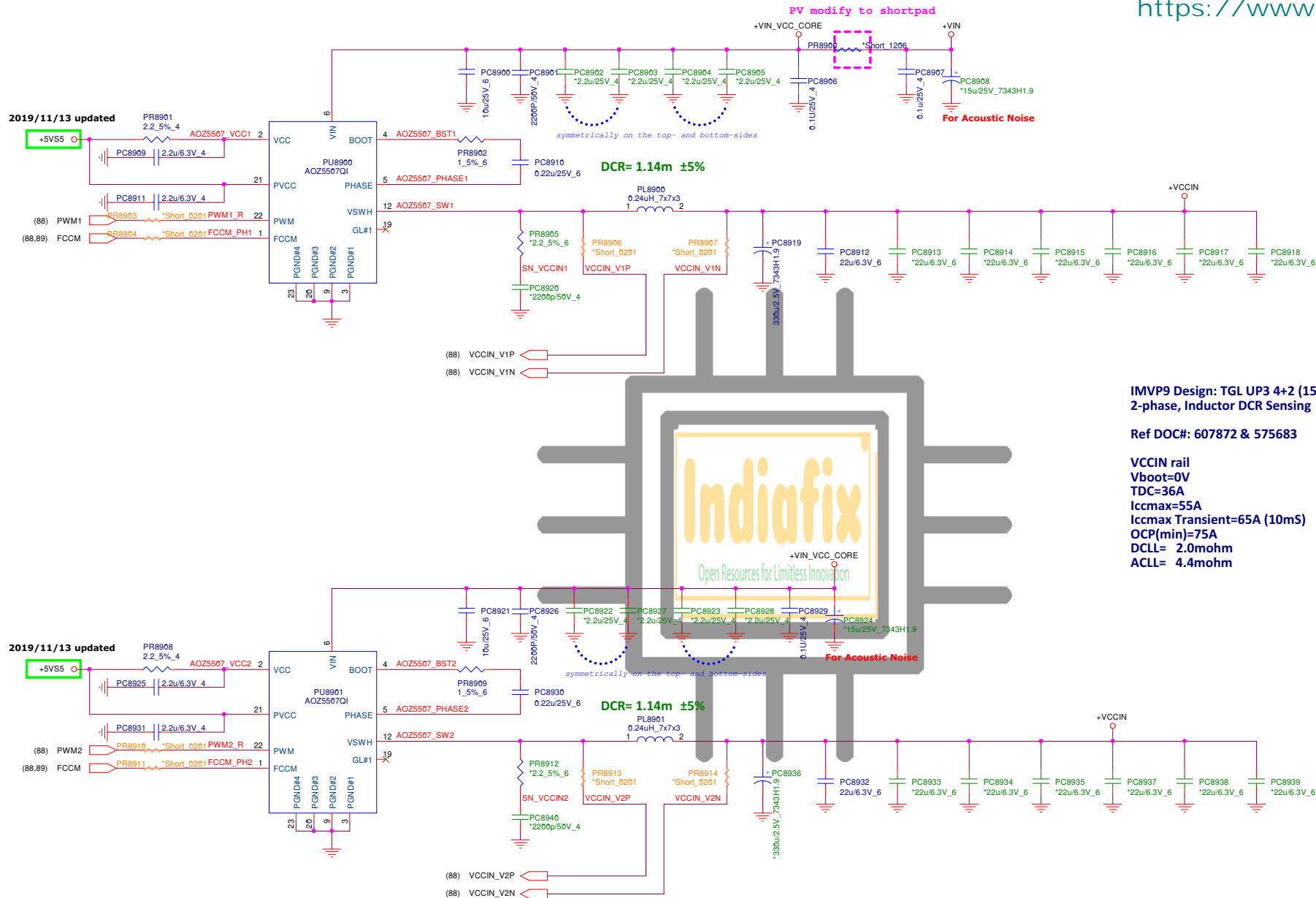



	Re	Rf
5VS5 +5%	Unstuff	604K
5VS5 -5%	1.8M	Unstuff

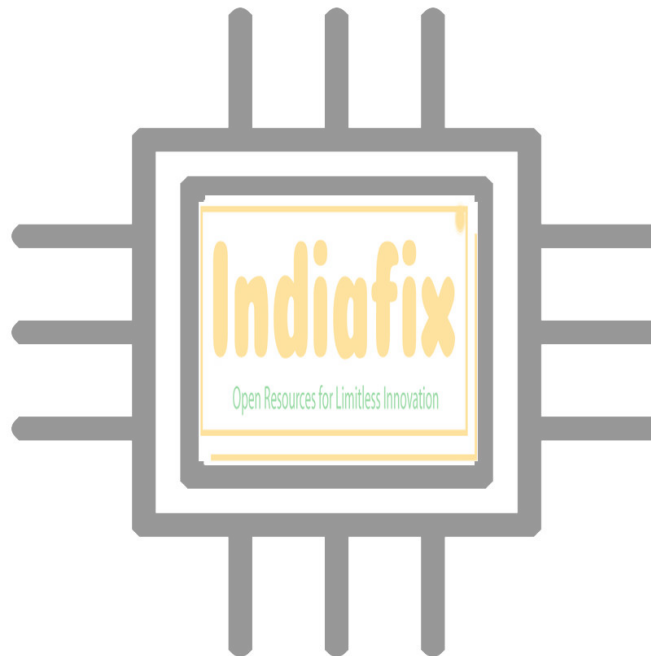





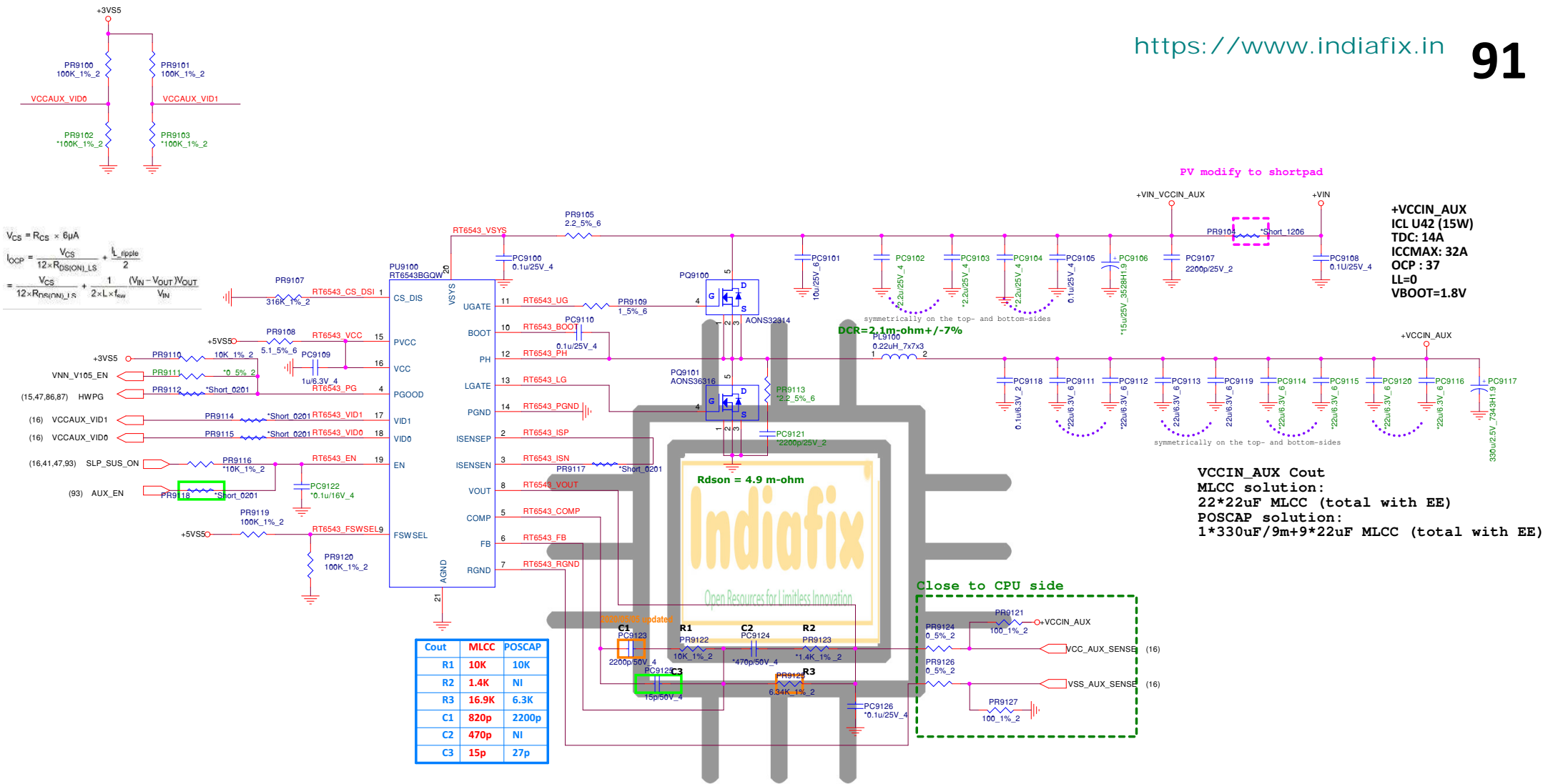
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	Date: Friday, August 21, 2020	Sheet	88 of 106

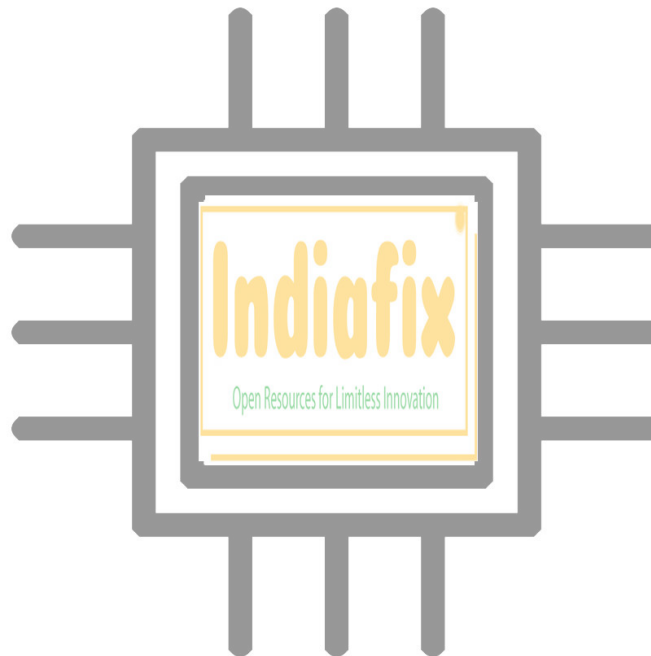



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Date: Friday, August 21, 2020	Sheet	89	of 106

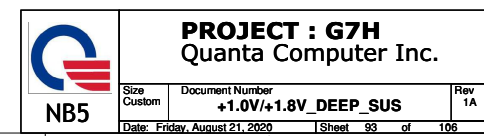


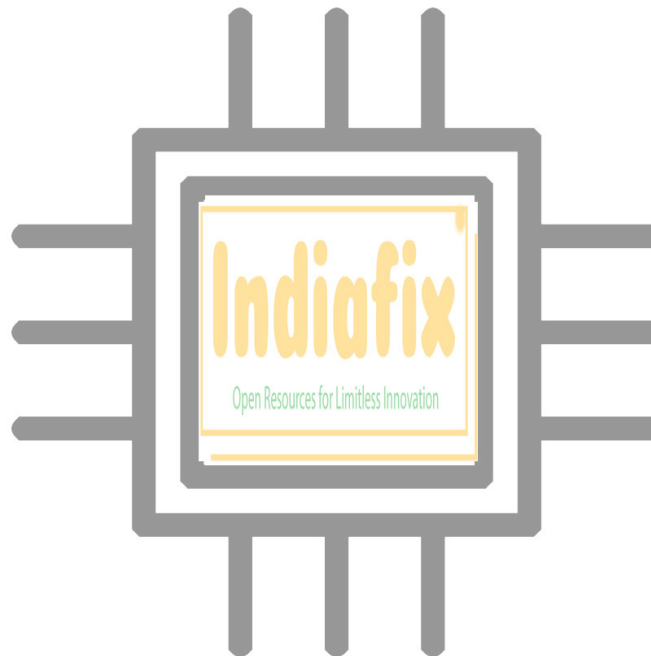
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	Size Custom	Document Number 91 -- GFX Core 2	Rev 1A
	Date: Friday, August 21, 2020		Sheet 90 of 106




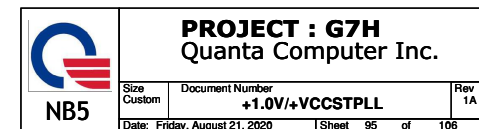


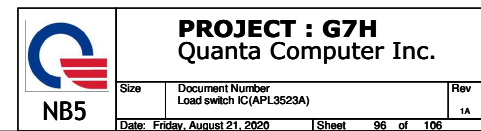
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	Size Custom	Document Number 91 -- GFX Core 2	Rev 1A
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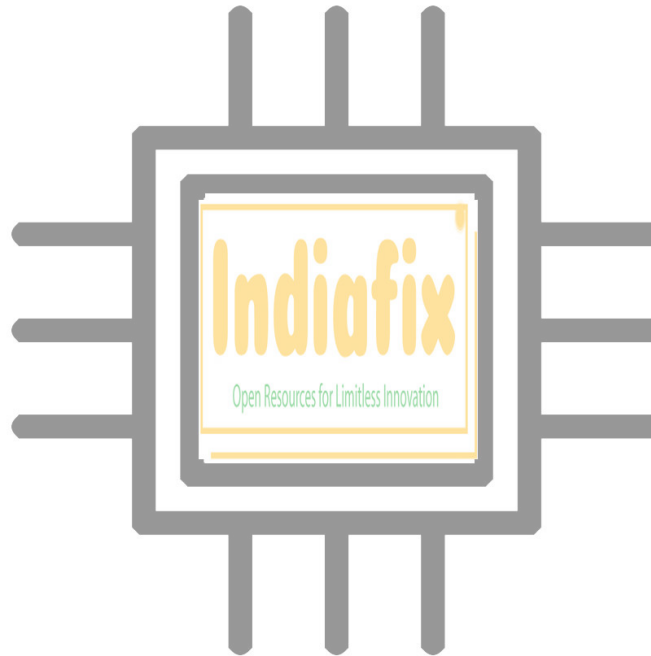
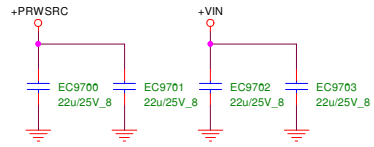


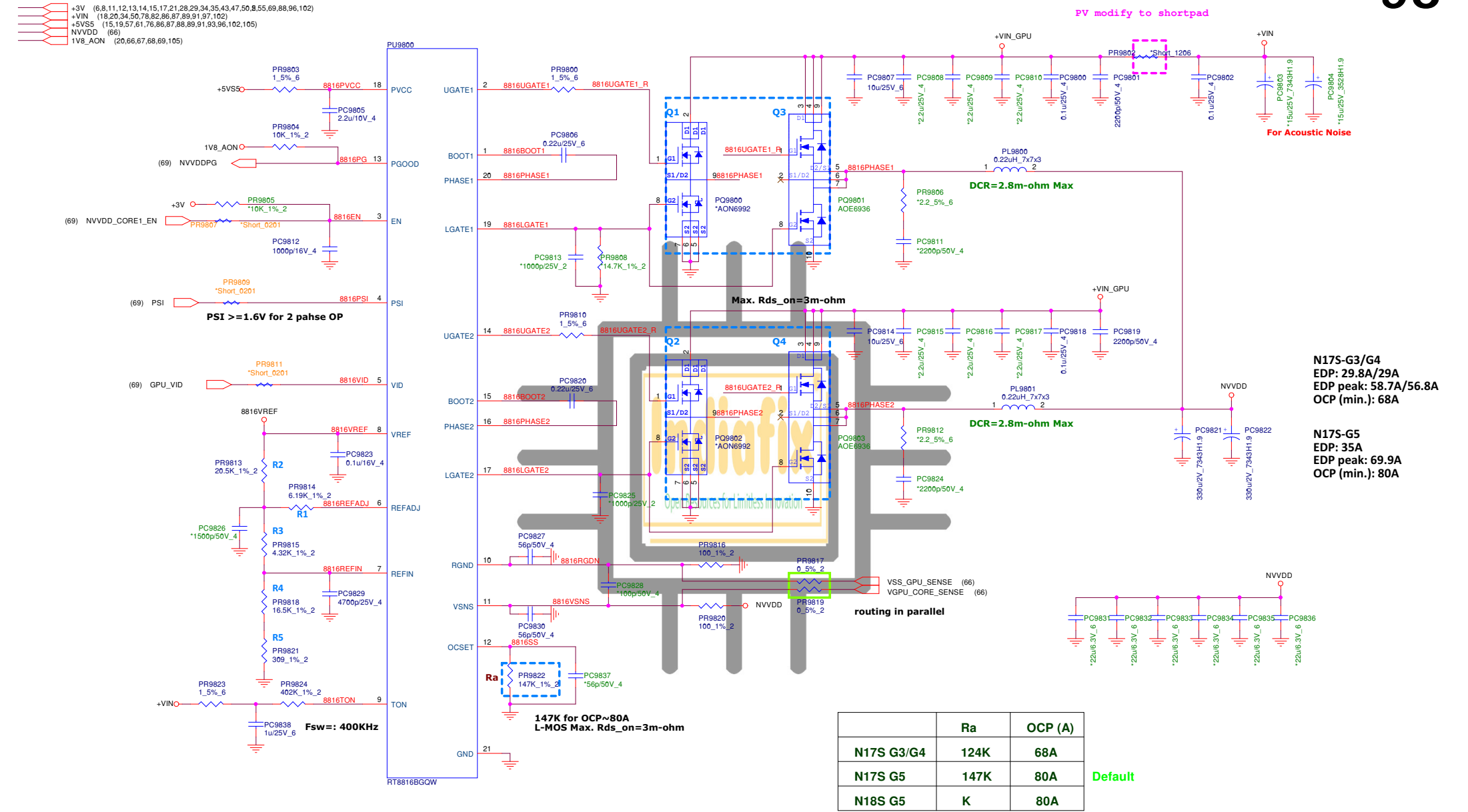
 NB5	PROJECT : G7H Quanta Computer Inc.		
	Size Custom	Document Number 99 -- reserve page	Rev 1A
Date: Friday, August 21, 2020		Sheet	94 of 106





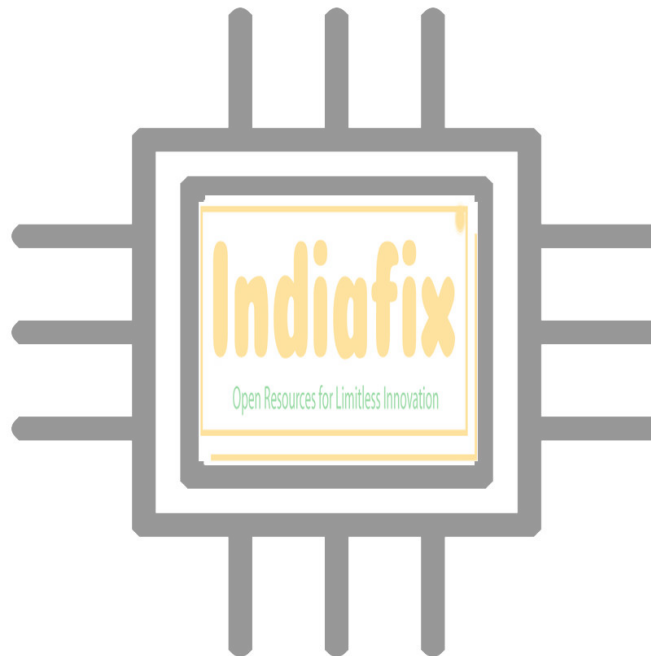
Reserve for ISEN




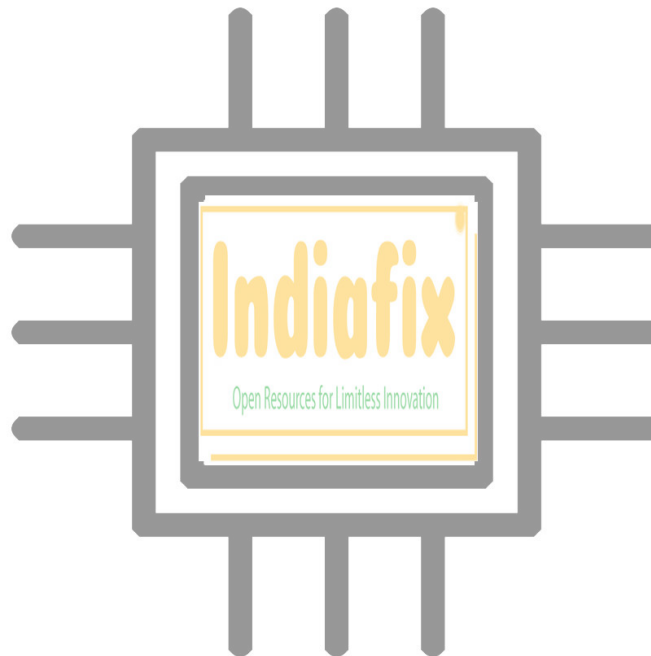



N17S-G3/G4
EDP: 29.8A/29A
EDP peak: 58.7A/56.8A
OCP (min.): 68A

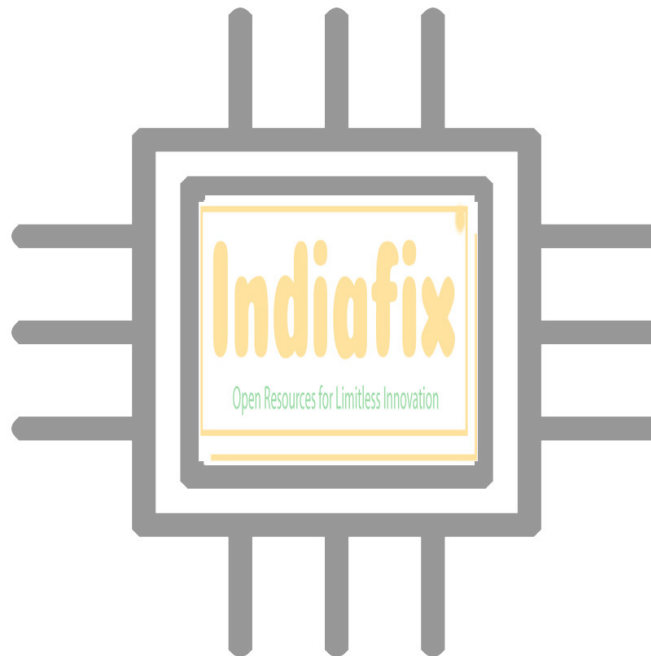
N17S-G5
EDP: 35A
EDP peak: 69.9A
OCP (min.): 80A




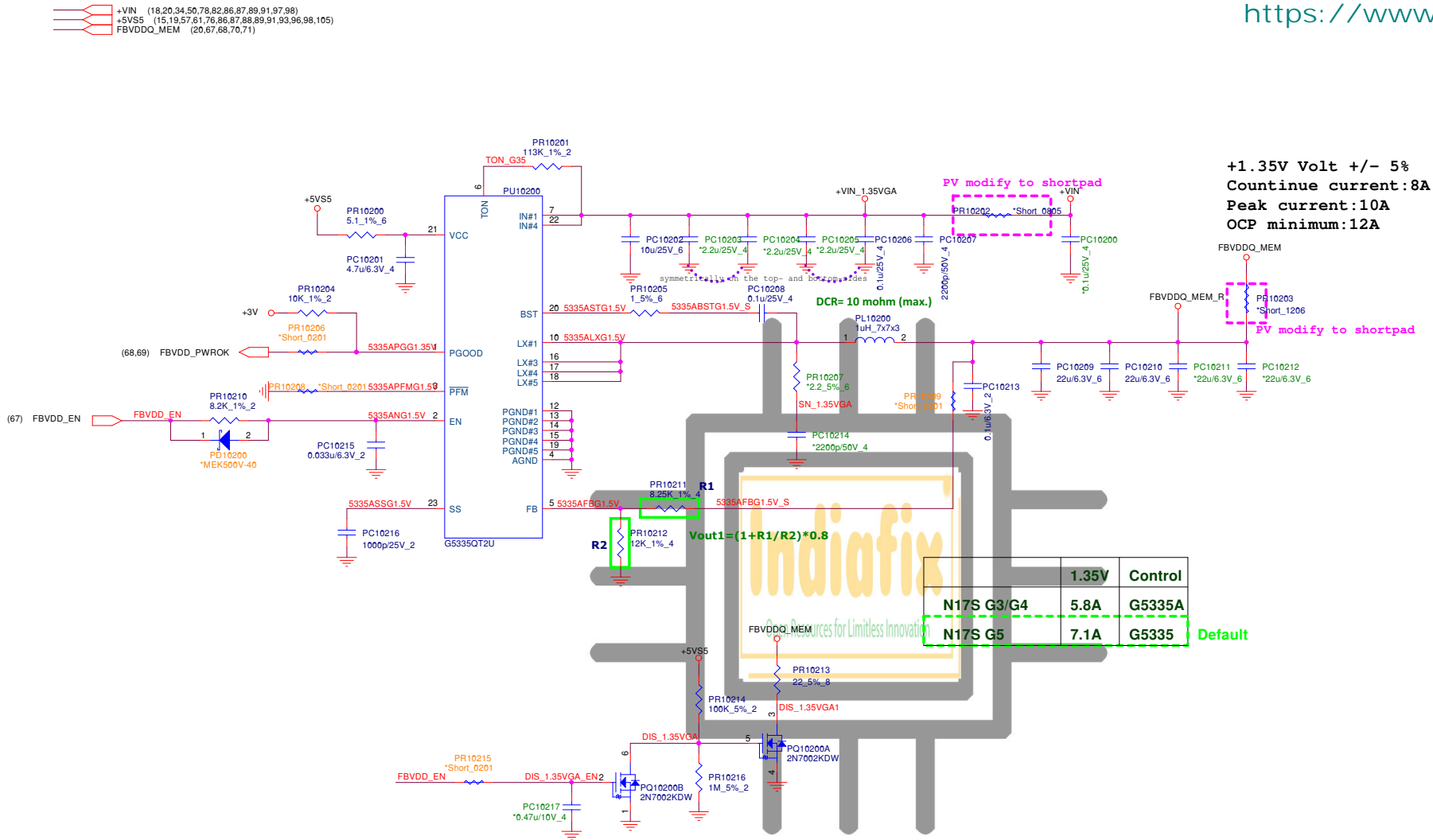
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	Size Custom	Document Number 99 -- reserve page	Rev 1A
	Date: Friday, August 21, 2020	Sheet 99 of 106	

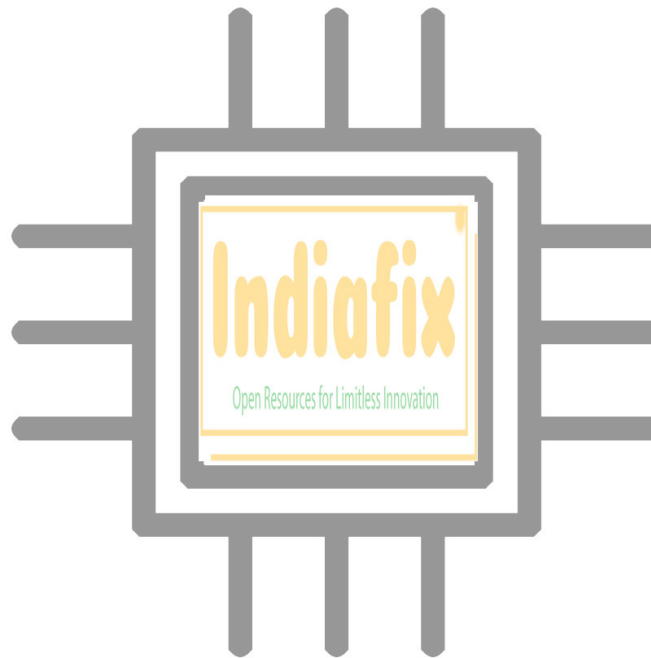



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	Date: Friday, August 21, 2020	Sheet 100 of 106	

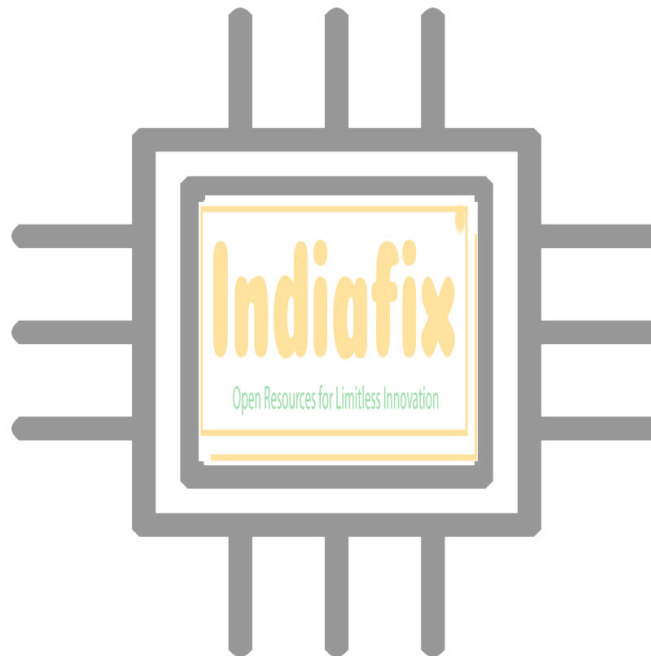



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+3VS5 (5,10,12,15,16,19,35,41,47,63,66,78,86,91,93,99)
 +5VS5 (15,19,57,61,76,86,87,88,89,91,93,96,98,102)
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 1V8_AON (20,66,67,68,69,98)
 +1.2VSUS (5,7,9,18,28,29,87)
 PEX_VDD (20,66,68)
 MAINON (41,47,87,95,96)

